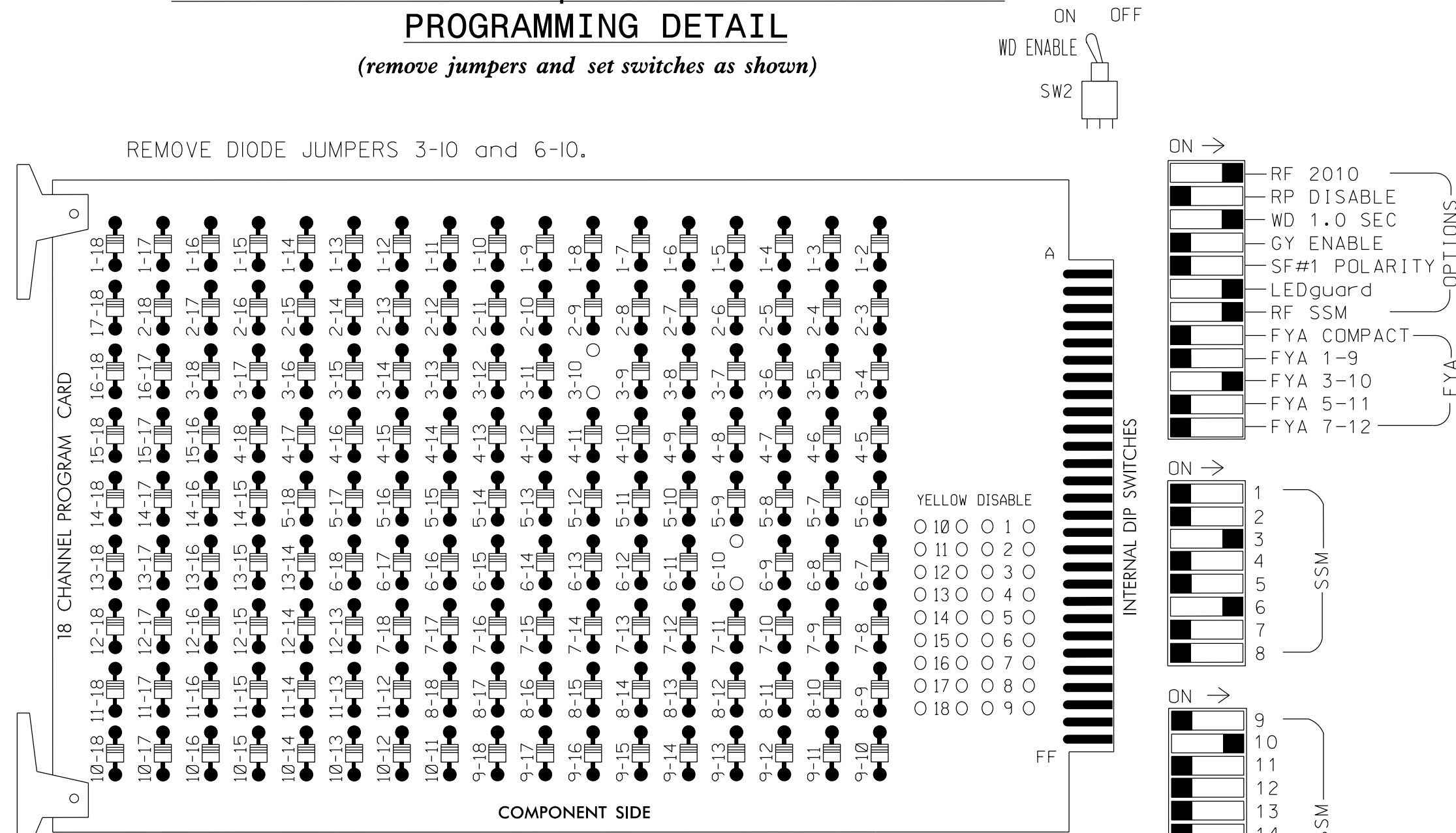


EDI MODEL 2018ECLip-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phase 6 for Variable Initial and Gap Reduction.
4. Program phase 6 for Start Up In Green.
5. Program phase 6 for Yellow Flash and overlap 2 as Wag Overlaps.
6. The cabinet and controller are part of the US 74 - Indian Trail Closed Loop System #2.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S4,S8,AUX S2.
 PHASES USED.....3,6.
 OVERLAP "A".....NOT USED
 OVERLAP "B".....3+6
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

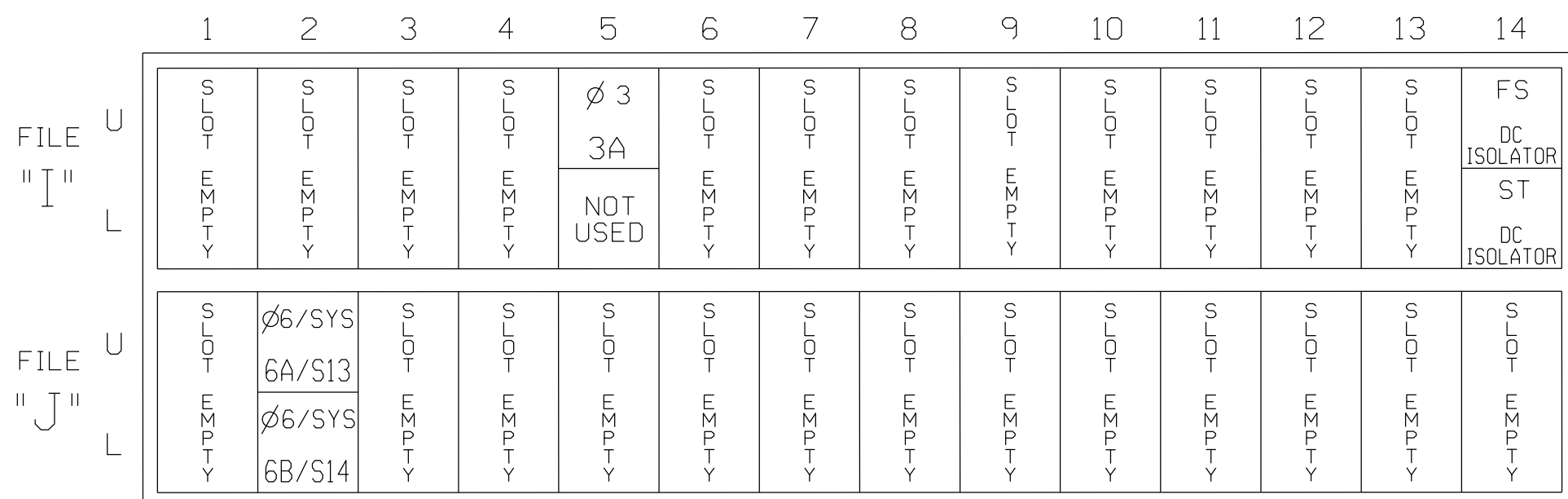
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	NU	NU	31	NU	NU	NU	61.62	NU	NU	NU	NU	31	NU	NU	NU	NU	NU
RED								134										
YELLOW				*				135										
GREEN								136										
RED ARROW														A124				
YELLOW ARROW														A125				
FLASHING YELLOW ARROW														A126				
GREEN ARROW				118														

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)



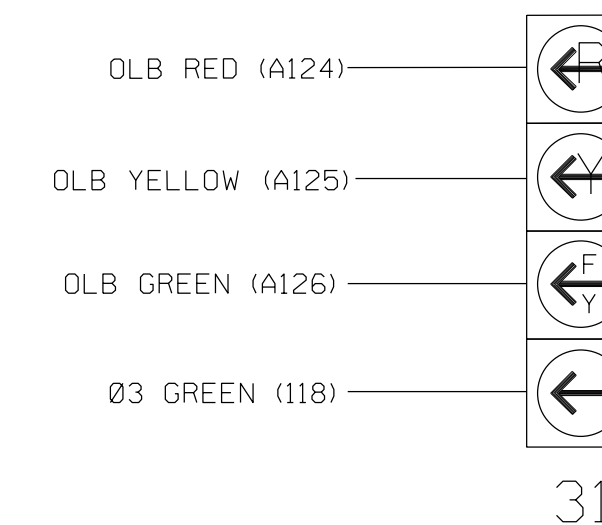
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			15
	-	I5U	58	20★	53	3	Y	Y			
6A/S13	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S14	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			

★ INPUT PAGE 2. SEE INPUT PAGE ASSIGNMENT PROGRAMMING DETAIL ON SHEET 3.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



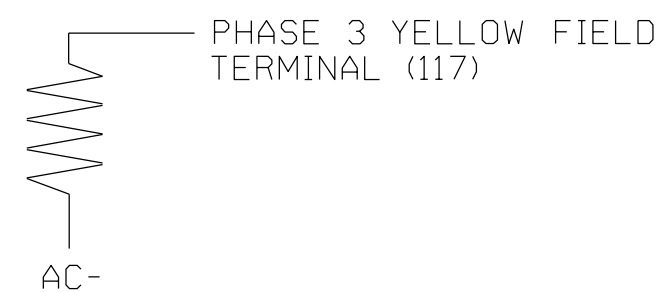
NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

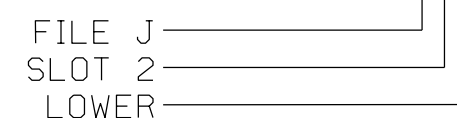
LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-2193
 DESIGNED: June 2015
 SEALED: August 30, 2016
 REVISED:

ELECTRICAL DETAIL SHEET 1 OF 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PLANS PREPARED BY:

 DRMP, INC.
 5960 FAIRVIEW ROAD, SUITE 320
 CHARLOTTE, NC 28210
 NC LICENSE NO. C-2213 • (704) 332-2289

US 74 (Andrew Jackson Hwy) WB
 at
Harris Teeter Distribution Center Eastbound U-turn

Division 10 Union County Indian Trail
 PLAN DATE: June 2015 REVIEWED BY: B Humfleet
 PREPARED BY: LM Moon REVIEWED BY:
 REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL

 Lisa M. Moon 12/12/2016
 DATE
 SIG. INVENTORY NO. 10-2193