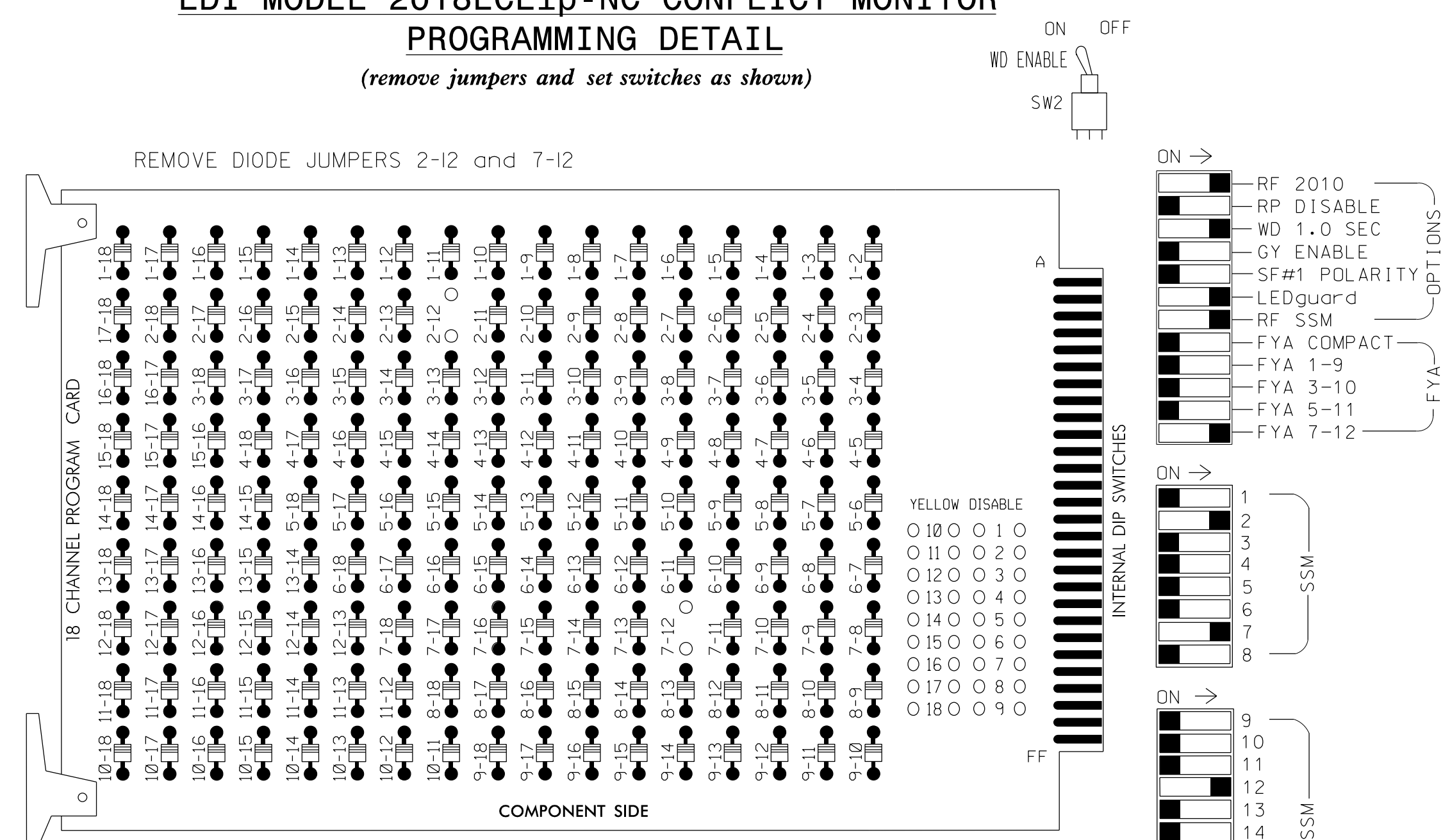


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phase 2 for Variable Initial and Gap Reduction.
- Program phase 2 for Start Up In Green.
- Program phase 2 for Yellow Flash.
- The cabinet and controller are part of the US 74 Indian Trail Closed Loop System #1.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S10,AUX S5.
 PHASES USED.....2,7.
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....2+7

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	NU	NU	NU	NU	NU	71★	72,73	NU	NU	NU	NU	NU	71★	NU
RED		128																
YELLOW		129																
GREEN		130																
RED ARROW																		A101
YELLOW ARROW										123								A102
FLASHING YELLOW ARROW																		A103
GREEN ARROW										124	124							

NU = Not Used

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅2/SYS	2A/S30	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	SYS. DET. S32	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	FS DC ISOLATOR
L	∅2/SYS	2B/S31	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	SYS. DET. S33	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	ST DC ISOLATOR
U	∅7	∅7	∅7	∅7	∅7	∅7	∅7	∅7	SYS. DET. S34	∅7	∅7	∅7	∅7	∅7
L	∅7	NOT USED	∅7	∅7	∅7	∅7	∅7	∅7	NOT USED	∅7	∅7	∅7	∅7	∅7

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

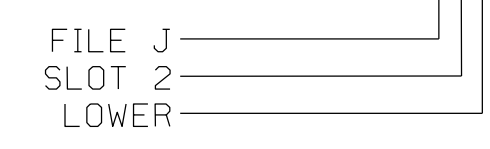
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S30	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S31	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
*S32	TB6-9,10	I9U	60	22	11	SYS					
*S33	TB6-11,12	I9L	62	24	13	SYS					
7A	TB5-9,10	J6U	42	4★	8	7	Y	Y			15
7B	TB5-11,12	J6L	46	8	18	7	Y	Y			10
7C	TB7-1,2	J7U	66	28	38	7	Y	Y			10
*S34	TB7-9,10	J9U	59	21	15	SYS					

* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

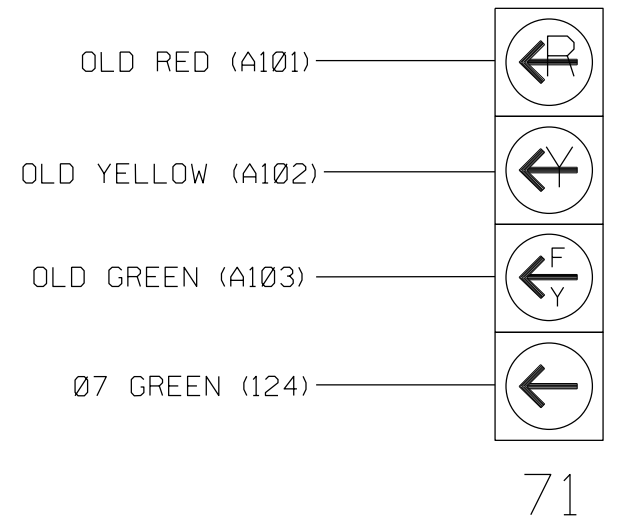
★ INPUT PAGE 2. SEE INPUT PAGE ASSIGNMENT PROGRAMMING DETAIL ON SHEET 3.

INPUT FILE POSITION LEGEND: J2L



4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-2188
 DESIGNED: June 2015
 SEALED: August 30, 2016
 REVISED:

ELECTRICAL DETAIL SHEET 1 OF 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

DRMP, INC.
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 CHARLOTTE, NC 28210
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US 74 (Independence Blvd) EB at SR 1367 (Unionville-Indian Trail Road)
 Division 10 Union County Indian Trail
 PLAN DATE: June 2015 REVIEWED BY: LM Moon
 PREPARED BY: K Smith REVIEWED BY: B Humfleet

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEERS
 SEAL 022516
 LISA M. MOON
 12/12/2016
 SIG. INVENTORY NO. 10-2188