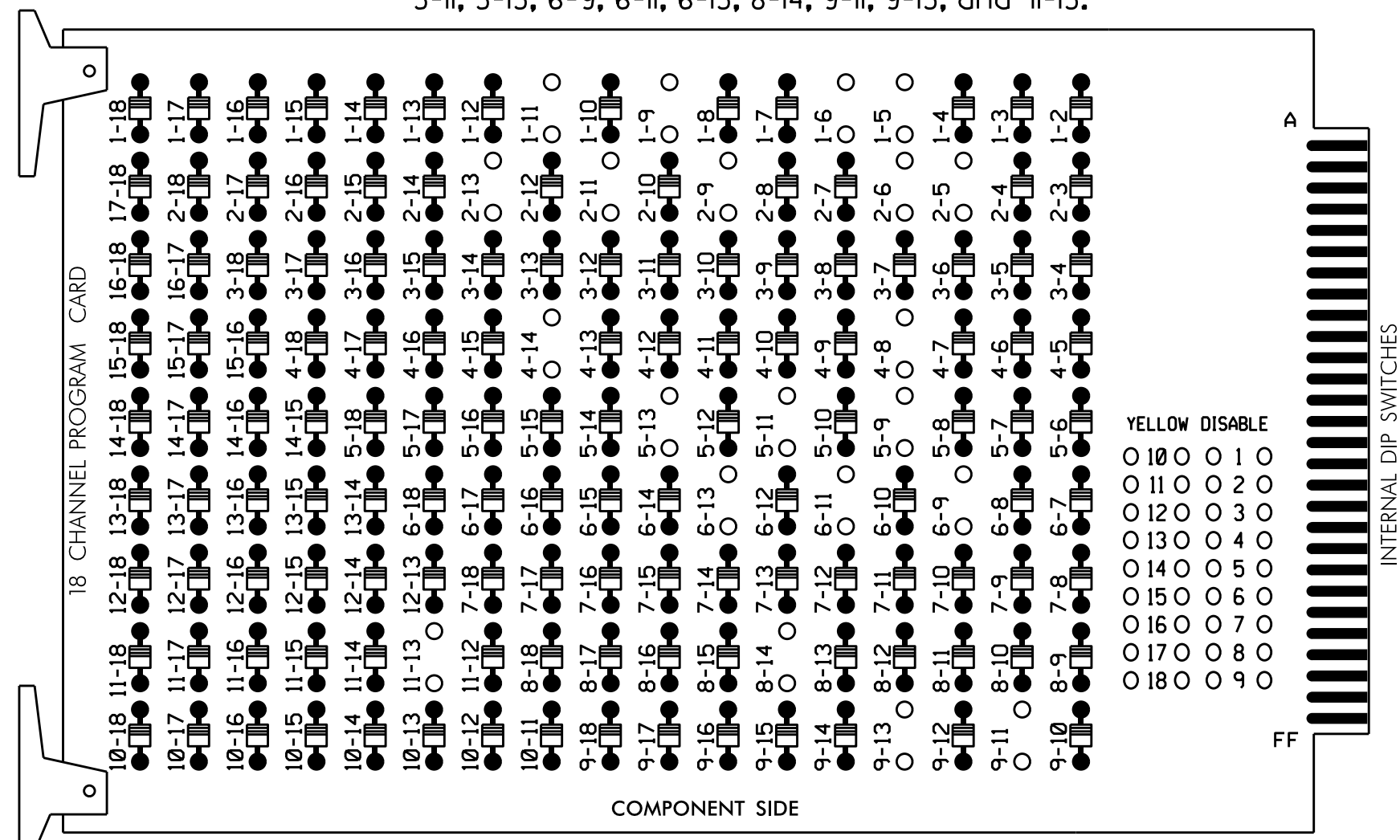


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 2-13, 4-8, 4-14, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 8-14, 9-11, 9-13, and 11-13.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 4 and 8 for Dual Entry.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 4 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S11,  
 AUX S1,AUX S4  
 PHASES USED.....1,2,2PED,4,4PED,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

PROJECT REFERENCE NO.	SHEET NO.
U-4715B	8ig. 206.1

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	21,22	P21, P22	NU	41,42	P41, P42	51	61,62	NU	NU	81,82	NU	11	NU	51	NU	NU
RED	*	128			101				134			107						
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114		
YELLOW ARROW	126												A122			A115		
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW	127	127					133											
Hand				113		104												
Walking				115		106												

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1 1A	∅ 1 1B	∅ 2 2A	∅ 4 4A	∅ 4 4C	∅ 4 4B	∅ 4 4C	∅ 4 4C	SYS. DET. S1	∅ 4 4C	∅ 4 4C	∅ 4 4C	∅ 4 4C	∅ 4 4C
L	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
U	∅ 5 5A	∅ 6 6A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A	∅ 8 8A
L	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

INPUT FILE CONNECTION & PROGRAMMING CHART

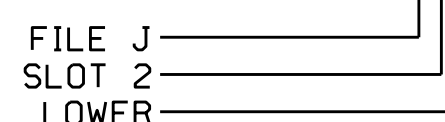
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			5
4C	TB6-1,2	I7U	65	27	34	4	Y	Y			15
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
* S1	TB6-9,10	I9U	60	22	11	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					

NOTE:  
 INSTALL DC ISOLATOR IN INPUT FILE SLOT I12.

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.

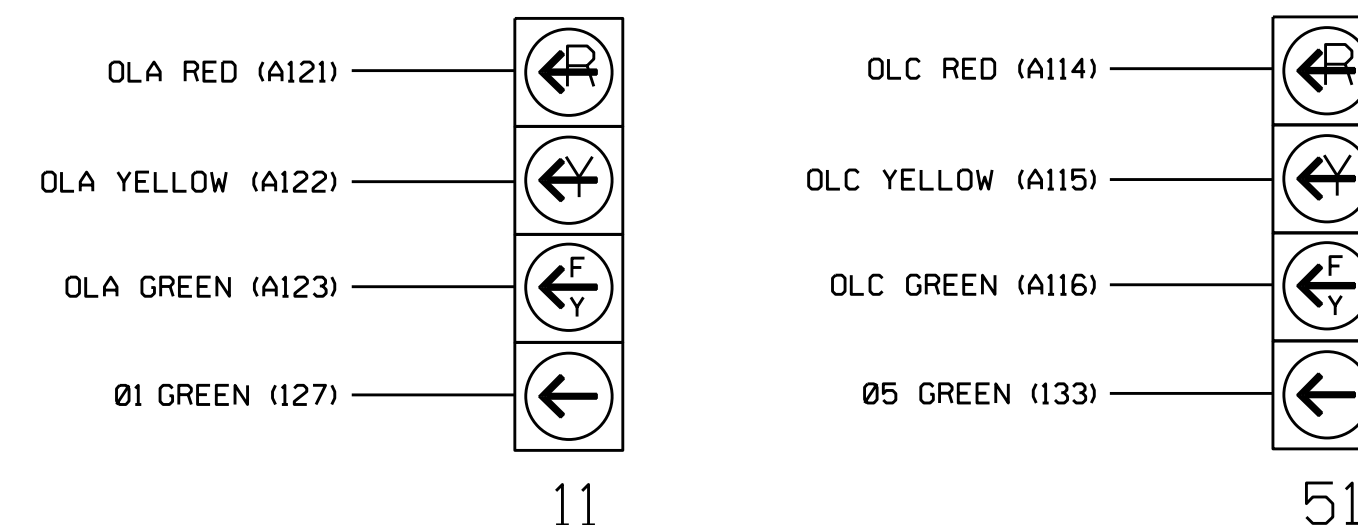
\* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

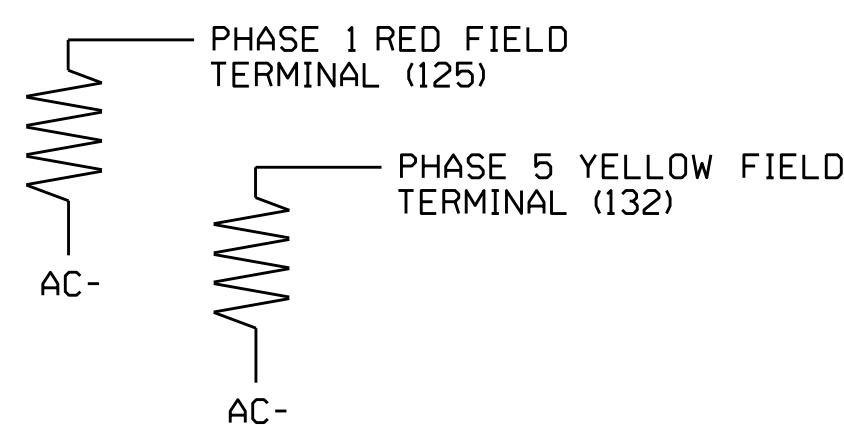
The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-1215  
 DESIGNED: May 2016  
 SEALED: 8/10/2016  
 REVISED: N/A

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  
 TRANSPORTATION MOBILITY AND SAFETY CONSULTANTS  
 750 N. Greenfield Pkwy, Garner, NC 27529

NC 112 (Sand Hill Road) at SR 3437 (Lake Drive)/ Colbond Driveway

Division 13 Buncombe County Enka

PLAN DATE: July 2016 REVIEWED BY: BAS

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by:  
 Keith M. Mims 9/7/2016

SIG. INVENTORY NO. 13-1215

11-10-2016 07:18  
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 sarmstrong