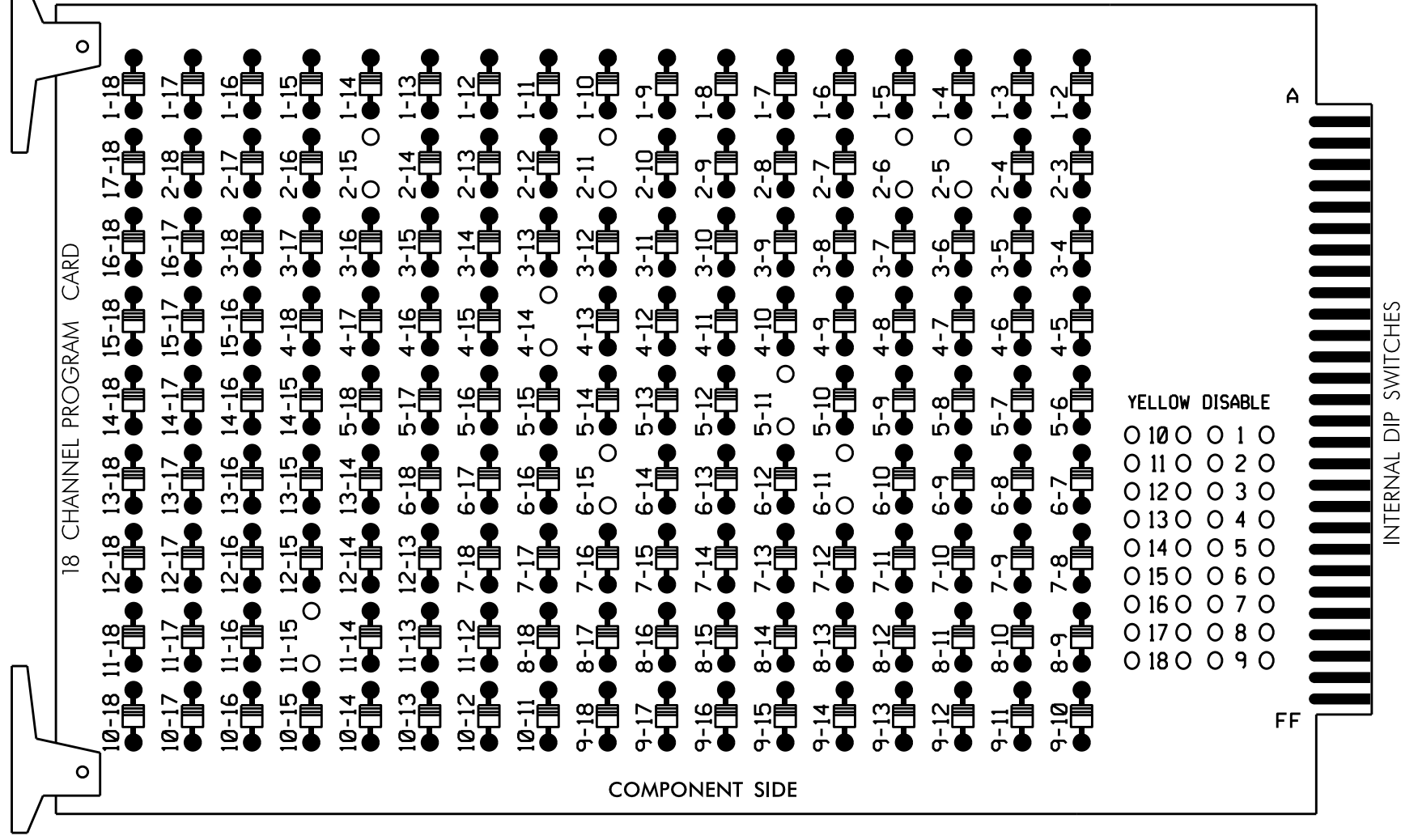


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

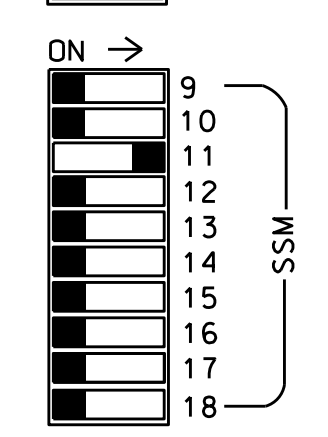
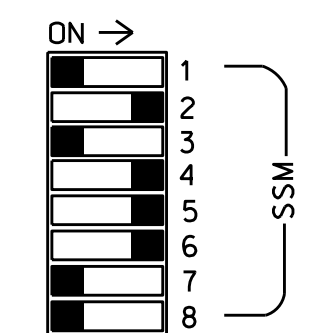
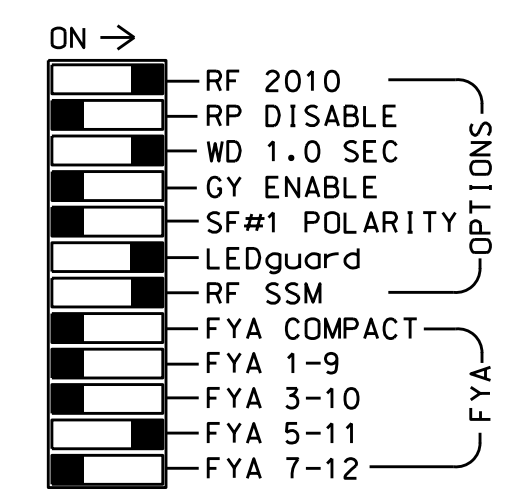
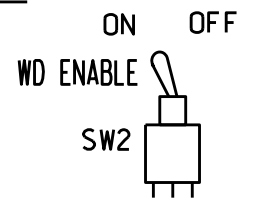
REMOVE DIODE JUMPERS 2-5, 2-6, 2-11, 2-15, 4-14, 5-11, 6-11, 6-15 and 11-15.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 4 and 6 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash.
7. The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE LOAD SWITCHES USED.....S2,S5,S6,S7,S8,S9,AUX S4
 PHASES USED.....2,4,4 PED,5,6,6 PED
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

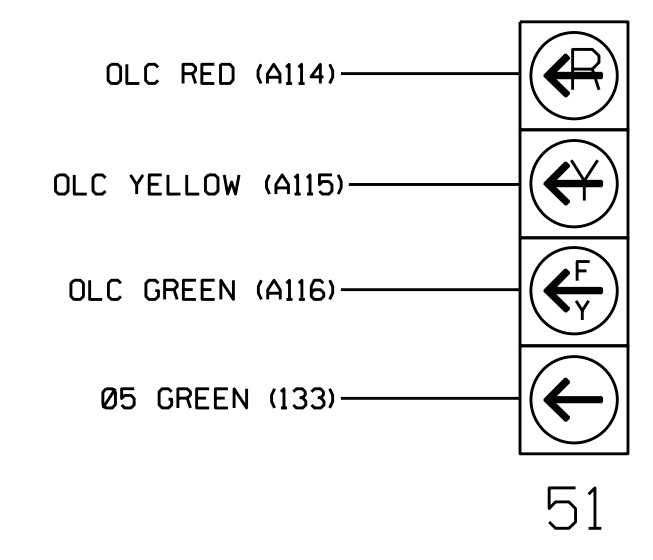
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	62	P41, P42	42	51	61,62	P61, P62	NU	NU	NU	NU	51	NU	NU	
RED	128			101			*		134										
YELLOW	129			102					135										
GREEN	130			103					136										
RED ARROW																		A114	
YELLOW ARROW						102		132											A115
FLASHING YELLOW ARROW																			A116
GREEN ARROW						103		133	133										
Hand icon							104			119									
Person icon							106			121									

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal head as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅2/SYS	∅2/SYS	∅2/SYS	∅4	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS
I	2A/S1	2A/S1	2B/S2	4A	2B/S2	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
L	∅2/SYS	∅2/SYS	∅2/SYS	NOT USED	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS
U	∅5	∅5	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS
J	5A	5B	6A/S3	6B/S4	6B/S4	6B/S4	6B/S4	6B/S4	6B/S4	6B/S4	6B/S4	6B/S4	6B/S4	6B/S4
L	NOT USED	NOT USED	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

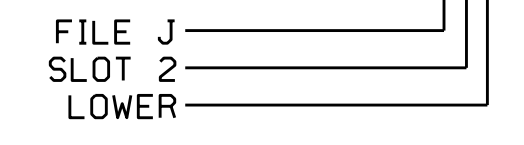
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S1	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S2	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
5A ¹	TB3-1,2	J1U	55	17	5	5	Y	Y			3
	-	I4U	47	9	22	2	Y	Y	Y		15
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			3
6A/S3	TB3-9,10	J3U	64	26	36	6/SYS	Y	Y			15
6B/S4	TB3-11,12	J3L	77	39	46	6/SYS	Y	Y			
PED PUSH BUTTONS											
P41,P42	TB8-5,6	I12L	69	31							
P61,P62	TB8-7,9	I13U	68	30							

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

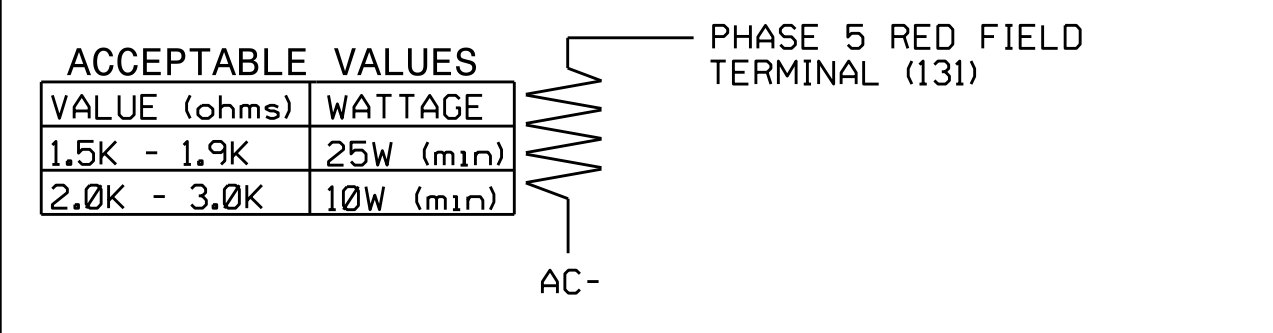
¹Add jumper from J1-W to I4-W. on rear of input file.

INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)



NOTE: The purpose of this resistor is to load the channel red monitor input in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-1195
 DESIGNED: April 2016
 SEALED: 8/10/2016
 REVISED:

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
 TRANSPORTATION MOBILITY AND SAFETY DIVISION
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
 Signal Management Section
 750 N. Greenfield Pkwy, Garner, NC 27529

US 25 (Hendersonville Road) at Racquet Club Road

Division 13 Buncombe County Asheville

PLAN DATE: June 2016 REVIEWED BY: BAS

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

Seal: NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 030530 JACUARY M. LITTLE

Designed by: *Carlynn M. Little* 8/16/2016

SIG. INVENTORY NO. 13-1195

16-AUG-2016 07:22 S:\TCS\ASIS\13-1195_Sig\EDI Model\2018ECLIP-NC Conflict Monitor.dgn