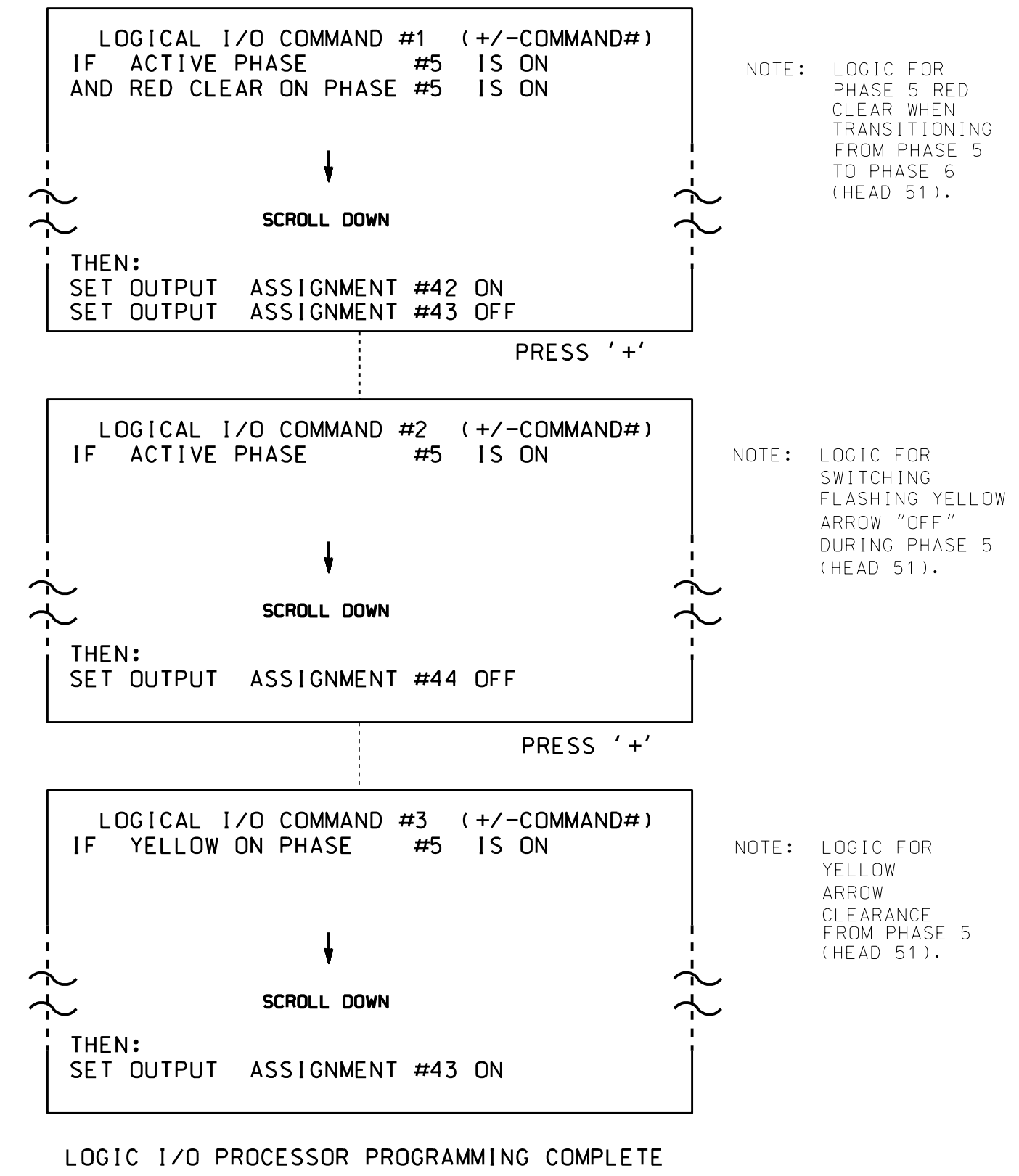


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



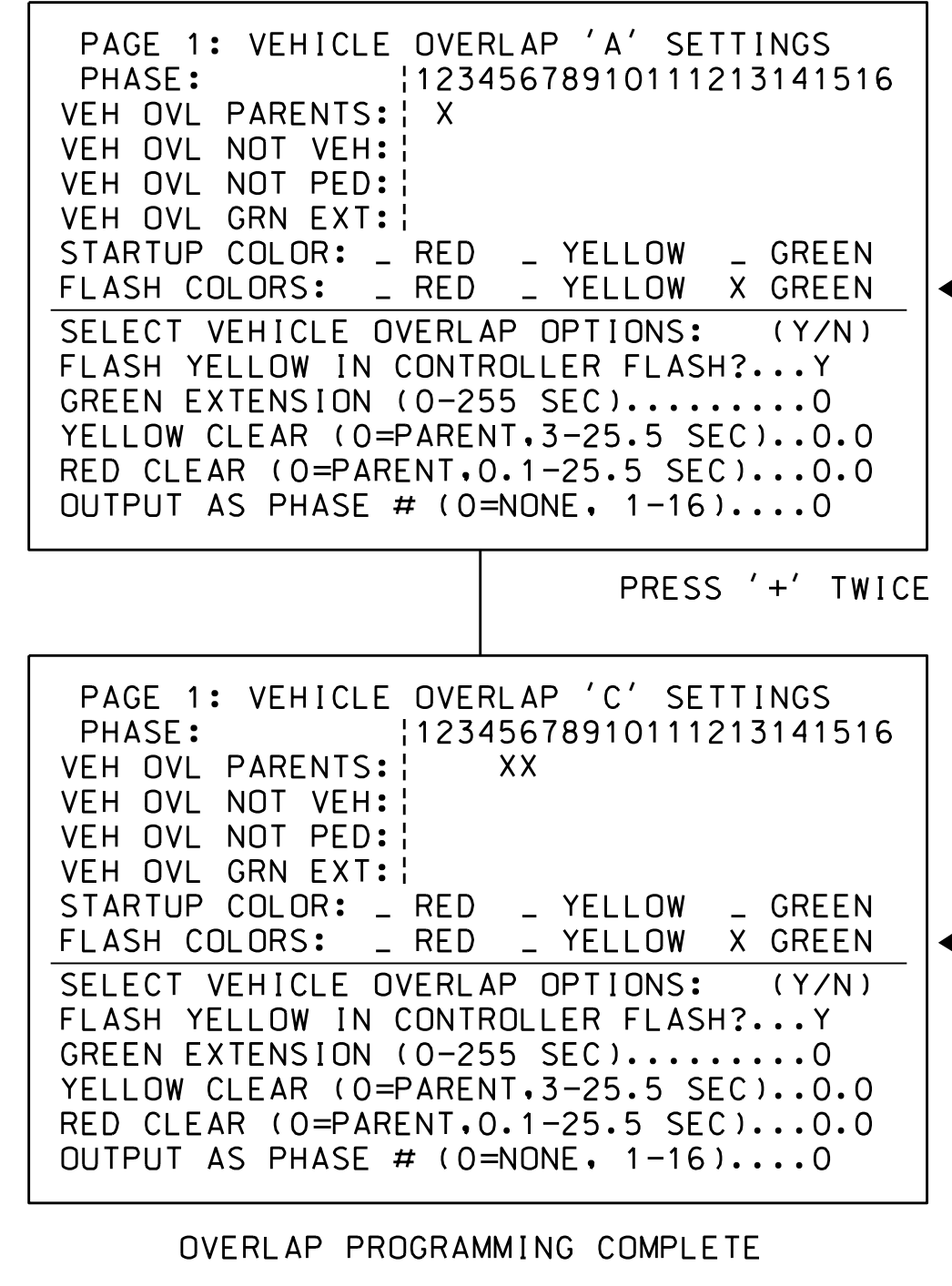
OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
 OUTPUT 43 = Overlap C Yellow
 OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

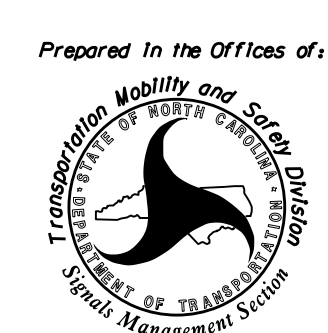
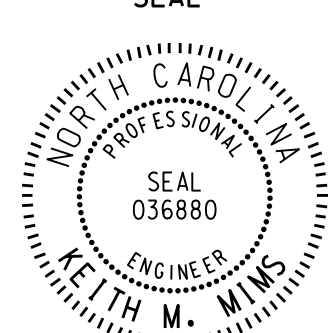
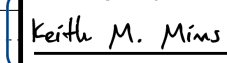
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 13-0854
 DESIGNED: January 2016
 SEALED: 8/16/2016
 REVISED: N/A

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	DETAILS FOR: NC 191 (Brevard Road) at SR 3479 (Pole Creasman Road) / Carolina Metals Entrance Division 13 Buncombe County Asheville PLAN DATE: May 2016 REVIEWED BY: BAS PREPARED BY: S. Armstrong REVIEWED BY:	SEAL  KEITH M. MINS ENGINEER
REVISIONS INIT. DATE		DocuSigned by:  9/12/2016 DATE
		SIG. INVENTORY NO. 13-0854

17-AUG-2016 11:55
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