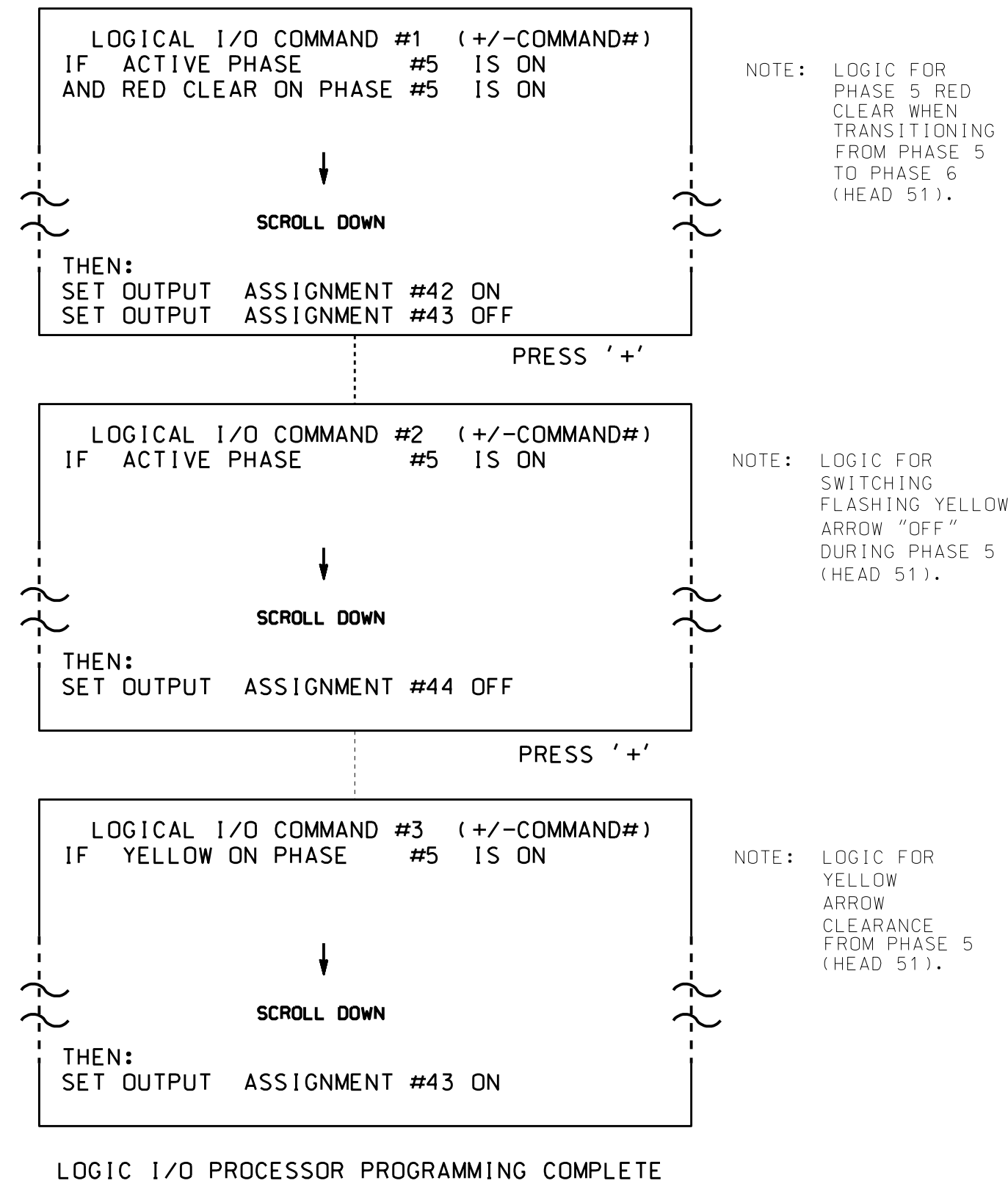


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



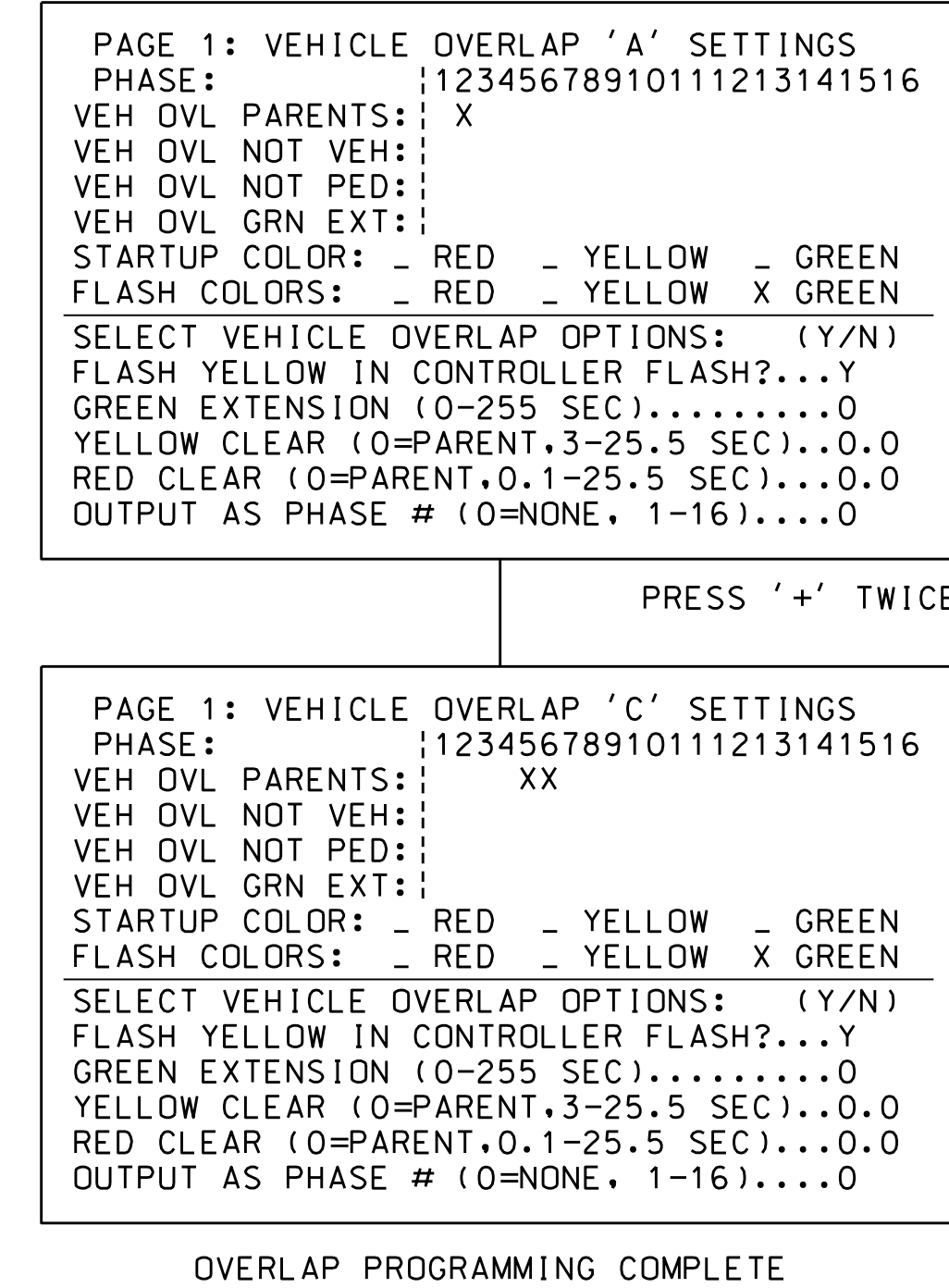
OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0839
DESIGNED: December 2015
SEALED: 8/10/2016
REVISED: N/A

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

<p>Electrical AND PROGRAMMING DETAILS FOR:</p> <p style="font-size: small;">Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>NC 191 (Brevard Road) at SR 3484 (Bent Creek Road)/ Church Entrance</p> <p>Division 13 Buncombe County Asheville</p> <p>PLAN DATE: May 2016 REVIEWED BY: BAS</p> <p>PREPARED BY: S. Armstrong REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>	REVISIONS	INIT.	DATE				<p>SEAL</p> <p>DocuSigned by: Keith M. Mins 9/12/2016</p> <p>SIG. INVENTORY NO. 13-0839</p>
REVISIONS	INIT.	DATE						

I:\13-0839-2016-0839-17-SIGNALS\SIGNALS\Sig\13-0839-2016-0839-sm.ele.xxx.dgn
S:\13-0839-2016-0839-17-SIGNALS\SIGNALS\Sig\13-0839-2016-0839-sm.ele.xxx.dgn
S:\13-0839-2016-0839-17-SIGNALS\SIGNALS\Sig\13-0839-2016-0839-sm.ele.xxx.dgn