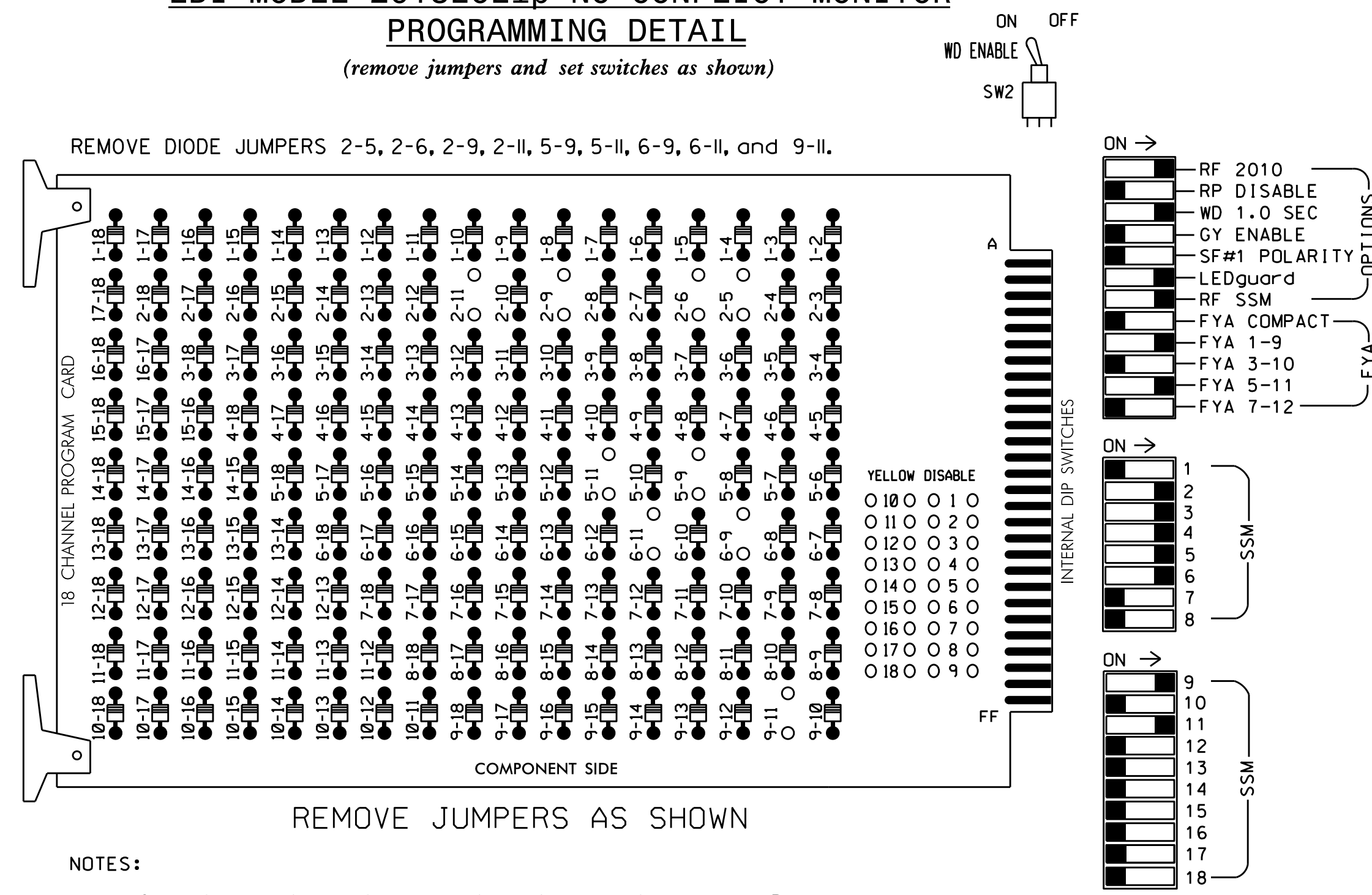


# EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

## NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Asheville Signal System.

## EQUIPMENT INFORMATION

CONTROLLER.....2070E  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S2,S4,S5,S7,S8,AUX S1,AUX S4  
 PHASES USED.....2,3,4,5,6  
 OVERLAP "A".....2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

## SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	21,22	NU	31	32	41	42	NU	42	51	62,63	NU	NU	NU	61	NU	NU	51	NU
RED		128		116	116	101	101		*		134								
YELLOW		129		117	117	102	102				135								
GREEN		130		118	118	103	103				136								
RED ARROW																			
YELLOW ARROW																			
FLASHING YELLOW ARROW																			
GREEN ARROW																			

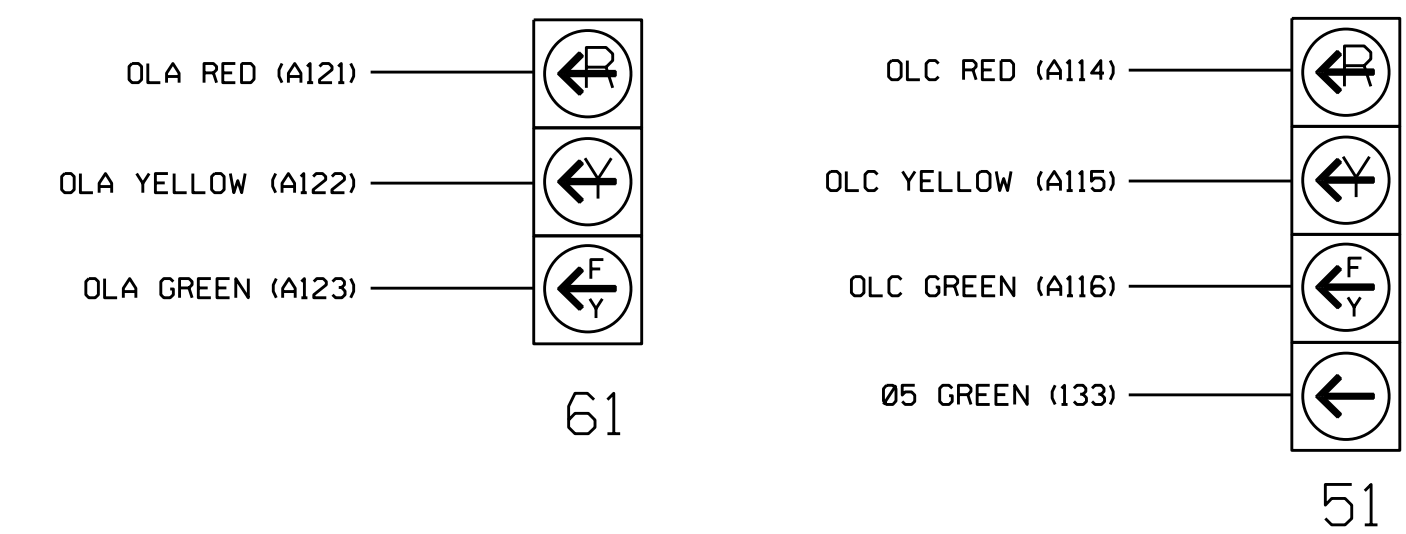
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

## FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

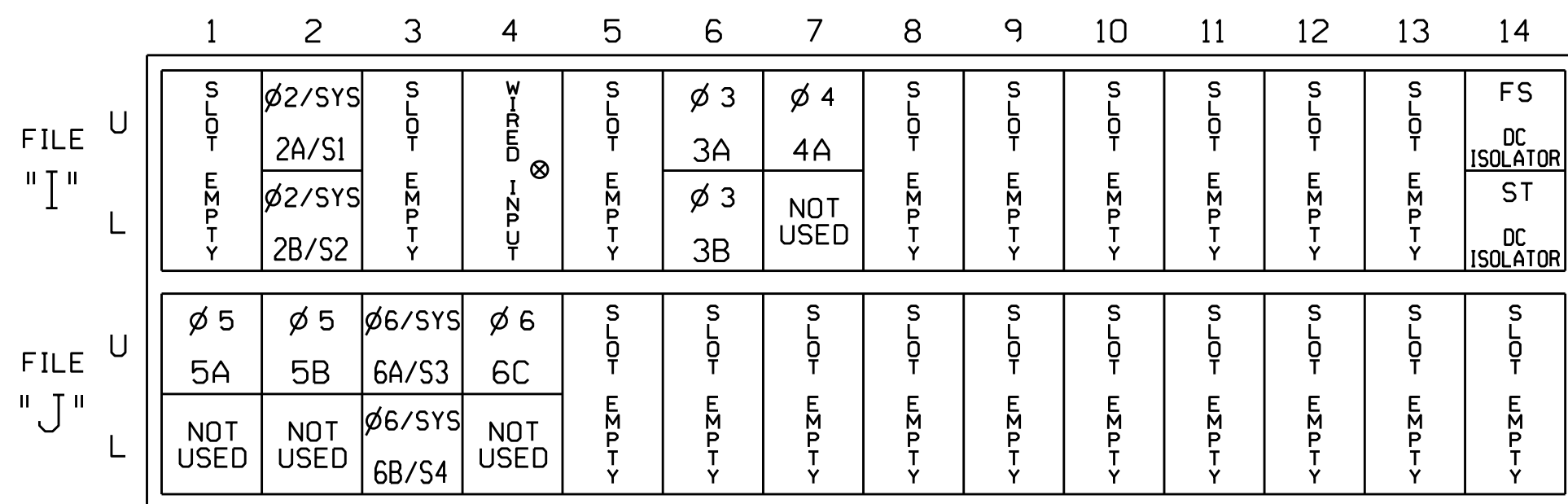


**NOTE**

The sequence display for signal head 51 requires special logic programming. See sheet 2 for programming instructions.

## INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

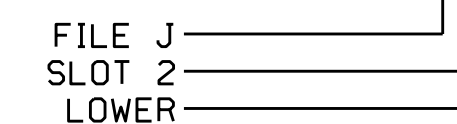
⊗ Wired Input - Do not populate slot with detector card

## INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S1	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S2	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
3A	TB4-9,10	I6U	41	3	4	3	Y	Y			3
3B	TB4-11,12	I6L	45	7	14	3	Y	Y			10
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			3
5A <sup>1</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A/S3	TB3-9,10	J3U	64	26	36	6/SYS	Y	Y			
6B/S4	TB3-11,12	J3L	77	39	46	6/SYS	Y	Y			
6C	TB5-1,2	J4U	48	10	26	6	Y	Y	Y		3

<sup>1</sup>Add jumper from J1-W to I4-W, on rear of input file.

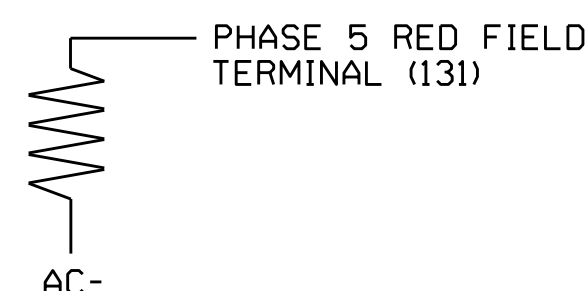
### INPUT FILE POSITION LEGEND: J2L



## LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical and Programming Details for: **NC 280 (Airport Road) at SR 3522 (Old Shoals Road)/ Arden 7th Day Adventist Ent.**

Division 13 Buncombe County Arden

PLAN DATE: April 2016 REVIEWED BY: BAS

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS: \_\_\_\_\_ INIT. DATE

Seal: **KEITH M. MIMS**, PROFESSIONAL ENGINEER, No. 036880

DocuSigned by: **Keith M. Mims**, 8/30/2016

SIG. INVENTORY NO. 13-0822

09-AUG-2016 13:57 S:\IT\ASIS\13-0822\Sig\Monitor\mstronm\30822\_sml.elec.xxx.dgn sarmstrong