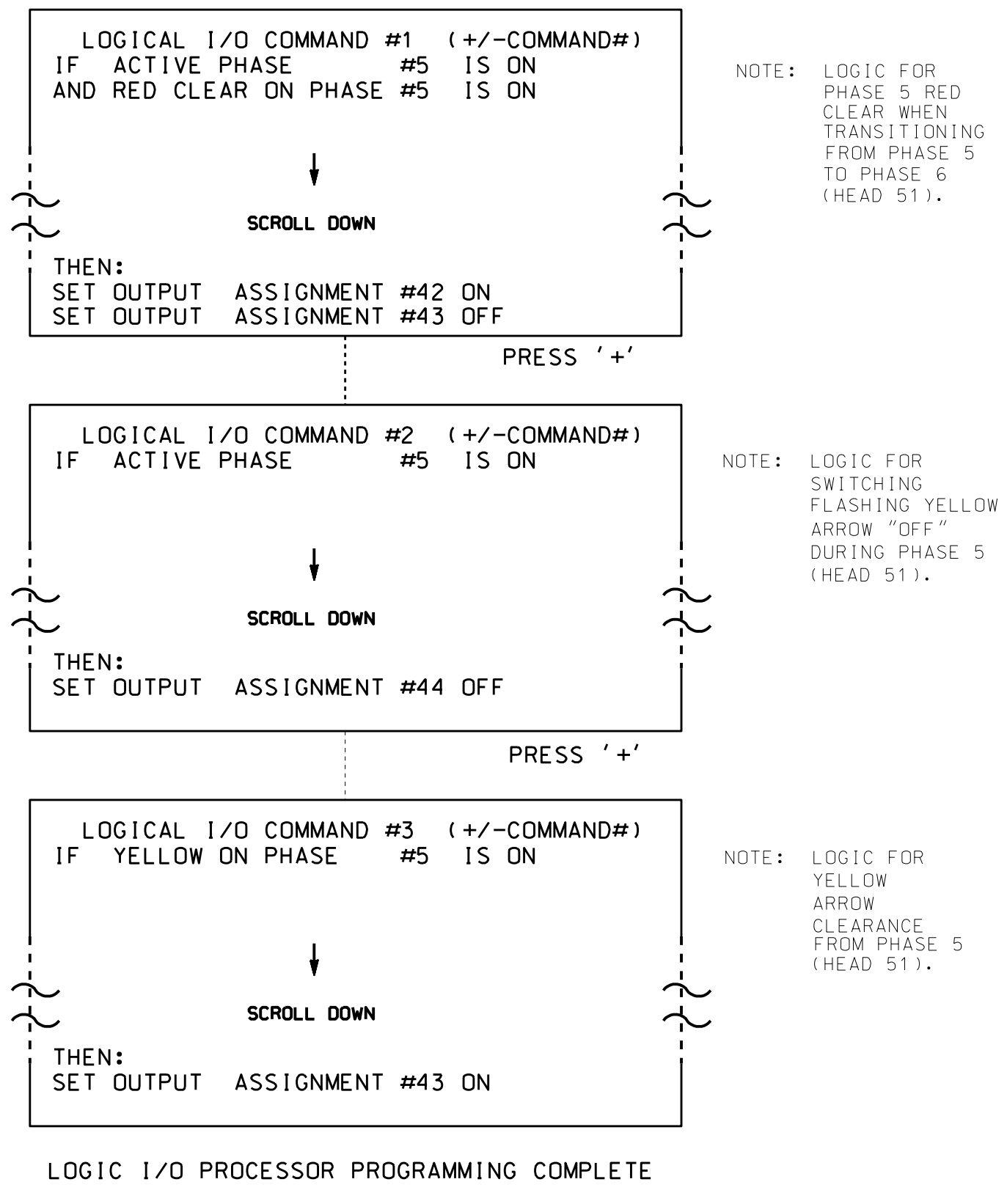


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



| <u>OUTPUT REFERENCE SCHEDULE</u> | |
|----------------------------------|------------------|
| OUTPUT 42 = | Overlap C Red |
| OUTPUT 43 = | Overlap C Yellow |
| OUTPUT 44 = | Overlap C Green |

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS:  XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED _ YELLOW _ GREEN
FLASH COLORS:  _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

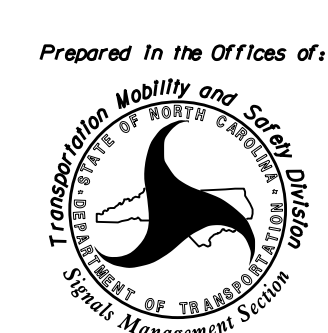
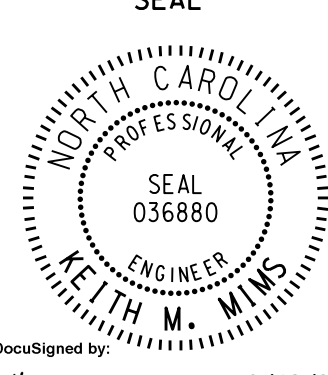
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0710
DESIGNED: June 2016
SEALED: 09-01-16
REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

| | | | |
|--|--|---|---|
| ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529 | NC 146 (Long Shoals Road) at West School Road | | SEAL  |
| | Division 13 PLAN DATE: August 2016 PREPARED BY: James Peterson | Buncombe County REVIEWED BY: REVIEWED BY: | Asheville DATE: 9/13/2016 DATE: |

07-SEP-2016 13:59 C:\IT\SS\ITS-Sigma\work\poups\sig_MarkPeterson\30710_sme.ele_xxx.dgn J.peterson