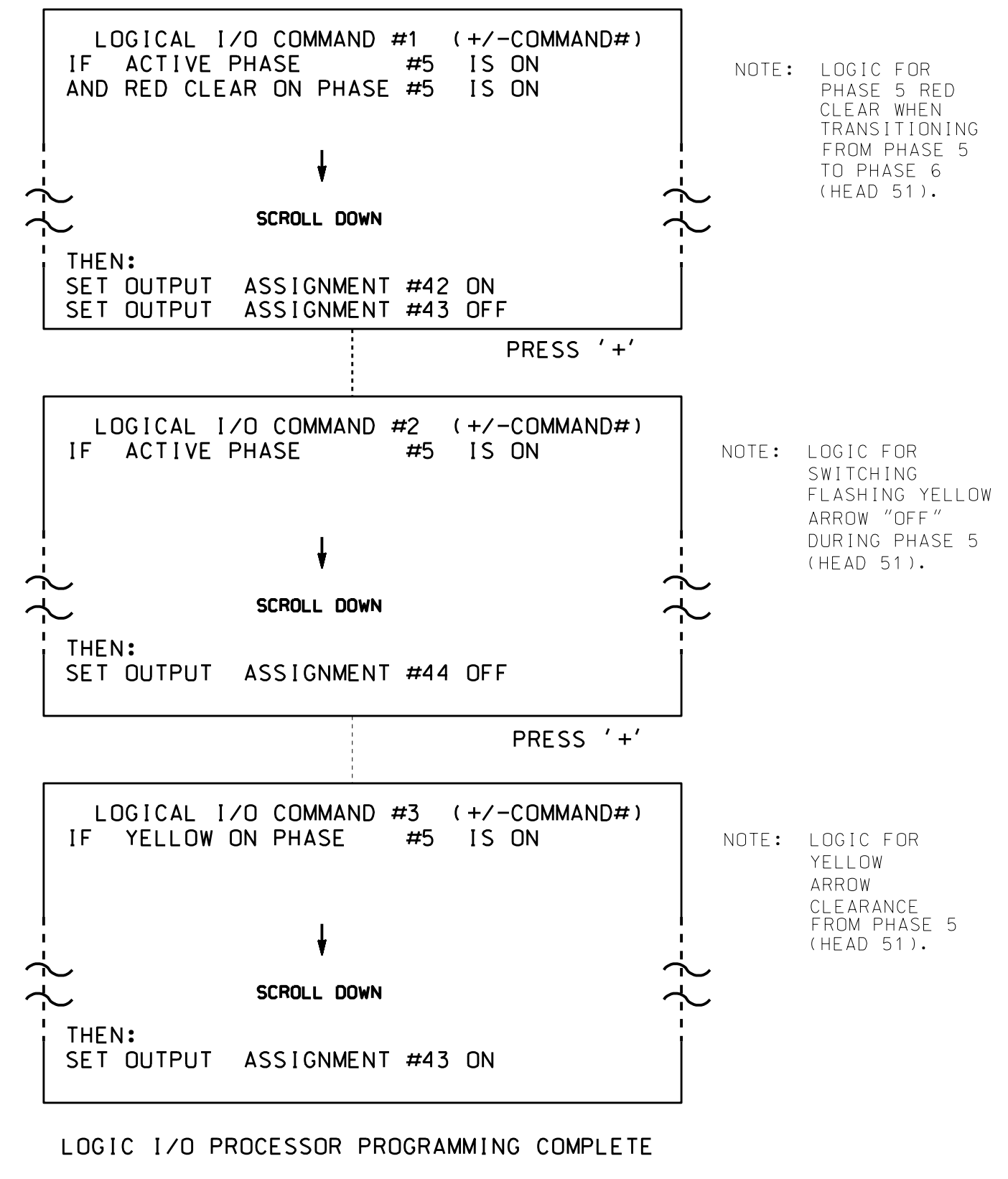


## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

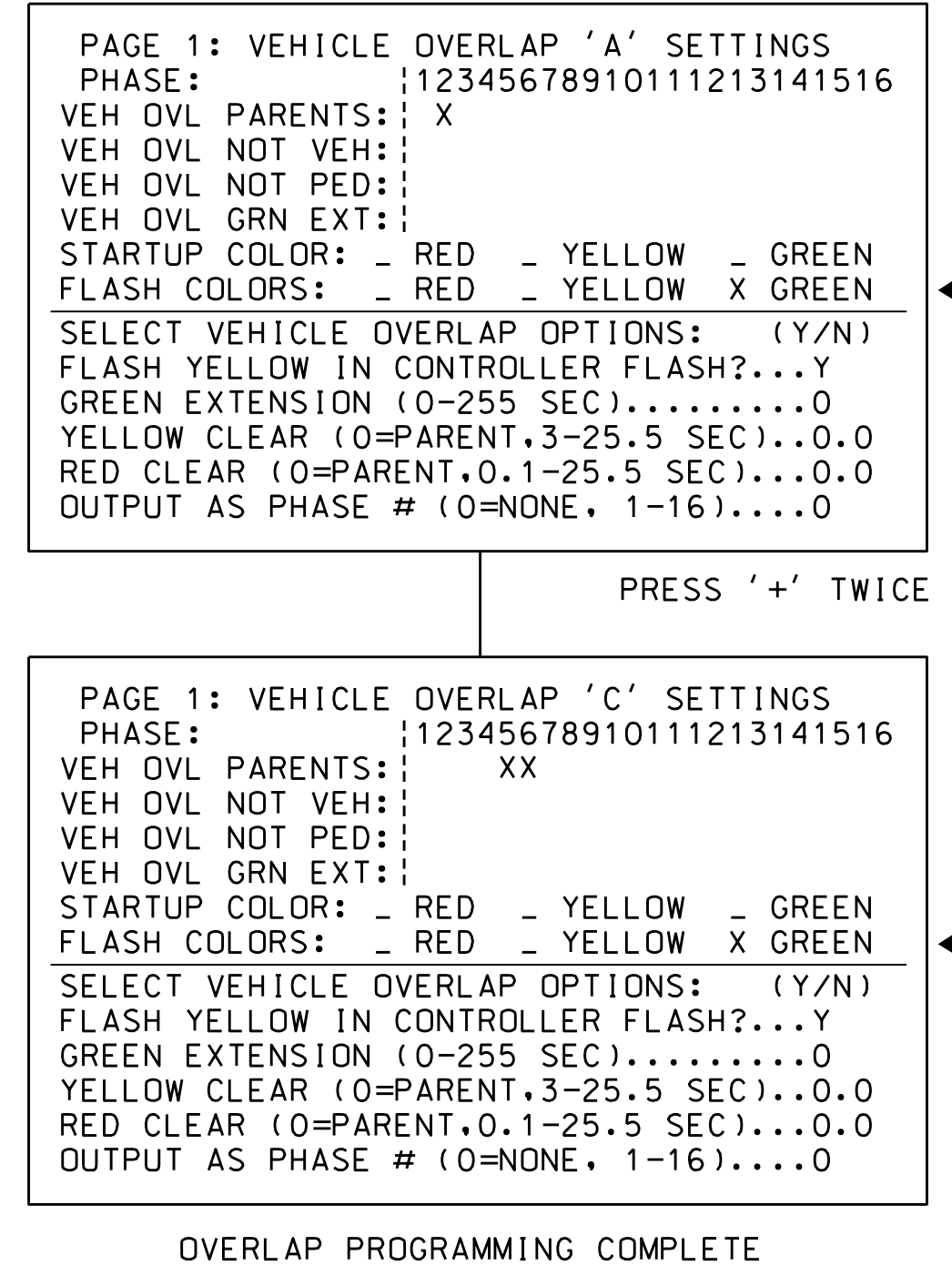


OUTPUT REFERENCE SCHEDULE
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 13-0680  
DESIGNED: February 2016  
SEALED: 8-16-16  
REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 2

DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED

<p style="font-size: x-small;">ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="font-size: x-small;">Prepared In the Offices of:</p> <p style="font-size: x-small;">750 N. Grantfield Pkwy, Garner, NC 27529</p>	<p style="font-size: large;">US 25A (Sweeten Creek Road) at Gerber Road / Factory Entrance</p> <p style="font-size: x-small;">Division 13    Buncombe County    Asheville</p> <p>PLAN DATE: July 2016    REVIEWED BY: DTJ</p> <p>PREPARED BY: James Peterson    REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse; font-size: x-small;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> </tbody> </table>	REVISIONS	INIT.	DATE										<p style="font-size: x-small;">SEAL</p> <p style="font-size: x-small;">Keith M. Mims    9/13/2016</p> <p style="font-size: x-small;">DATE</p> <p style="font-size: x-small;">SIG. INVENTORY NO. 13-0680</p>
REVISIONS	INIT.	DATE												

31-100-2016 14239  
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J. Peterson