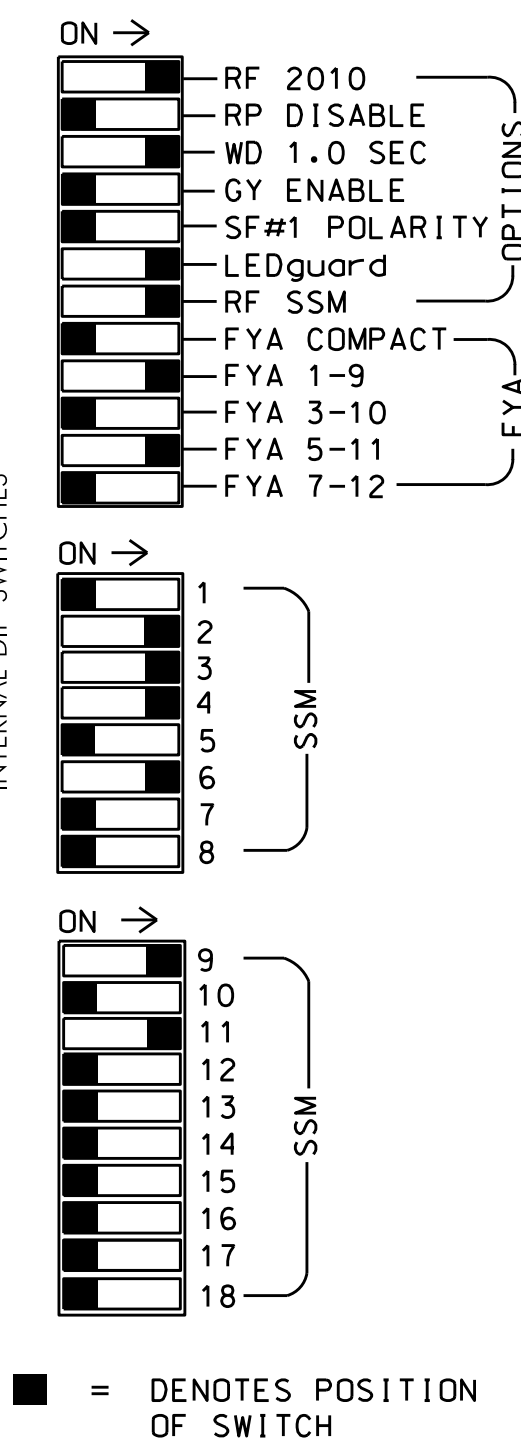
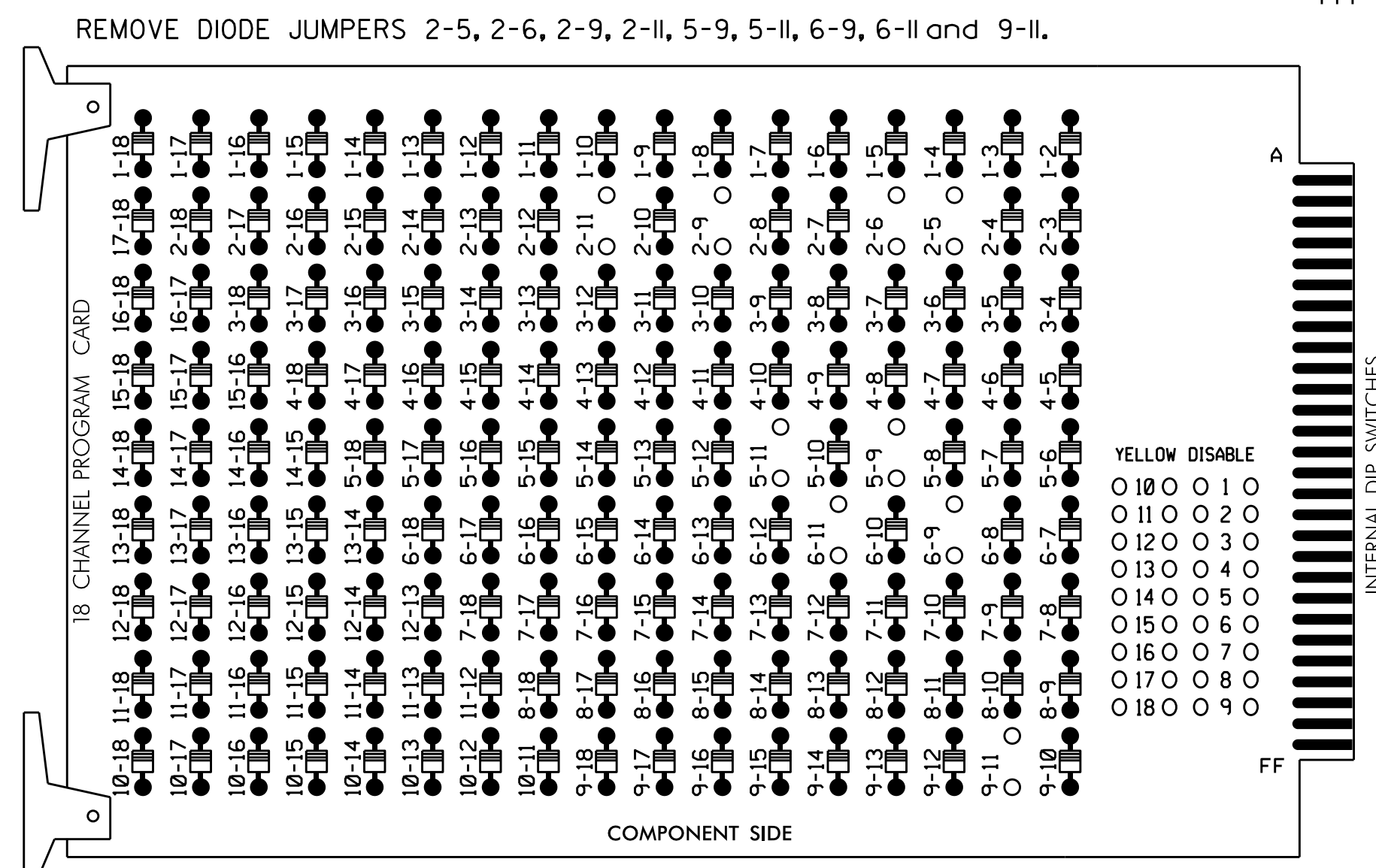


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Asheville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S2,S4,S5,S7,S8,AUX S1,AUX S4.  
 PHASES USED.....2,3,4,5,6  
 OVERLAP "A".....2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	31	32	41	42	NU	51*	62,63	NU	NU	NU	61*	NU	NU	51*	NU
RED		128		116	116	101	101				134							
YELLOW		129		117	117	102	102		*	135								
GREEN		130		118	118	103	103			136								
RED ARROW														A121			A114	
YELLOW ARROW														A122			A115	
FLASHING YELLOW ARROW														A123			A116	
GREEN ARROW						118	103			133								

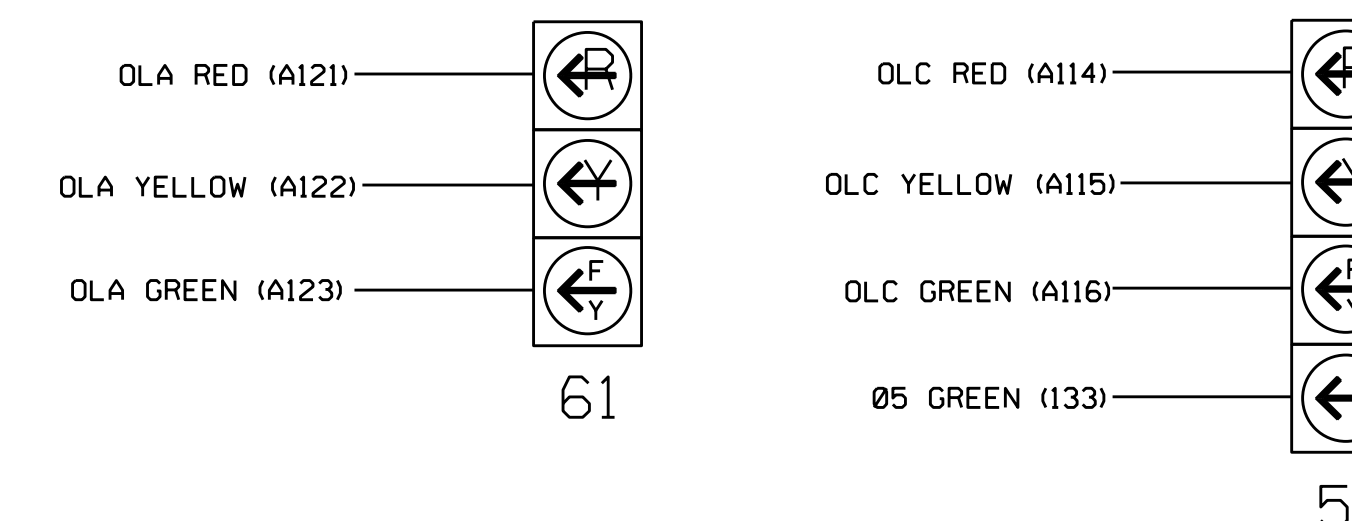
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

\* See pictorial of head wiring in detail below.

**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)

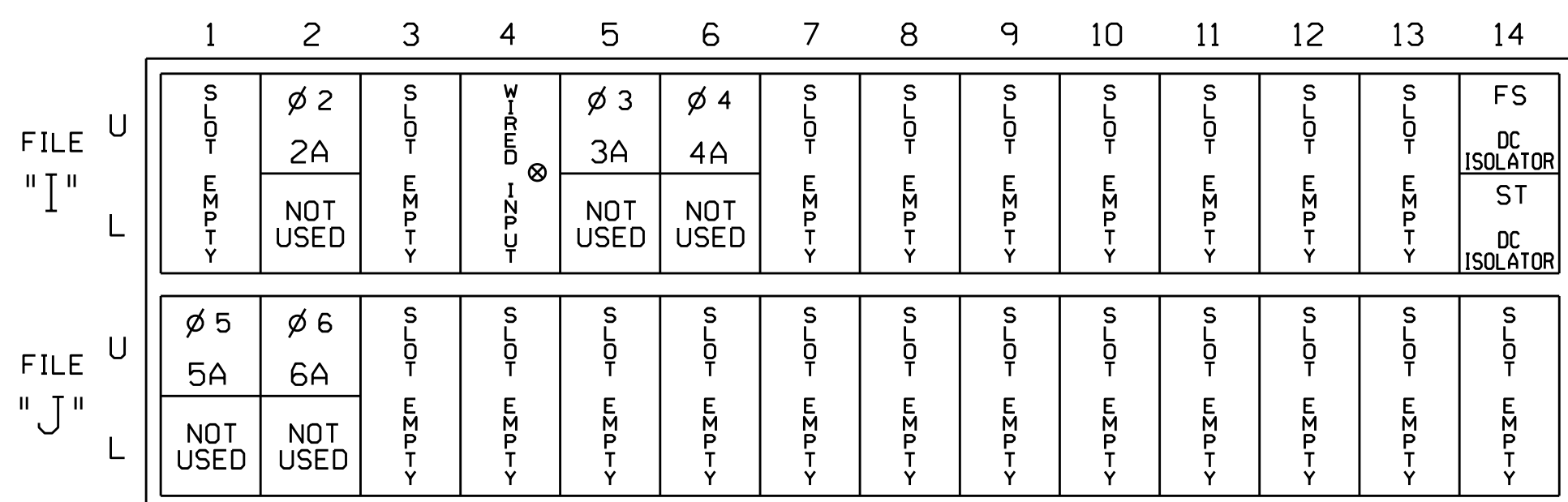


**NOTE**

- The sequence display for head 51 requires special logic programming. See sheet 2 of 2 for programming instructions.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

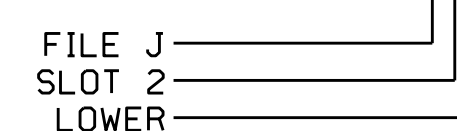
⊗ Wired Input - Do not populate slot with detector card

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			10
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			15
5A <sup>1</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			

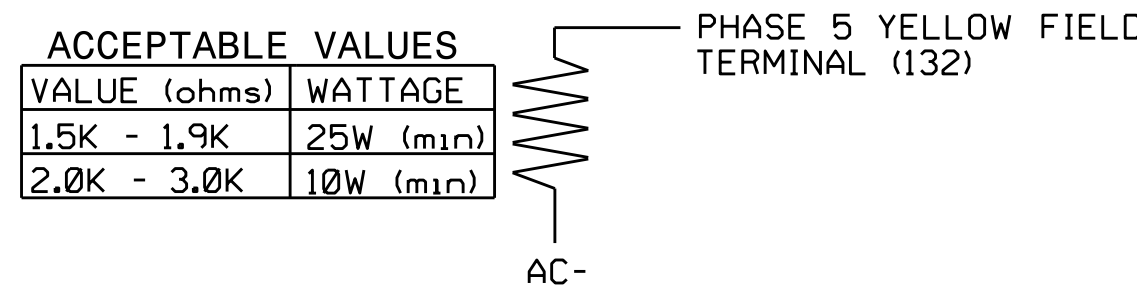
<sup>1</sup>Add jumper from J1-W to 14-W, on rear of input file.

**INPUT FILE POSITION LEGEND: J2L**



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)



ELECTRICAL DETAIL SHEET 1 OF 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  
  
 750 N. Greenfield Pkwy, Garner, NC 27529

US 25A (Sweeten Creek Road) at Gerber Road / Factory Entrance

Division 13 Buncombe County Asheville

PLAN DATE: July 2016 REVIEWED BY: DTJ  
 PREPARED BY: James Peterson REVIEWED BY:

REVISIONS: INIT. DATE

DocuSigned by:  
 Keith M. Mims 9/13/2016  
 2760780948

SIG. INVENTORY NO. 13-0680