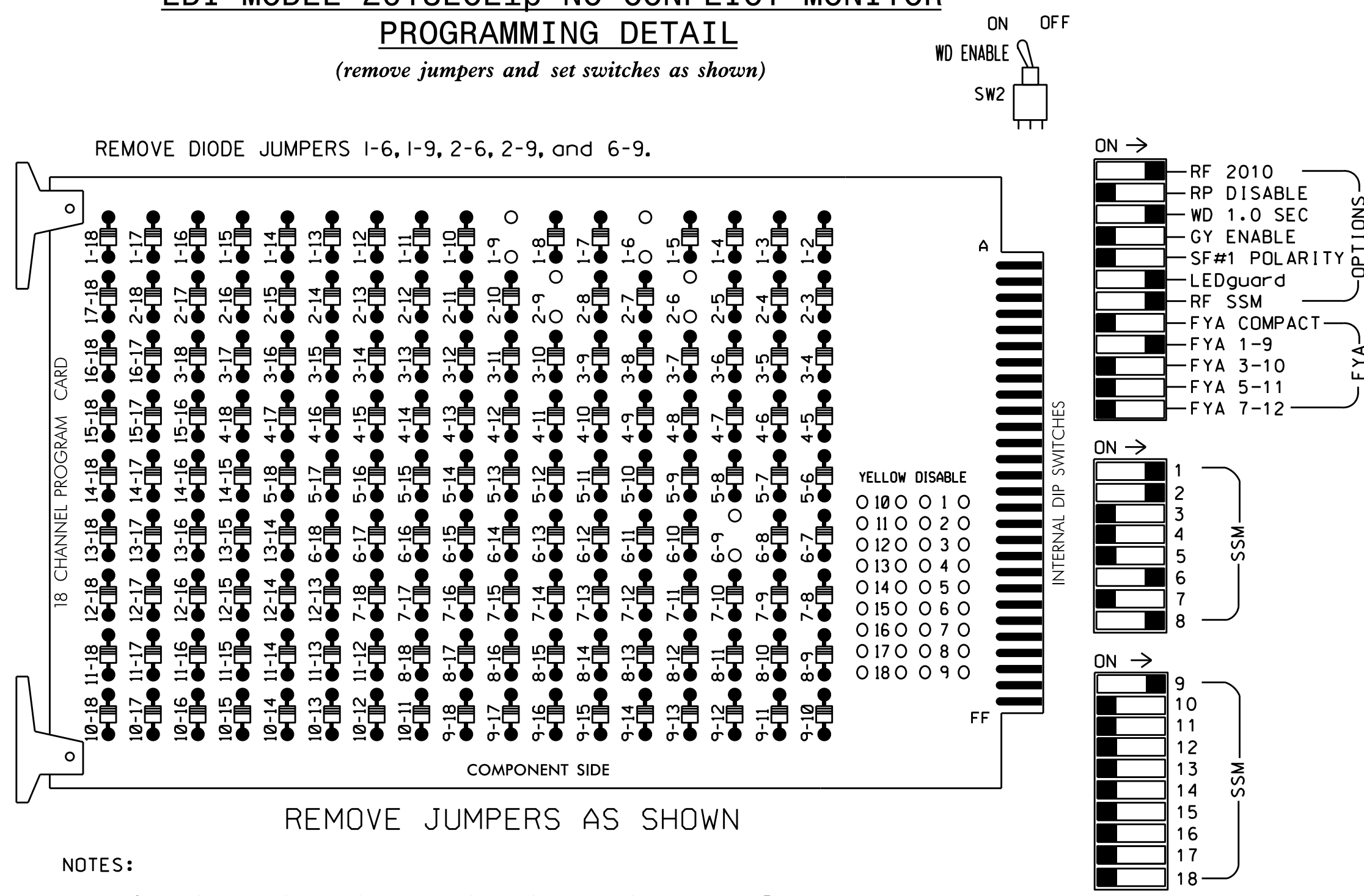


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S1
 PHASES USED.....1,2,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

| LOAD SWITCH NO. | S1 | S2 | S3 | S4 | S5 | S6 | S7 | S8 | S9 | S10 | S11 | S12 | AUX S1 | AUX S2 | AUX S3 | AUX S4 | AUX S5 | AUX S6 |
|-----------------------|-----|-----|-------|----|----|-------|----|-------|-------|-----|-------|-------|--------|--------|--------|--------|--------|--------|
| CMU CHANNEL NO. | 1 | 2 | 13 | 3 | 4 | 14 | 5 | 6 | 15 | 7 | 8 | 16 | 9 | 10 | 17 | 11 | 12 | 18 |
| PHASE | 1 | 2 | 2 PED | 3 | 4 | 4 PED | 5 | 6 | 6 PED | 7 | 8 | 8 PED | OLA | OLB | SPARE | OLC | OLD | SPARE |
| SIGNAL HEAD NO. | 11 | 82 | 21,22 | NU | NU | NU | NU | 61,62 | NU | NU | 81,82 | NU | 11 | NU | NU | NU | NU | NU |
| RED | | * | 128 | | | | | 134 | | | 107 | | | | | | | |
| YELLOW | | | 129 | | | | | 135 | | | 108 | | | | | | | |
| GREEN | | | 130 | | | | | 136 | | | 109 | | | | | | | |
| RED ARROW | | | | | | | | | | | | | A121 | | | | | |
| YELLOW ARROW | | 126 | | | | | | | | | | | A122 | | | | | |
| FLASHING YELLOW ARROW | | | | | | | | | | | | | A123 | | | | | |
| GREEN ARROW | 127 | 127 | | | | | | | | | | | | | | | | |

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

| FILE "I" | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 |
|----------|----------|----------|------|------|------|------|------|------|--------------|------|------|------|------|----------------|
| U | ∅ 1 | ∅ 1 | ∅ 2 | -OR- | -OR- | -OR- | -OR- | -OR- | SYS. DET. S1 | -OR- | -OR- | -OR- | -OR- | FS DC ISOLATOR |
| L | NOT USED | NOT USED | ∅ 2 | ↙ ↘ | ↙ ↘ | ↙ ↘ | ↙ ↘ | ↙ ↘ | SYS. DET. S2 | ↙ ↘ | ↙ ↘ | ↙ ↘ | ↙ ↘ | ST DC ISOLATOR |
| U | -OR- | ∅ 6 | -OR- | ⊗ | -OR- | ∅ 8 | -OR- | -OR- | -OR- | -OR- | -OR- | -OR- | -OR- | S |
| L | ↙ ↘ | NOT USED | ↙ ↘ | ↙ ↘ | ↙ ↘ | ∅ 8 | ↙ ↘ | ↙ ↘ | ↙ ↘ | ↙ ↘ | ↙ ↘ | ↙ ↘ | ↙ ↘ | T |

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

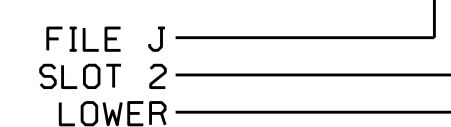
INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO. | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | INPUT ASSIGNMENT NO. | DETECTOR NO. | NEMA PHASE | CALL | EXTEND | FULL TIME DELAY | STRETCH TIME | DELAY TIME |
|-----------------|---------------|-----------------|---------|----------------------|--------------|------------|------|--------|-----------------|--------------|------------|
| 1A ¹ | TB2-1,2 | I1U | 56 | 18 | 1 | 1 | Y | Y | | | 15 |
| | - | J4U | 48 | 10 | 26 | 6 | Y | Y | Y | | 3 |
| 1B | TB2-5,6 | I2U | 39 | 1 | 2 | 1 | Y | Y | | | 15 |
| 2A | TB2-9,10 | I3U | 63 | 25 | 32 | 2 | Y | Y | | | |
| 2B | TB2-11,12 | I3L | 76 | 38 | 42 | 2 | Y | Y | | | |
| 6A | TB3-5,6 | J2U | 40 | 2 | 6 | 6 | Y | Y | | | |
| 8A | TB5-9,10 | J6U | 42 | 4 | 8 | 8 | Y | Y | | | |
| 8B | TB5-11,12 | J6L | 46 | 8 | 18 | 8 | Y | Y | | | |
| * S1 | TB6-9,10 | I9U | 60 | 22 | 11 | SYS | | | | | |
| * S2 | TB6-11,12 | I9L | 62 | 24 | 13 | SYS | | | | | |

¹Add jumper from I1-W to J4-W, on rear of input file.

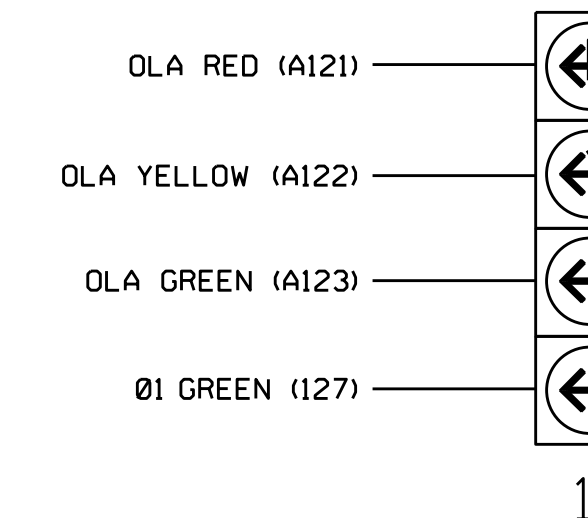
* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



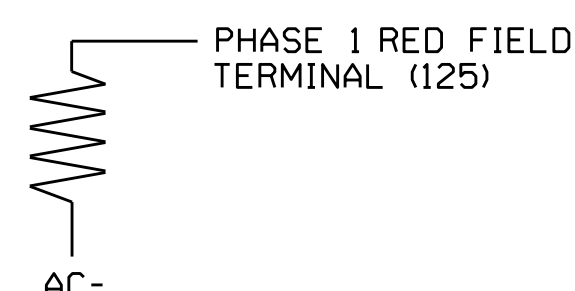
NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

| VALUE (ohms) | WATTAGE |
|--------------|-----------|
| 1.5K - 1.9K | 25W (min) |
| 2.0K - 3.0K | 10W (min) |



Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical and Programming Details for: US 25-A (Sweeten Creek Road) at I-40 WB Off Ramp "A" / WB On Loop "A"

Division 13 Buncombe County Asheville

PLAN DATE: June 2016 REVIEWED BY: BAS

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal of Keith M. Mims, Professional Engineer, No. 036880

DocuSigned by: Keith M. Mims 8/22/2016

SIG. INVENTORY NO. 13-0668