

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF
    ↓
    PRESS '+'
  
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #44 OFF
    ↓
    PRESS '+'
  
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #43 ON
  
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
 OUTPUT 43 = Overlap C Yellow
 OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

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PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS:  XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

BACKUP PROTECTION NOTE


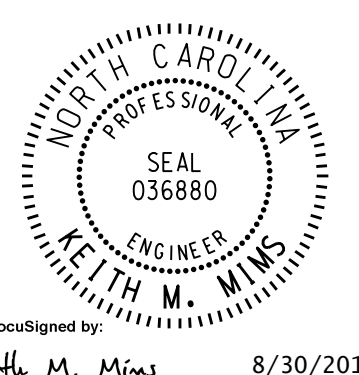
(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phase 2 for 'Backup Protect'. Make sure the Red Revert times shown on the Signal Design Plans are programmed in the 'Phase Timing' menu.

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 13-0559
 DESIGNED: March 2016
 SEALED: 8-15-16
 REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Grantfield Pkwy, Garner, NC 27529	SR 1224 (Monte Vista Road) at SR 1245 (Acton Circle)		SEAL  KEITH M. MIMS ENGINEER
	Division 13 Buncombe County Asheville PLAN DATE: May 2016 REVIEWED BY: PREPARED BY: James Peterson REVIEWED BY:	REVISIONS INIT. DATE	DocuSigned by: Keith M. Mims 8/30/2016 2F807B82A4A8E6 DATE
		SIG. INVENTORY NO. 13-0559	

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 J. Peterson