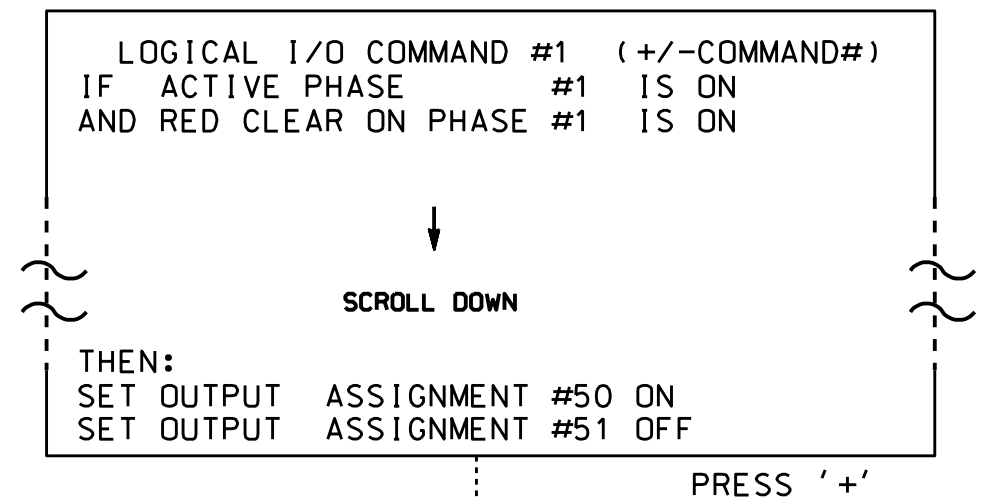


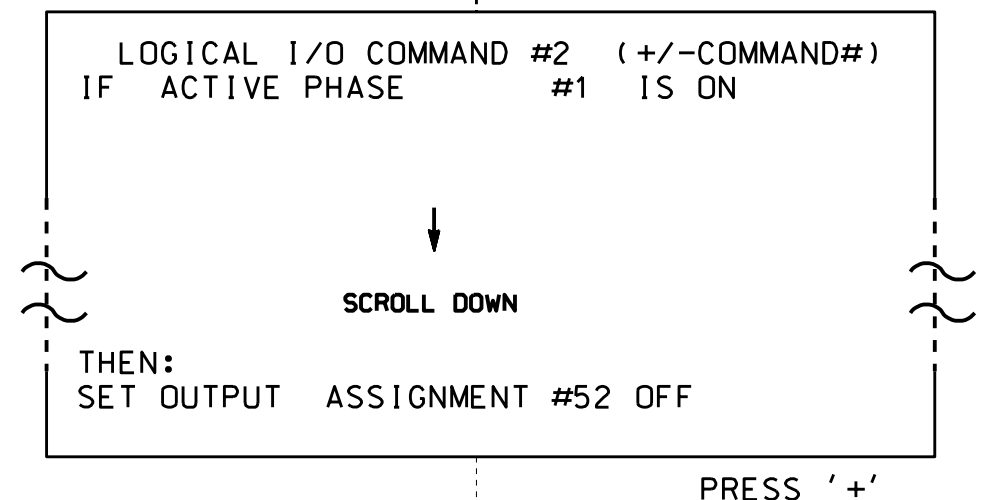
**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

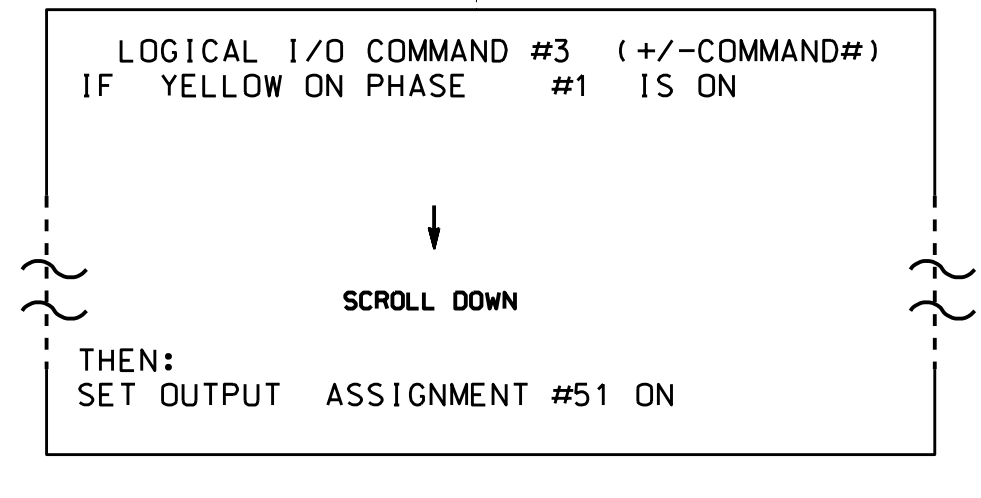
- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



NOTE: Logic for Phase 1 RED Clear when transitioning from Phase 1 to Phase 2 (Head 11).



NOTE: Logic for Switching Flashing Yellow Arrow "OFF" during Phase 1 (Head 11).



NOTE: Logic for Yellow Arrow Clearance from Phase 1 (Head 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

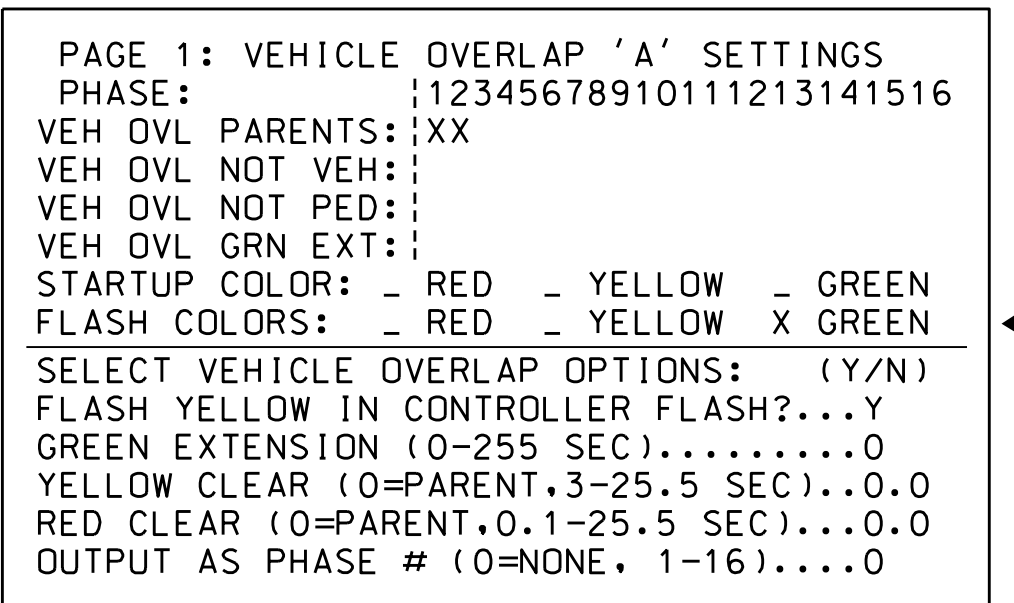
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red
 OUTPUT 51 = Overlap A Yellow
 OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

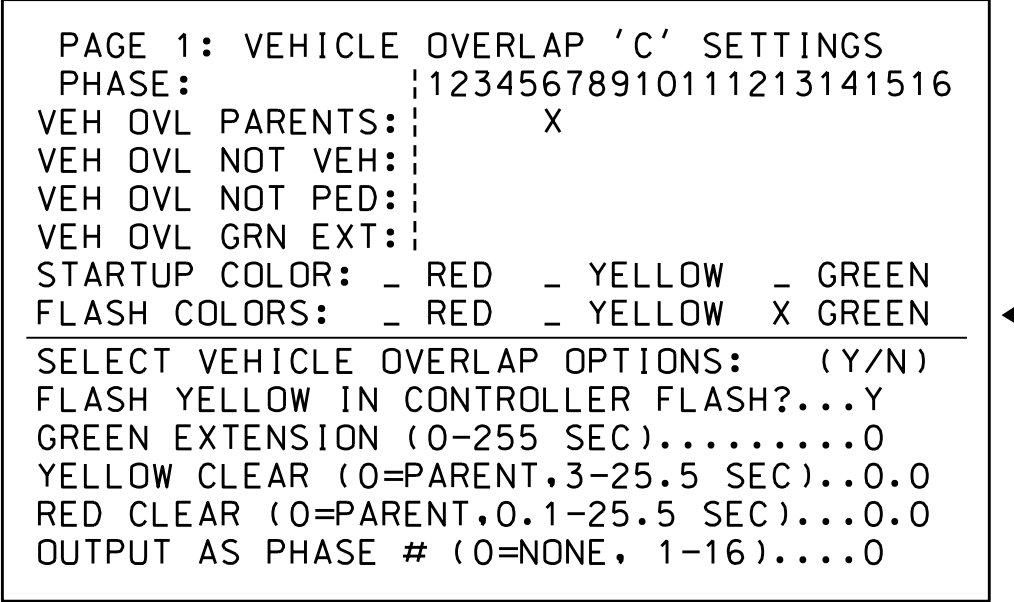
(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).



← NOTICE GREEN FLASH

PRESS '+' TWICE




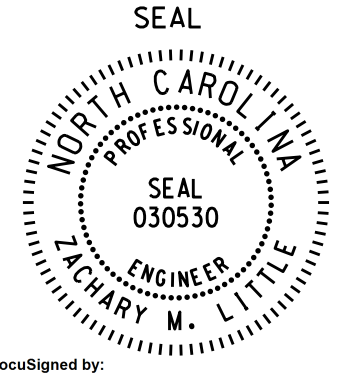
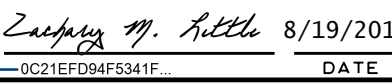
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 13-0525
 DESIGNED: June 2016
 SEALED: 8/11/2016
 REVISED:

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	SR 1781 (Broadway Avenue) at W. T. Weaver Boulevard		SEAL  SEAL 030530 ENGINEER CARY M. LITTLE
	Division 13 Buncombe County Asheville	PLAN DATE: July 2016 REVIEWED BY: BAS	
REVISIONS	INIT.	DATE	DocuSigned by:  8/19/2016 DATE 0021EFD04F5341F
SIG. INVENTORY NO. 13-0525			DATE

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