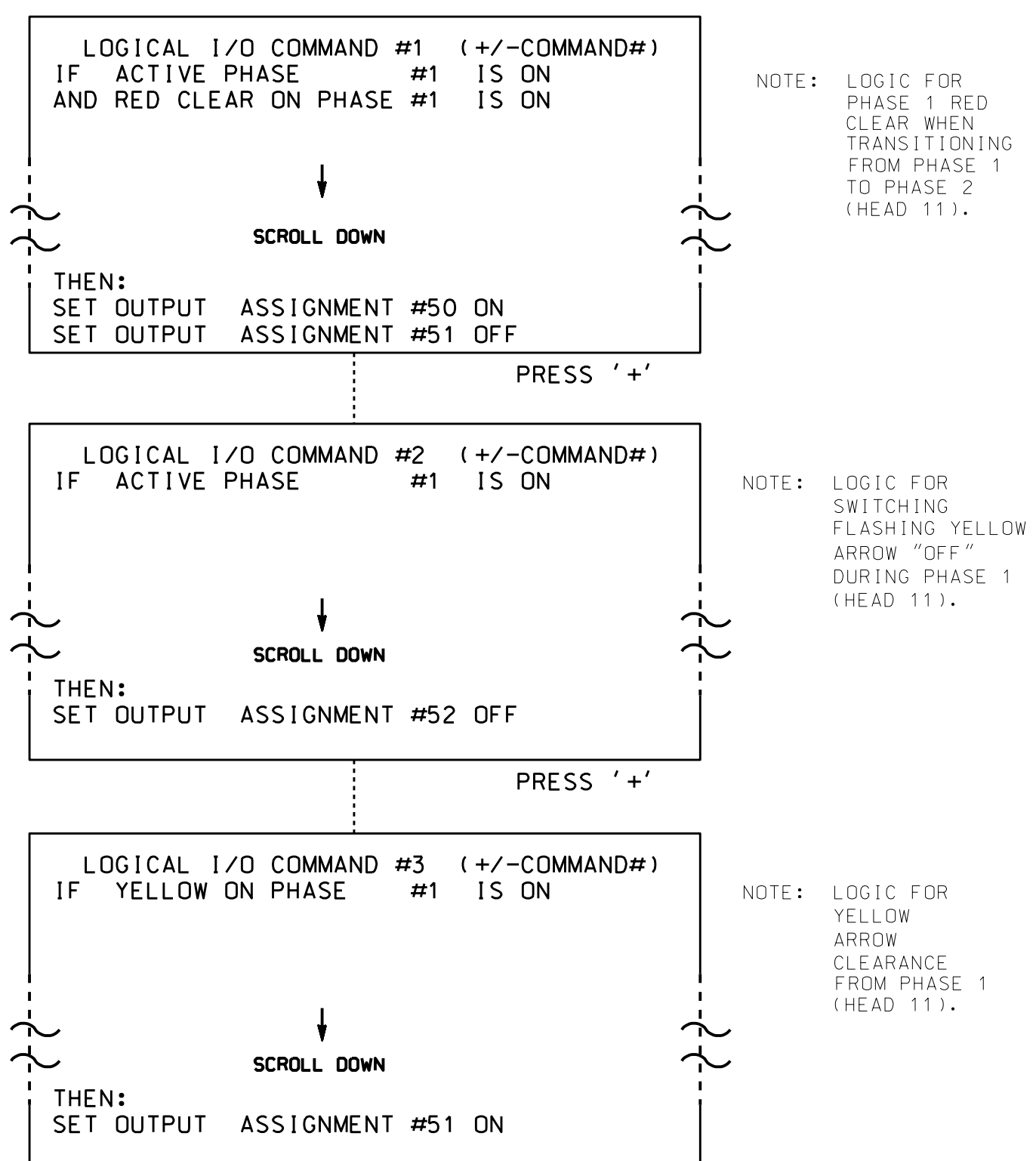


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

PED 3 PROGRAMMING DETAIL

(program controller as shown below)

CHANGING OUTPUT ASSIGNMENTS

- FROM MAIN MENU SELECT '6' (OUTPUTS), THEN '1' (OUTPUT ASSIGNMENTS)
- ENTER 17 (PHASE 8 DW) FOR OUTPUT ASSIGNMENT #.
- SCROLL DOWN TO 'PEDESTRIAN PHASE' AND ENTER 'Y' **REGARDLESS OF DEFAULT PROGRAMMING**
- ENTER '3' FOR 'SELECT PEDESTRIAN PHASE'. NO CHANGE NEEDED FOR 'SELECT COLOR'
- BACKUP TO 'OUTPUT ASSIGNMENTS AND SETTINGS MENU:' BY PRESSING THE 'ESC' BUTTON ON KEYBOARD.
- SELECT '1' (OUTPUT ASSIGNMENTS)
- ENTER 18 (PHASE 8 W) FOR OUTPUT ASSIGNMENT #.
- REPEAT STEPS # 3 AND # 4.

CHANGING INPUT ASSIGNMENTS

- FROM MAIN MENU SELECT '7' (DETECTORS), THEN '2' (PEDESTRIAN DETECTOR ASSIGNMENTS)
- CYCLE TO PED DETECTOR #8 BY REPEATEDLY DEPRESSING '+' KEY
- MODIFY PHASE ASSIGNED TO PED DETECTOR # 8 FROM PHASE 8 TO PHASE 3

PROGRAMMING COMPLETE

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

    PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
    PHASE: |12345678910111213141516
    VEH OVL PARENTS: |XX
    VEH OVL NOT VEH: |
    VEH OVL NOT PED: |
    VEH OVL GRN EXT: |
    STARTUP COLOR: _ RED _ YELLOW _ GREEN
    FLASH COLORS: _ RED _ YELLOW X GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0.0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

PRESS '+' TWICE

```

    PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
    PHASE: |12345678910111213141516
    VEH OVL PARENTS: | X
    VEH OVL NOT VEH: |
    VEH OVL NOT PED: |
    VEH OVL GRN EXT: |
    STARTUP COLOR: _ RED _ YELLOW _ GREEN
    FLASH COLORS: _ RED _ YELLOW X GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0.0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0453
DESIGNED: June 2016
SEALED: 8/10/2016
REVISED: N/A

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:	US 74A (Charlotte Highway) at Rocket Drive (A.C. Reynolds HS) / Cedar Ridge Drive	SEAL
	Division 13 Buncombe County Reynolds	Keith M. Mins ENGINEER
Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	PLAN DATE: August 2016 PREPARED BY: S. Armstrong	REVIEWED BY: BAS REVIEWED BY:
REVISIONS		DocuSigned by: Keith M. Mins 8/17/2016
		SIG. INVENTORY NO. 13-0453

13-0453-2016-08-152
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