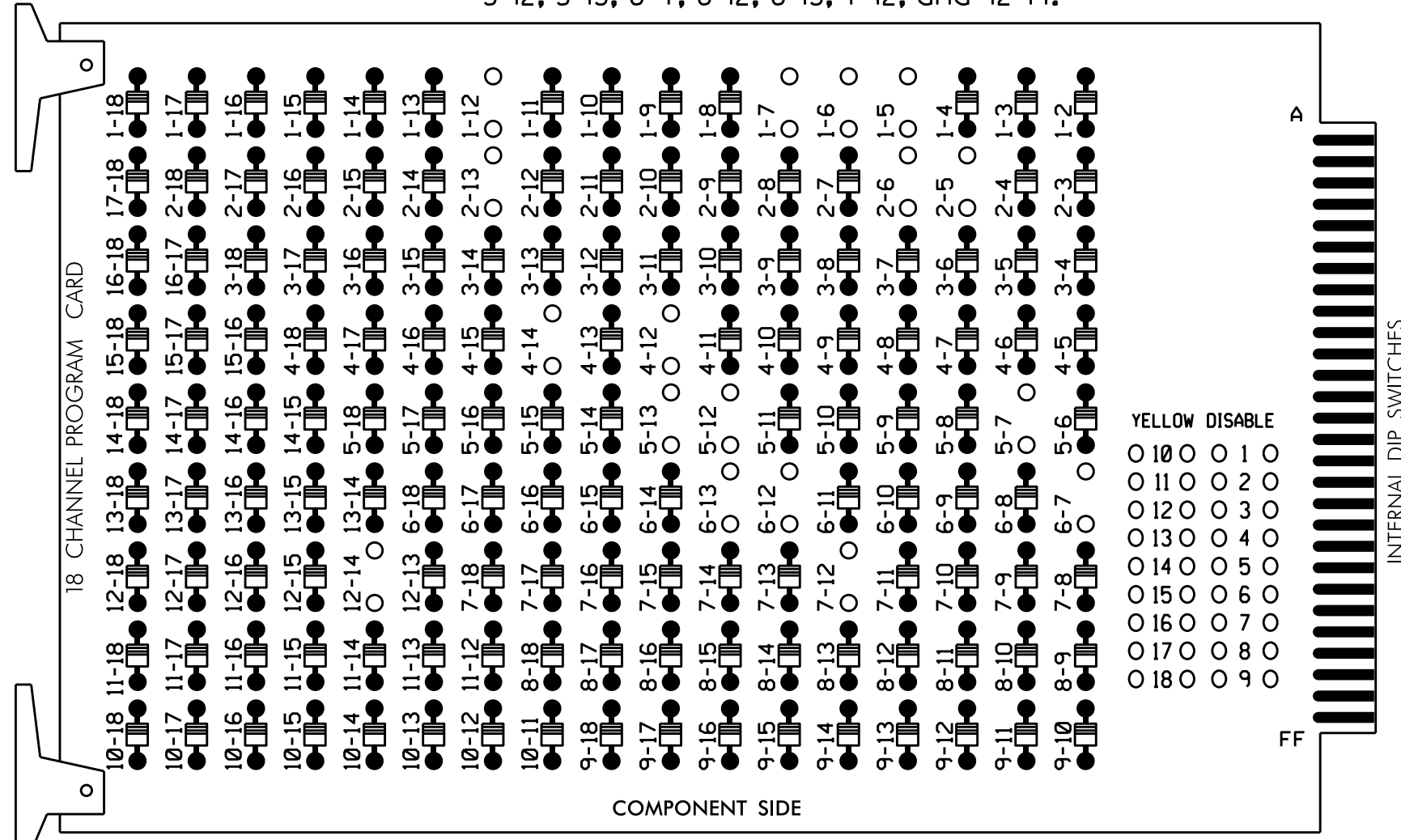


EDI MODEL 2018ECLip-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

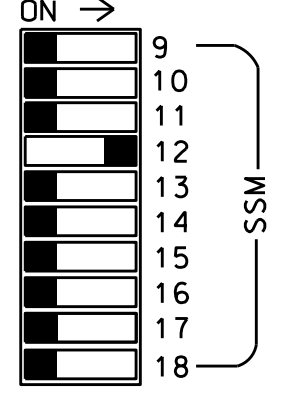
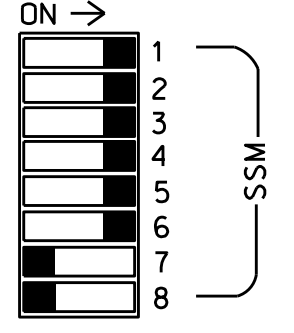
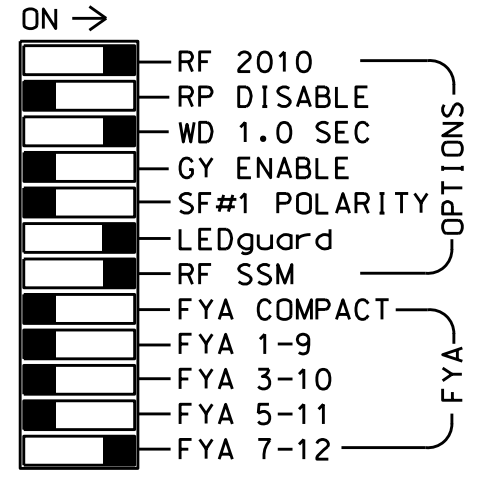
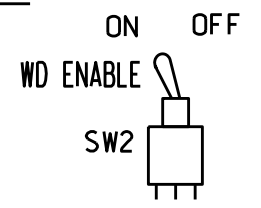
REMOVE DIODE JUMPERS 1-5, 1-6, 1-7, 1-12, 2-5, 2-6, 2-13, 4-12, 4-14, 5-7, 5-12, 5-13, 6-7, 6-12, 6-13, 7-12, and 12-14.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, and 4 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash.
- The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,
 S10,AUX S5
 PHASES USED.....1,2,2PED,3,4,4PED,5,6
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....1+4
 OVERLAP "E".....1

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	OLE	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11,12	21,22	P21, P22	31	32	41	42	P41, P42	51	61,62	NU	43,44	NU	NU	NU	NU	43,44	NU
RED	128			116	116	101	101			134								A101
YELLOW	129			117	117	102	102			135		*						
GREEN	130			118	118	103	103			136								
RED ARROW	125									131								
YELLOW ARROW	126									132								A102
FLASHING YELLOW ARROW																		A103
GREEN ARROW	127			118		103			133		124							
Hand				113					104									
Walking				115					106									

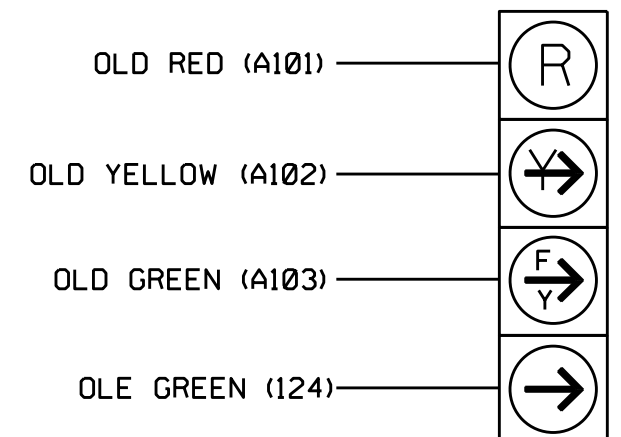
NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.
 NOTE: Output assignments for load switch S10 have been remapped. See sheet 3 for details.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



43,44

NOTE

The sequence display for signal heads 43 and 44 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0389
 DESIGNED: May 2016
 SEALED: 8/15/2016
 REVISED: N/A

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 1	∅ 2	∅ 3	∅ 4	S	S	S	S	S	∅ 2 PED	S	FS
L	NOT USED	∅ 1	∅ 2	NOT USED	NOT USED	NOT USED	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
U	∅ 5	∅ 6	S	S	S	S	S	S	S	S	S	S	S	S
L	NOT USED	∅ 6	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

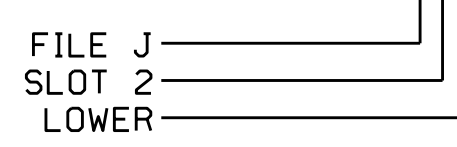
FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			
1C	TB2-7,8	I2L	43	5	12	1	Y	Y			10
1D	TB2-9,10	I3U	63	25	32	1	Y	Y			15
2A	TB2-11,12	I3L	76	38	42	2	Y	Y			
2B	TB4-1,2	I4U	47	9	22	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					

NOTE:
 INSTALL DC ISOLATOR IN INPUT FILE SLOT 112.

INPUT FILE POSITION LEGEND: J2L

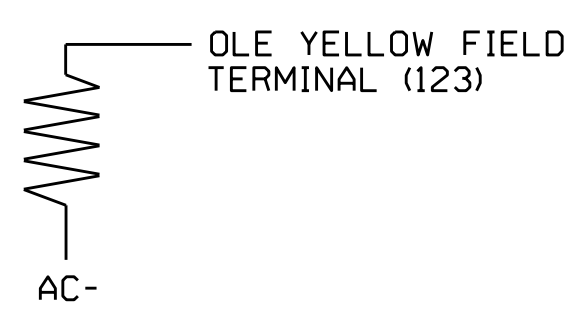


LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
 Transportation Mobility and Safety Solutions
 NORTH CAROLINA PROFESSIONAL ENGINEER
 KEITH M. MINES
 750 N. Greenfield Pkwy, Garner, NC 27529

US 25 (McDowell St./ All Souls Crescent) at US 25A (Lodge St.)/ Biltmore Estate Drive

Division 13 Buncombe County Asheville

PLAN DATE: July 2016 REVIEWED BY: BAS

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: Keith M. Mines 9/12/2016

SIG. INVENTORY NO. 13-0389

17-AUG-2016 09:43
 S:\MITSAS\15_Signal\work\hork\coups\g_Mph\mstron\g\30389_sm.elec_xxx.dgn
 sarmstrong