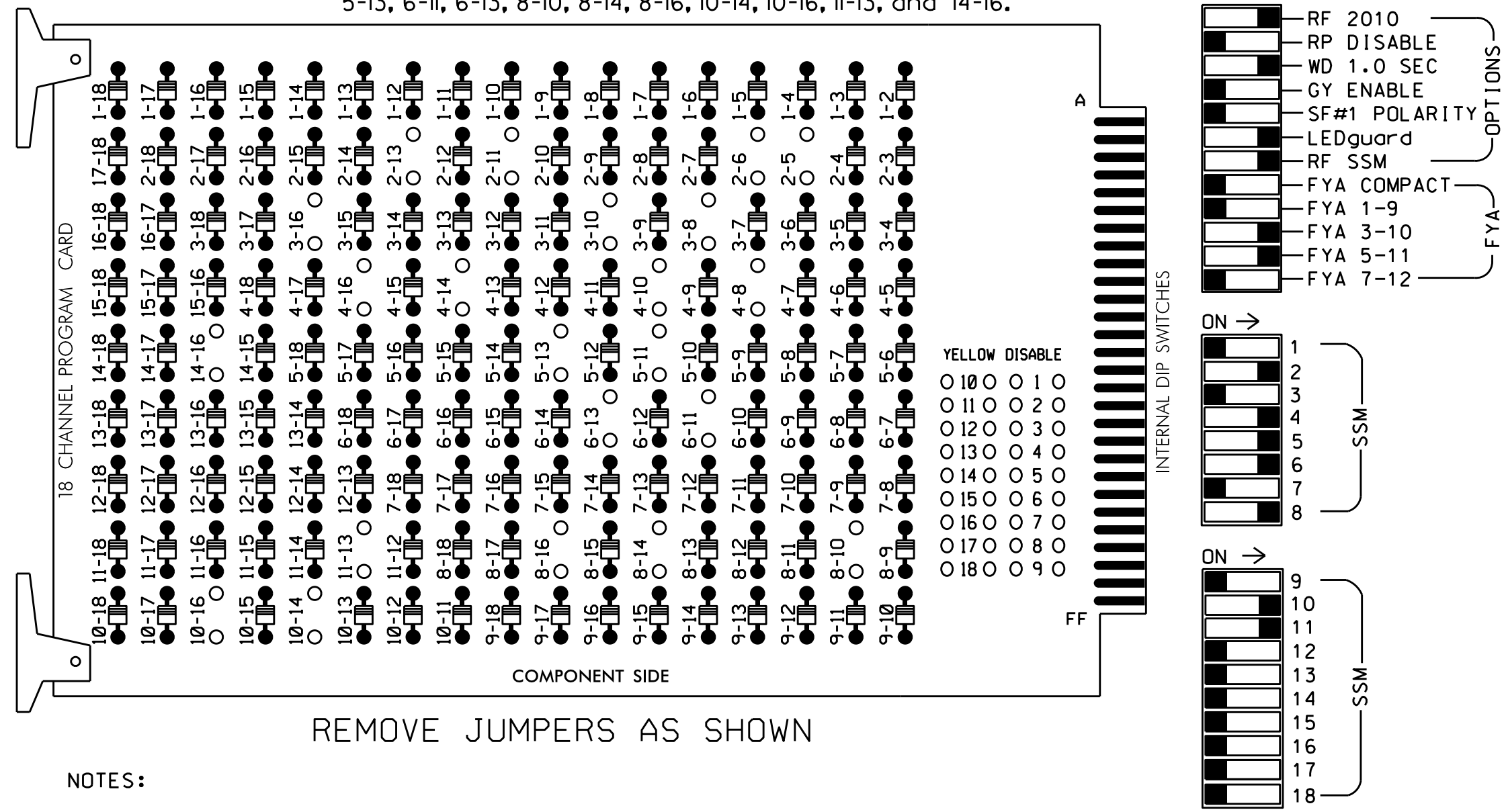


**EDI MODEL 2018EClip-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-11, 2-13, 3-8, 3-10, 3-16, 4-8, 4-10, 4-14, 4-16, 5-11, 5-13, 6-11, 6-13, 8-10, 8-14, 8-16, 10-14, 10-16, 11-13, and 14-16.



REMOVE JUMPERS AS SHOWN

**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

**INPUT FILE POSITION LAYOUT**

(front view)

FILE "I" L	1	2	3	4	5	6	7	8	9	10	11	12	13	14
	∅ 2 2A,2B	∅ 3 3A	∅ 4 4A	∅ 5 5A	∅ 5 5B	∅ 6 6A,6B	∅ 8 8A	∅ 2 PED DC ISOLATOR	∅ 4 PED DC ISOLATOR	∅ 8 PED DC ISOLATOR	FS DC ISOLATOR	∅ 3 DC ISOLATOR	∅ 4 DC ISOLATOR	∅ 5 DC ISOLATOR
FILE "J" L	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	SYS. DET. S1	SYS. DET. S2					

EX.: 1A, 2A, ETC. = LOOP NO.'S

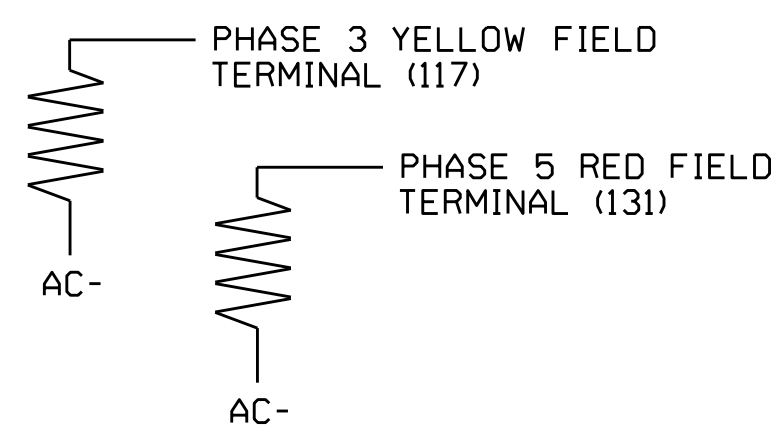
FS = FLASH SENSE  
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2, 4 and 8 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash, and overlap 2 as Wag Overlaps.
7. The cabinet and controller are part of the Asheville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S2,S3,S4,S5,S6,S7,S8,S11,S12,  
 AUX S2,AUX S4  
 PHASES USED.....2,2PED,3,4,4PED,5,6,8,8PED  
 OVERLAP "A".....NOT USED  
 OVERLAP "B".....3+4  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**COUNTDOWN PEDESTRIAN SIGNAL OPERATION**

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

**ADVANCE WALK NOTE**

(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phases 2, 4 and 8 for 'Advanced Walk'. Make sure the Walk Advance Time shown on the Signal Design plans are programmed in the 'Phase Timing' menu.

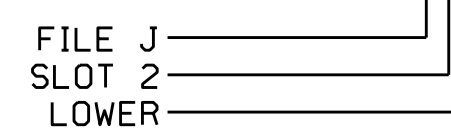
**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
3A <sup>1</sup>	TB4-5,6	I5U	58	20	3	3	Y	Y			15
		J8U	50	12	28	8	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
		I4U	47	9	22	2	Y	Y			
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
6A,6B	TB3-9,10	J3U	64	26	36	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:  
 INSTALL DC ISOLATORS  
 IN INPUT FILE SLOTS  
 I12 AND I13.

- <sup>1</sup>Add jumper from I5-W to J8-W, on rear of input file.
  - <sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.
- \* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	P21, P22	31*	41,42	P41, P42	42	51*	61,62	NU	NU	81,82	P81, P82	NU	31*	NU	51*	NU
RED	128			101		*		134			107							
YELLOW	129		*	102				135			108							
GREEN	130			103				136			109							
RED ARROW														A124			A114	
YELLOW ARROW							132							A125			A115	
FLASHING YELLOW ARROW														A126			A116	
GREEN ARROW				118			133	133										
Hand icon			113			104					110							
Person icon			115			106					112							

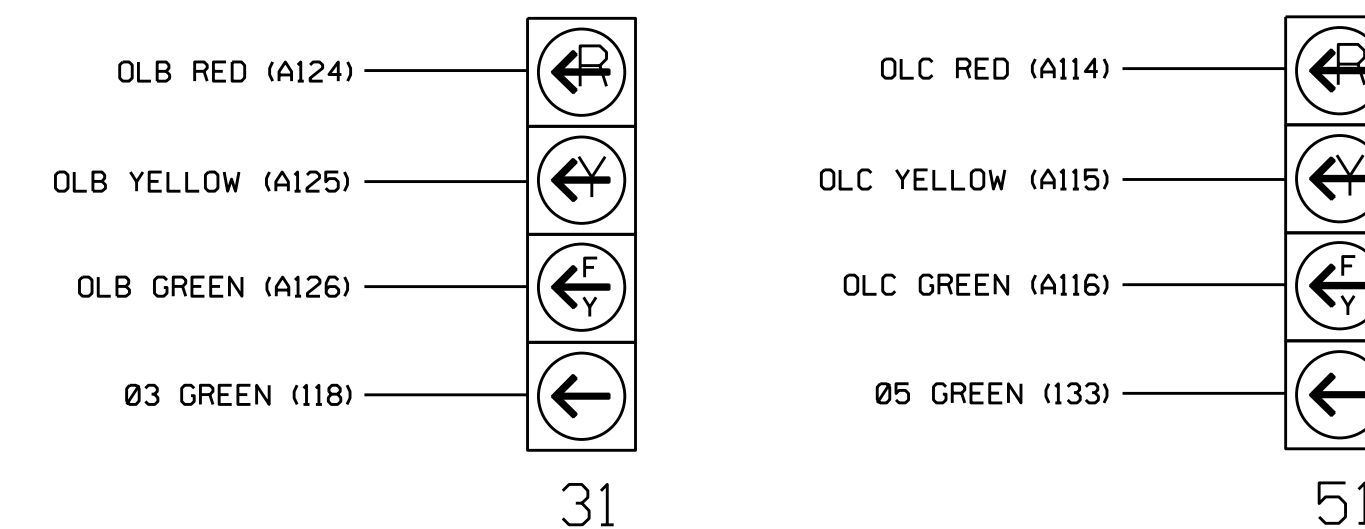
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

\* See pictorial of head wiring in detail below.

**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

The sequence display for signal heads 31 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR  
 THE SIGNAL DESIGN: 13-0263  
 DESIGNED: April 2016  
 SEALED: 8/4/2016  
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	SR 3548 (Patton Ave.) at SR 3602/SR 3548 (Clingman Ave.)		SEAL  Keith M. Mims 8/23/2016
	Division 13 PLAN DATE: July 2016 PREPARED BY: S. Armstrong	Buncombe County Asheville REVIEWED BY: BAS REVIEWED BY:	