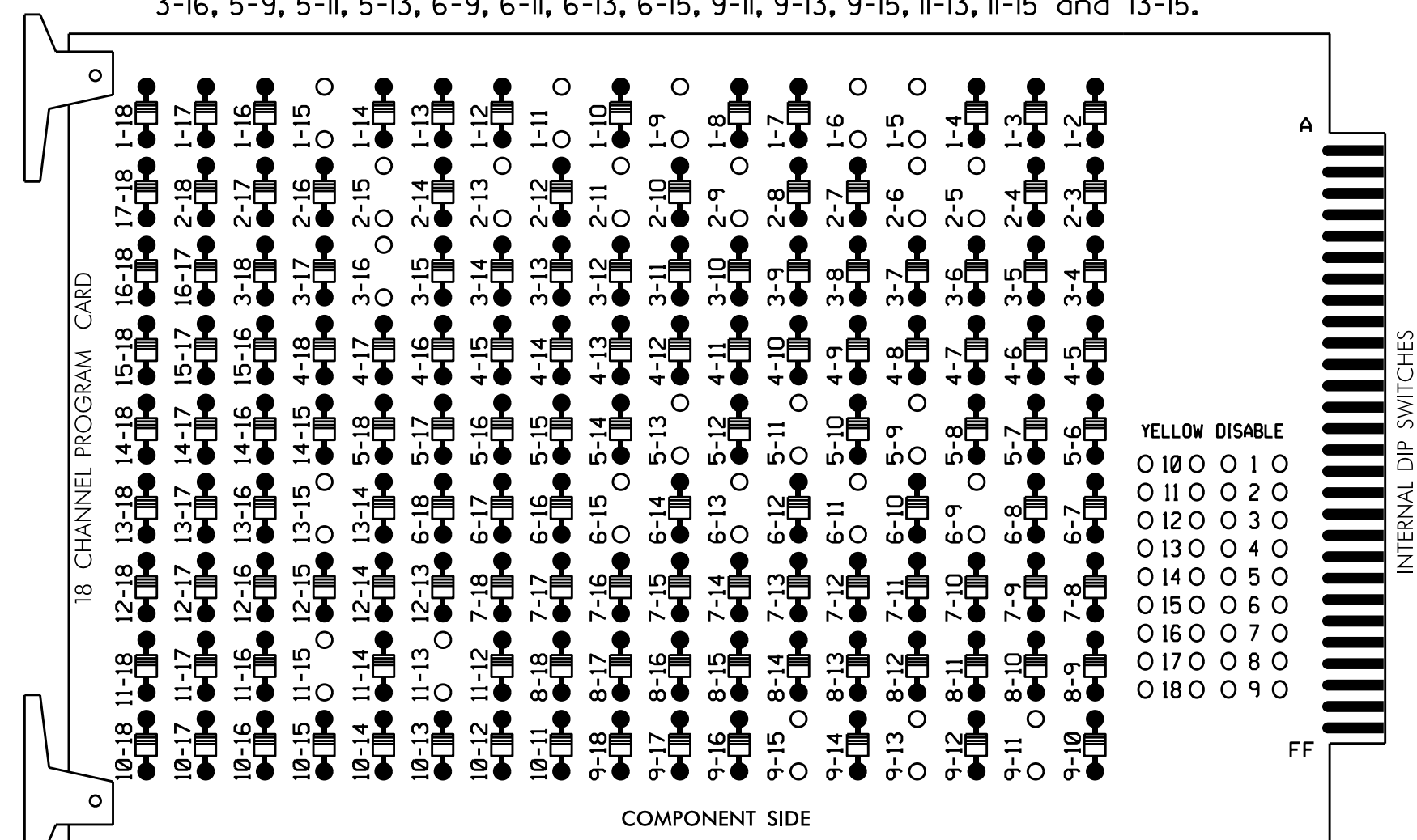


EDI MODEL 2018EClip-NC CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

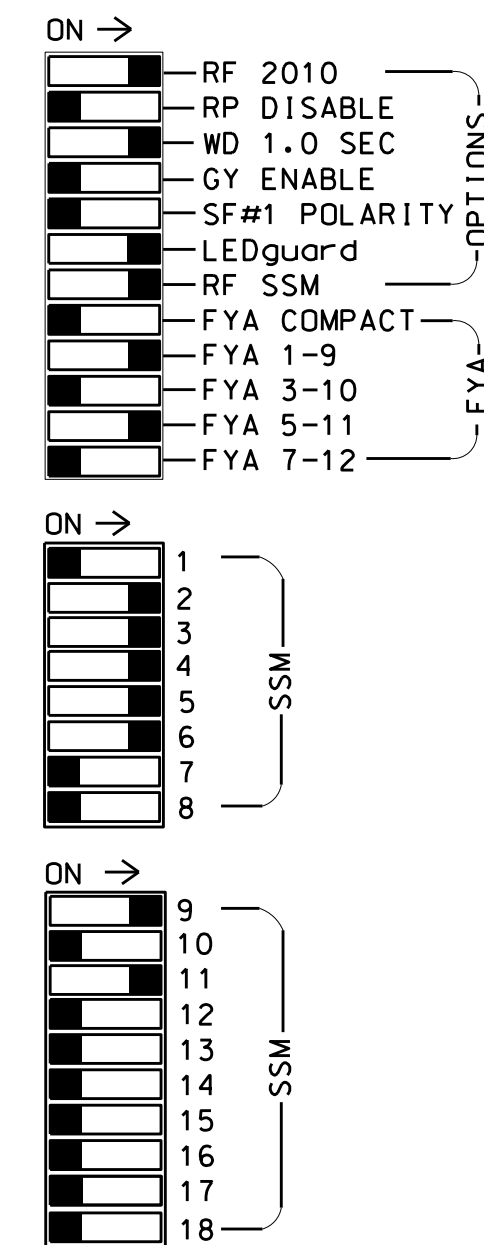
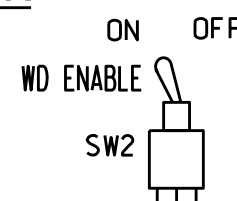
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 9-11, 9-13, 9-15, 11-13, 11-15 and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Start Up In Green.
4. Program phases 2, 3 and 6 for 'STARTUP PED CALL'.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S7,S8,S9, S12,AUX S1,AUX S4.
 PHASES USED.....1,2,2PED,3,3PED,4,5,6,6PED.
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	3 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11*	21,22	P21, P22	31	32	41	42,43	NU	42	51	61,62, 63	P61, P62	NU	NU	P31, P32	11*	NU	51*	NU
RED		128		116	116	101	101			*	134								
YELLOW	*	129		117	117	102	102				135								
GREEN		130		118	118	103	103				136								
RED ARROW																A121		A114	
YELLOW ARROW											132					A122		A115	
FLASHING YELLOW ARROW																A123		A116	
GREEN ARROW	127			118		103			133	133									
Hand			113									119		110					
Person			115									121		112					

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	∅ 1 1A	∅ 2 2A,2B	∅ 3 3A	∅ 4 4A	∅ 5 5A	∅ 6 6A,6B	∅ 7 7A	∅ 8 8A	∅ 9 9A	∅ 10 10A	∅ 11 11A	∅ 12 12A	∅ 13 13A	∅ 14 14A
FILE "J"	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

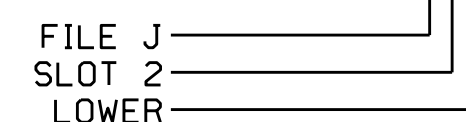
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
2A,2B	TB2-5,6	J4U	48	10	26	6	Y	Y			
3A	TB4-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I5U	58	20	3	3	Y	Y			5
4B	TB4-11,12	I6U	41	3	4	4	Y	Y			
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			15
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2	PED				
P31,P32	TB8-8,9	I13L	70	32	PED 8	3	PED				
P61,P62	TB8-7,9	I13U	68	30	PED 6	6	PED				

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

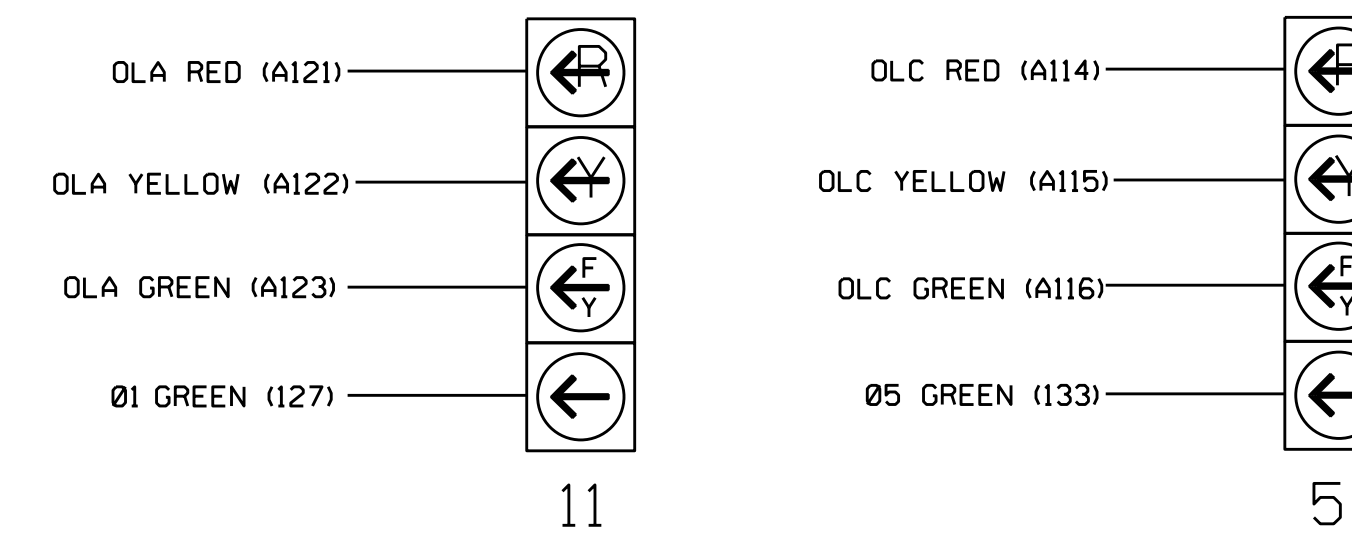
- 1 Add jumper from I1-W to J4-W, on rear of input file.
- 2 Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

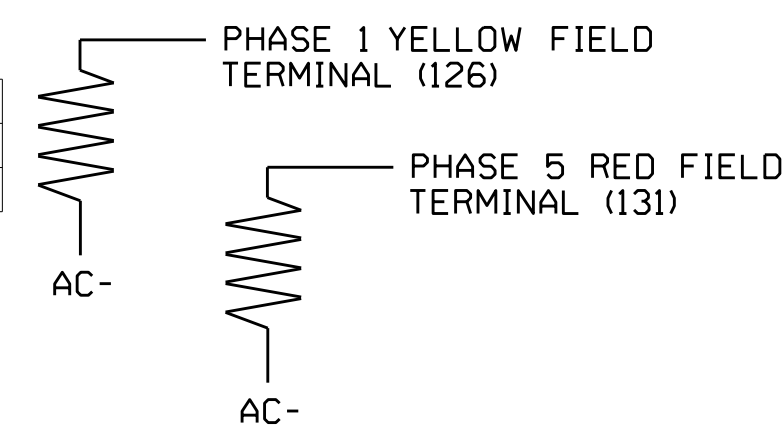
1. The sequence display for these signals requires special logic programming. See sheet 2 of 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0232
 DESIGNED: December 2015
 SEALED: 8/5/2016
 REVISED:

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



ELECTRICAL DETAIL SHEET 1 OF 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
 TRANSPORTATION MOBILITY AND SAFETY ADMINISTRATION
 NORTH CAROLINA DIVISION OF TRANSPORTATION
 Signal Management Section
 750 N. Greenfield Pkwy, Garner, NC 27529

US 70-74A (Tunnel Rd.)
 at
 Kenilworth Road

Division 13 Buncombe County Asheville
 PLAN DATE: May 2016 REVIEWED BY: K. Mims
 PREPARED BY: Z.M. Little REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by:
 Keith M. Mims 8/18/2016
 2F807B82A434E6 DATE

SIG. INVENTORY NO. 13-0232