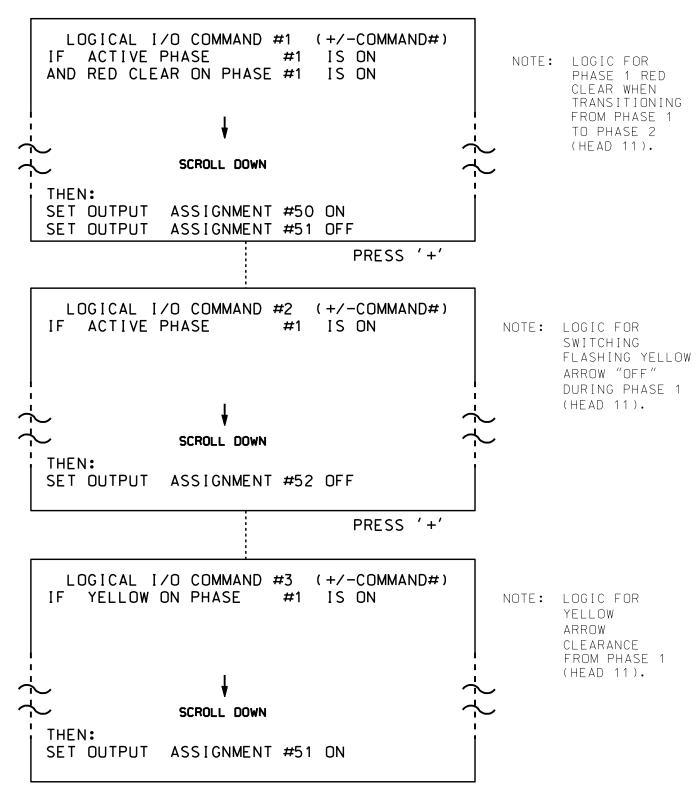
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

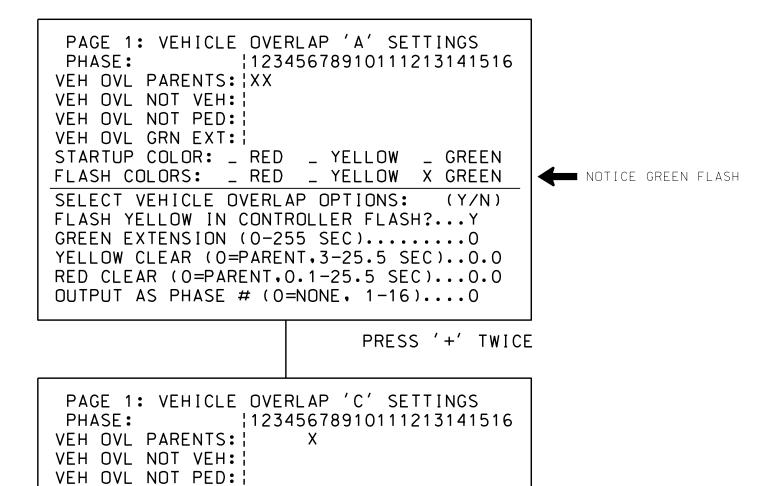
## OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN

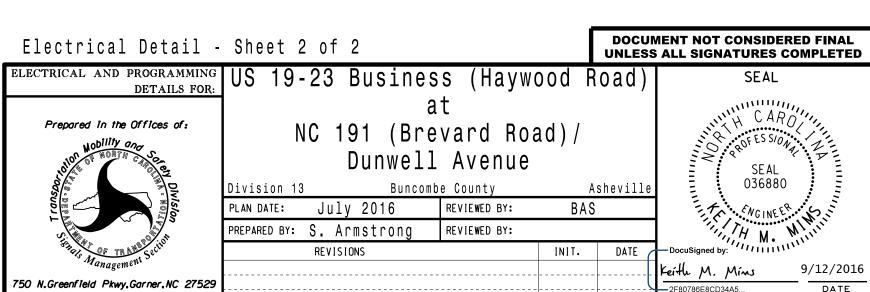
SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y

RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE. 1-16)...0

GREEN EXTENSION (0-255 SEC)......O
YELLOW CLEAR (0=PARENT,3-25.5 SEC).....

VEH OVL GRN EXT: :

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0215
DESIGNED: June 2016
SEALED: 8/17/2016
REVISED: N/A



SIG. INVENTORY NO. 13-0215

S:\*ITS&SU\*ITS Signals\*Workgroups\*Sig Man\*Armstrong\*130215\_sm\_ele\_; sarmstrong \_