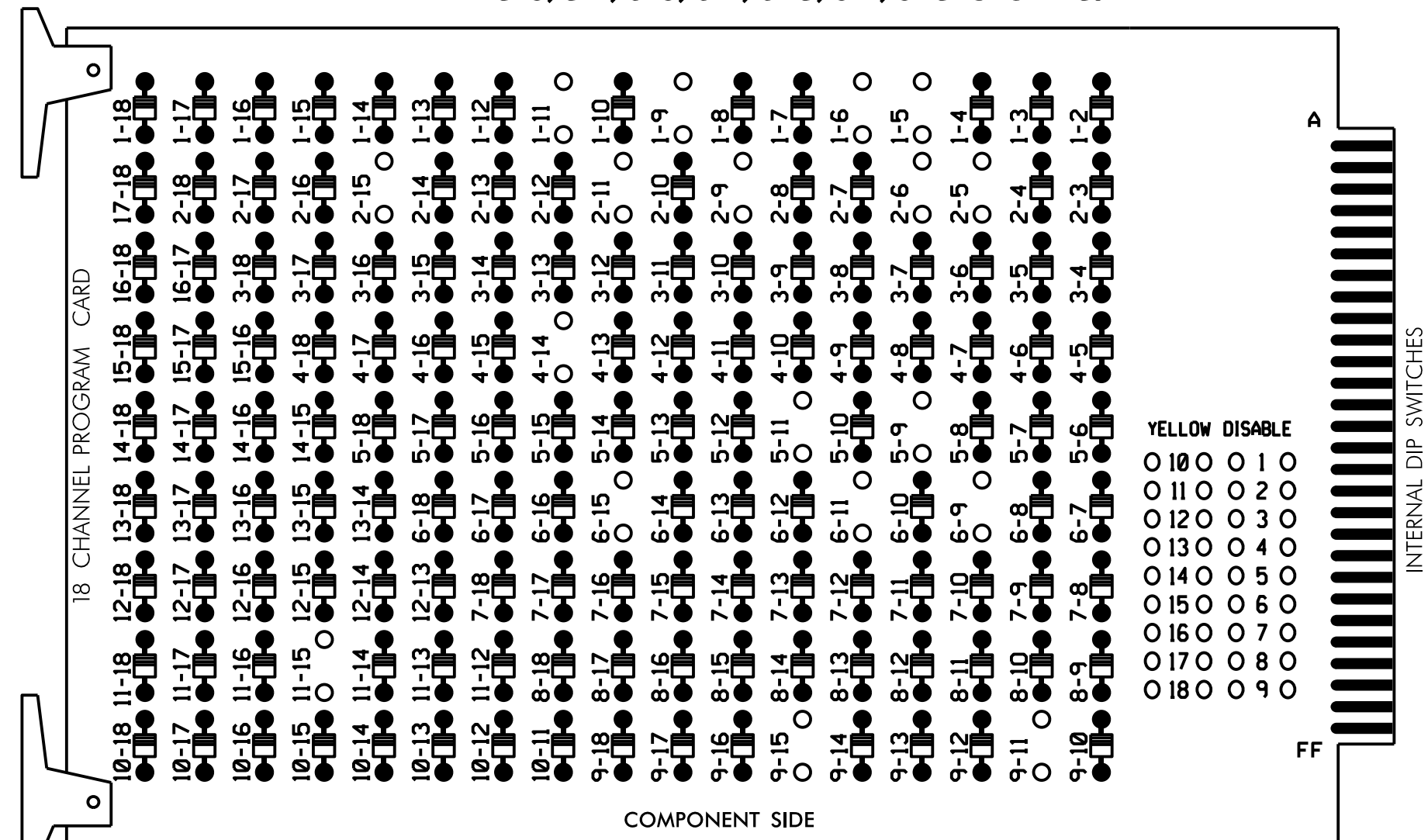


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

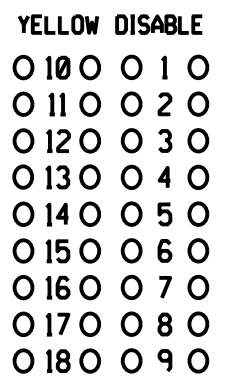
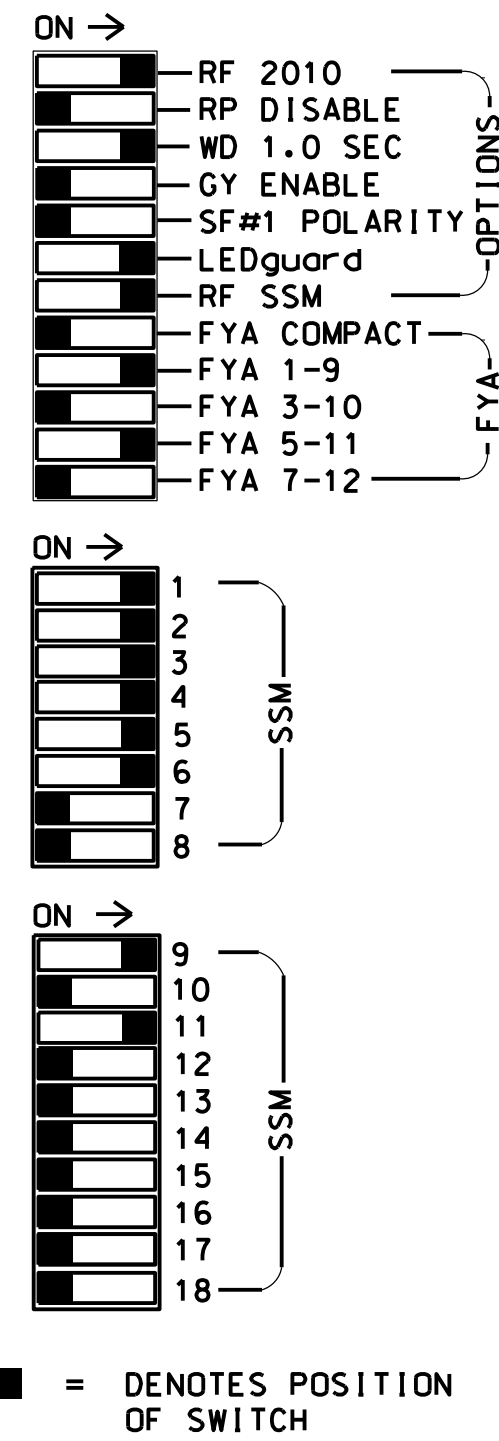
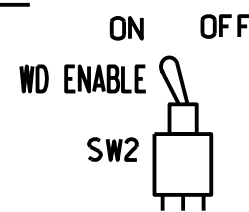
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 2-15, 4-14
5-9, 5-11, 6-9, 6-11, 6-15, 9-11, 9-15 and 11-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Walk.
- The cabinet and controller are part of the Fayetteville Signal System.
- Program phases 4 and 6 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S6,S7,S8,S9,AUX S1,
 AUX S4
 PHASES USED.....1,2,3,4,5,6
 OVERLAP "A".....*
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED
 * See overlap programming detail on sheet 2

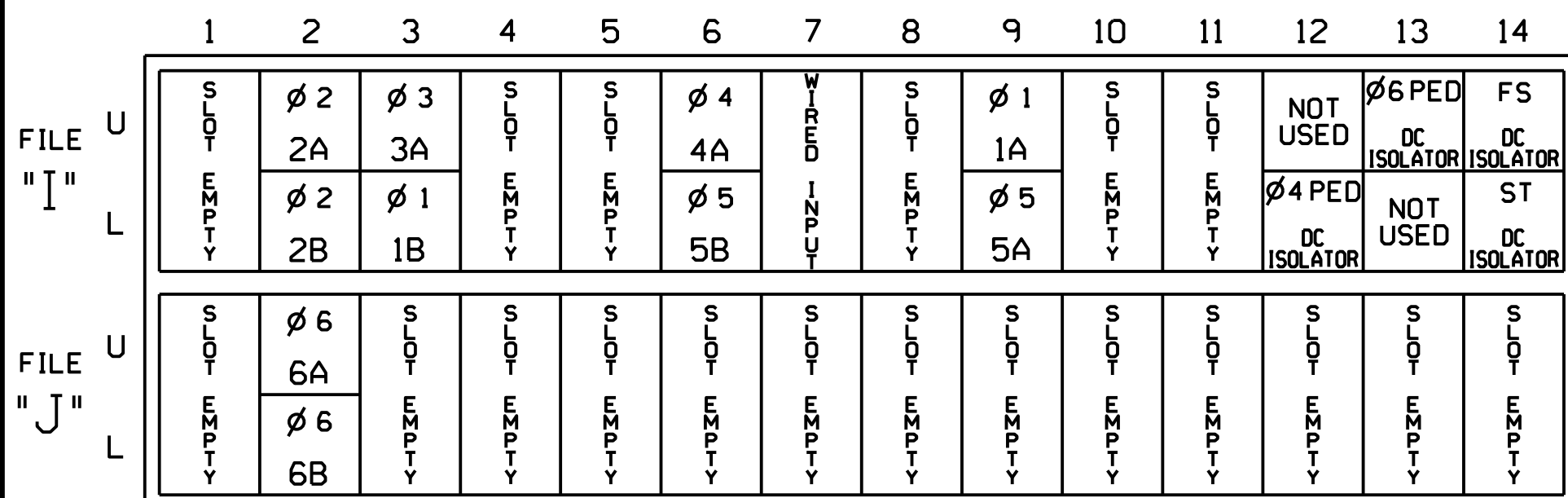
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CHU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	SPARE	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	32	21,22	NU	31	32	41	42	P41,P42	42	51	61,62	P61,P62	NU	NU	NU	51	NU
RED	*	128		116	116	101	101			*	134							
YELLOW		129		117	117	102	102				135							
GREEN		130		118	118	103	103				136							
RED ARROW													A121				A114	
YELLOW ARROW	126								132					A122			A115	
FLASHING YELLOW ARROW														A123			A116	
GREEN ARROW	127	127		118	103		133	133										
							104				119							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

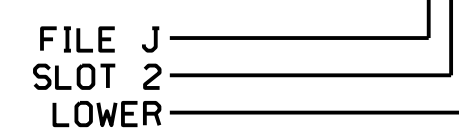
Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB6-9,10	19U	60	11	1		15	S
	TB6-1,2	17U	65	34	6		3	C
1B	TB2-11,12	13L	76	42	1		15	S
	TB2-5,6	12U	39	2	2			S
2B	TB2-7,8	12L	43	12	2			S
	TB2-9,10	13U	63	32	3		3	S
4A	TB4-9,10	16U	41	4	4		3	S
	TB6-11,12	19L	60	13	5		15	S
5A	TB6-3,4	17L	78	44	2		3	C
	TB4-11,12	16L	45	14	5		15	S
6A	TB3-5,6	J2U	40	6	6			S
	TB3-7,8	J2L	44	16	6			S
P41,P42	TB8-5,6	112L	67	PED2	4 PED			S
P61,P62	TB8-7,9	113U	68	PED4	6 PED			S

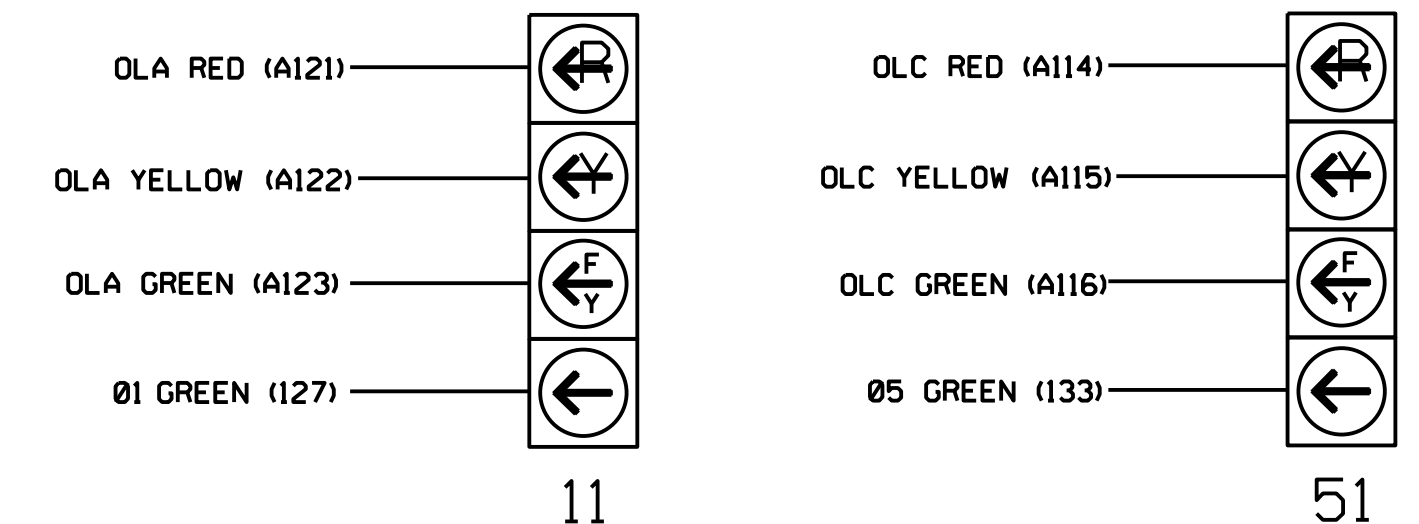
- * Add jumper from 17-F to 19-F.
- * Add jumper from 17-W to 19-W.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



COUNTDOWN PEDESTRIAN SIGNAL OPERATION

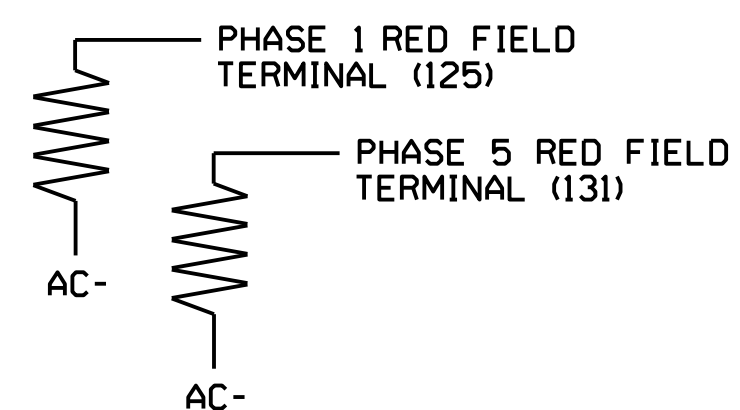
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: C023
 DESIGNED: NOVEMBER 2016
 SEALED: 11/18/2016
 REVISED:

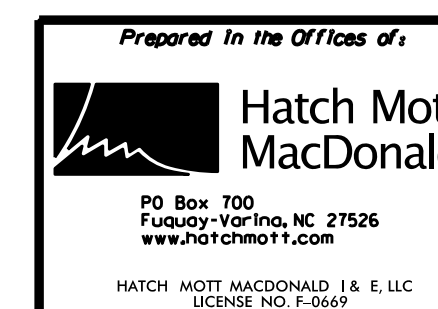
LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

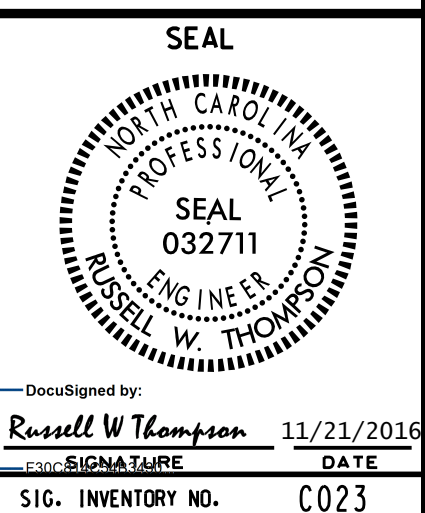
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail Sheet 1 of 2



VILLAGE DRIVE AT BOONE TRAIL EXTENSION/ FRIENDSHIP DRIVE			
PLAN DATE: NOVEMBER 2016	REVIEWED BY: RWT	DATE: 11/21/2016	DATE: 11/21/2016
PREPARED BY: BLR	REVIEWED BY:	INITIALS:	DATE:
REVISIONS:	INITIALS:	DATE:	DATE:



default \\NCF-DATA\Project\360655_U-5742_Fey-Sig\Project\Signals\Design\100%\FINAL SEALED PLANS\Revised 11/21/2016\1010407.dgn 11/21/2016