ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER |
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE:PPLT FYA PROTECTED PHASE (LEFT TURN)..... 1 PERMISSIVE PHASE (OPPOSING THRU).... 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED PHASE (LEFT TURN)..... 5 PERMISSIVE PHASE (OPPOSING THRU).... 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: C014 DESIGNED: NOVEMBER 2016 SEALED: 11/18/2016 REVISED:

Electrical Detail Sheet 2 of 2



Prepared in the Offices of:

McPHERSON CHURCH ROAD BARCELONA DRIVE

FAYETTEVILLE RWT

PLAN DATE: NOVEMBER 2016 REVIEWED BY: PREPARED BY: BLR

SEAL (032711 SIG. INVENTORY NO.

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