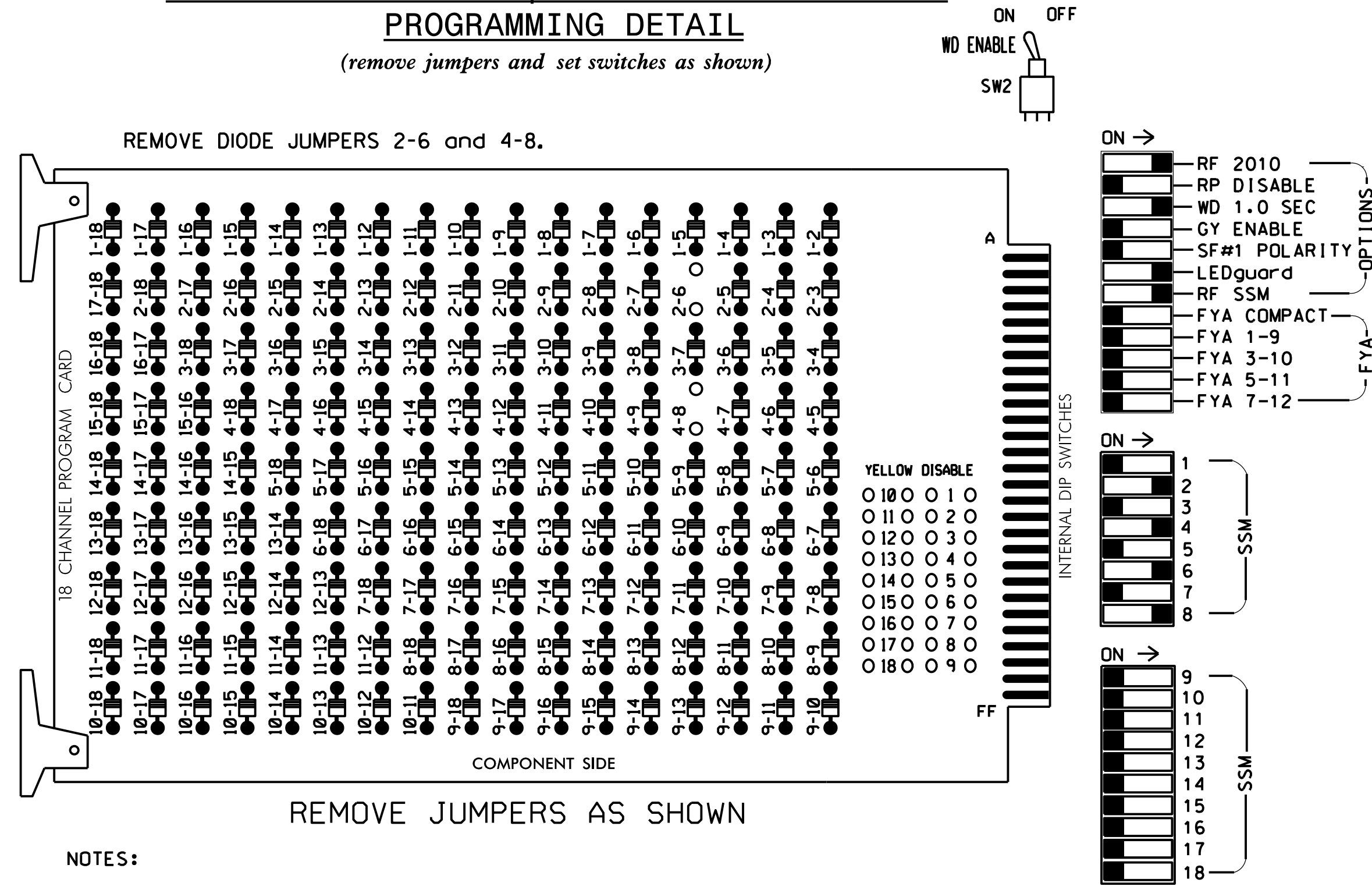


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR  
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.
- Program phases 2 and 6 for Yellow Flash.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	NU	61,62	NU	NU	81,82	NU
RED		128			101			134			107	
YELLOW		129			102			135			108	
GREEN		130			103			136			109	
RED ARROW												
YELLOW ARROW												
GREEN ARROW												

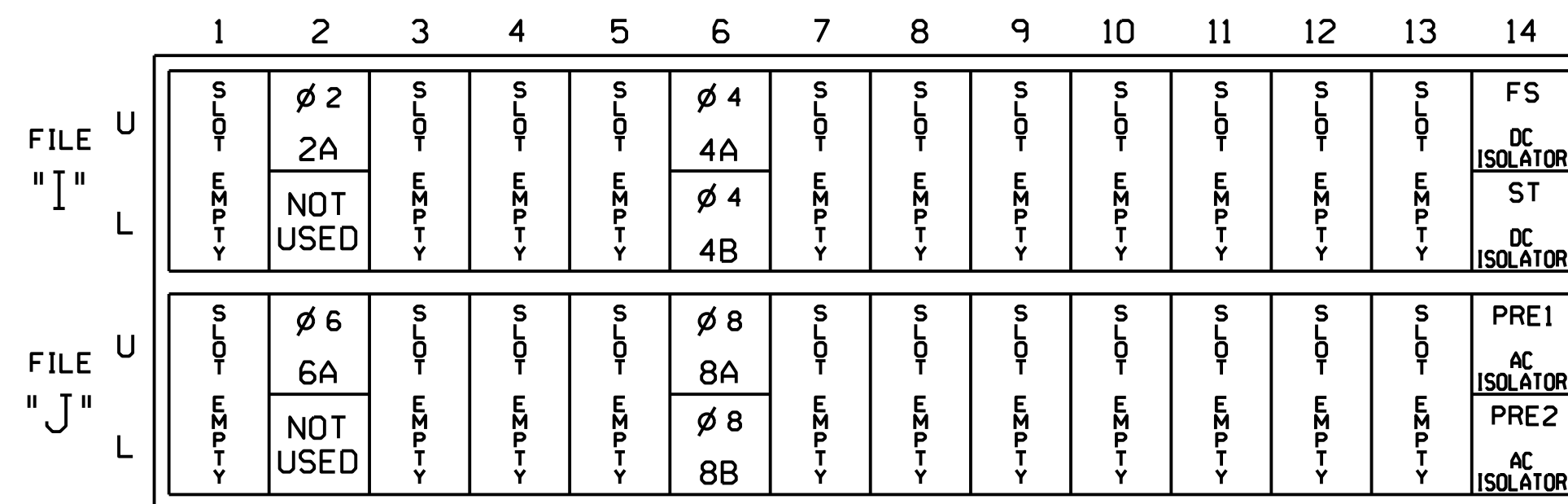
NU = Not Used

EQUIPMENT INFORMATION

CONTROLLER.....SAFETRAN 2070E  
CABINET.....SAFETRAN 332  
SOFTWARE.....ECONOLITE ASC/3-2070  
CABINET MOUNT.....BASE  
OUTPUT FILE POSITIONS...12  
LOAD SWITCHES USED.....S2,S5,S8,S11  
PHASES USED.....2,4,6,8  
OVERLAPS.....NONE

INPUT FILE POSITION LAYOUT

(front view)



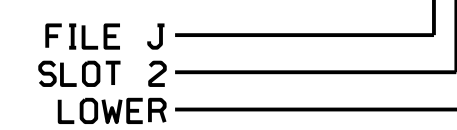
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
ST = STOP TIME  
PRE = PREEMPT

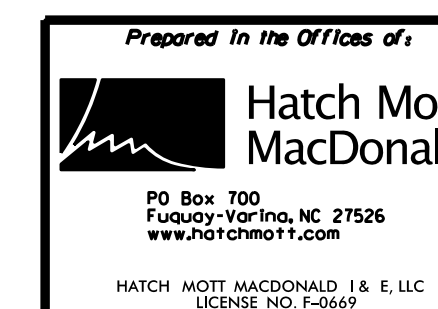
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PI/N NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
2A	TB2-5,6	I2U	39	2	2	YES			S
4A	TB4-9,10	I6U	41	4	4	YES			S
4B	TB4-11,12	I6L	45	14	4	YES		10	S
6A	TB3-5,6	J2U	40	6	6	YES			S
8A	TB5-9,10	J6U	42	8	8	YES			S
8B	TB5-11,12	J6L	46	18	8	YES		10	S

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: C013  
DESIGNED: NOVEMBER 2016  
SEALED: 11/17/2016  
REVISED:



HILLSBORO ROAD AT CUMBERLAND STREET	
PLAN DATE: NOVEMBER 2016	REVIEWED BY: RWT
PREPARED BY: BLR	REVIEWED BY:
REVISIONS	INIT. DATE



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