ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VFH OVIP [A] and 'PPLT FYA'

Select ING VEH OVER [A] GIRG FREI FIA
TMG VEH OVLP[A] TYPE: PPLT FYA
PROTECTED LEFT TURN PHASE 1 OPPOSING THROUGH PHASE 2
FLASHING ARROW OUTPUTCH9 ISOLATE DELAY START OF: FYAO.O CLEARANCEO.O ACTION PLAN SF BIT DISABLE
Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'NORMAL'

TMG VEH OVLP...[B] TYPE:NORMAL INCLUDED X X LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1334 DESIGNED: July 2016 SEALED: 10/11/2016 REVISED:

Electrical Detail - Sheet 2 of 2 ELECTRICAL AND PROGRAMMING DETAILS FOR:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Canopy Lane/Reilly Road at Reilly Road

Cumberland County Division 6 Fayetteville

PLAN DATE: October 2016 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY: REVISIONS

INIT. DATE