ECONOLITE ASC/3-2070 OUTPUT PIN REMAPPING TO PRODUCE THE FYA 31 COMPACT MODE SEQUENCE

The ASC/3 Configurator utility program must be used to remap the output pins as shown below. Consult the ASC/3 Configurator User Guide for specific instructions on software use.

1. Run the Configurator utility. Load a file as the Current DB.

2. Choose the C1-out tab to change the Output mapping. Use the drop down list within the program to select the assigned function for the pins shown below.

3. Save the database file and download it to the controller.

C1   DEFAULT
PIN #   FUNCTION   ASSIGNED FUNCTION

PIN 34-PHASE 5 GREEN ———→ | LS 15 YELLOW PC ▼ | NOTE: FOR FYA 5-11
PIN 36-PED 6 YELLOW ———→ | LS 5 GREEN WALK ▼ | COMPACT MODE

NOTE: The steps below can be used to view changes to Output pins within the controller. Any Output pins that have been remapped will display and show their default function in addition to the current assigned function.

1. From Main Menu select | 7. STATUS DISPLAY |

2. From STATUS DISPLAY Submenu select | 8. INPUTS/OUTPUTS |

3. From INPUT/OUTPUT Submenu select | 9. I/O DIFFERENCES |

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL
(program controller as shown)

1. From Main Menu select | 2. CONTROLLER |

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |

Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: ..... PPLT FYA

PROTECTED LEFT TURN.... OVERLAP    M
OPPOSING THROUGH.......    PHASE   6

FLASHING ARROW OUTPUT.....CH15 YEL PED

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

Toggle to advance
to Overlap 'M'

OVERLAP M

Select TMG VEH OVLP [M] and 'NORMAL'

```
TMG VEH OVLP...[M] TYPE: ...... NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

END PROGRAMMING

ECONOLITE ASC/3-2070 PED 3 DETECTOR PROGRAMMING AND LOAD SWITCH PROGRAMMING DETAIL
(program controller as shown)

1. From Main Menu select | 6. DETECTORS |

2. From DETECTOR Submenu select | 3. PED DETECTOR INPUT ASSIGNMENT |

```
PED DET PHASE ASSIGNMENT MODE:  NTCIP

    PHASE   1  2  3  4  5  6  7  8
  DETECTOR  0  2  8  4  0  6  0  0   ←── NOTICE PED DETECTOR 8
                                         ASSIGNED TO PHASE 3
    PHASE   9 10 11 12 13 14 15 16
  DETECTOR  0  0  0  0  0  0  0  0
```

Program LD SWITCH 5 as OVLP '13' TYPE 'O' and LD SWITCH 16 as PHASE '3' TYPE 'P' as shown below.
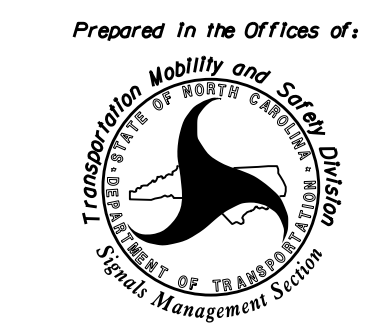
1. From Main Menu select | 1. CONFIGURATION |

2. From CONFIGURATION Submenu select | 3. LOAD SW ASSIGN |

LD SWITCH ASSIGN

	PHASE /OVLP	TYPE	DIMMING R	Y	G	D	FLASH PWR	AUT	TGR
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	R	.
3	3	V	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	13	O	.	.	.	−	A	Y	.
6	6	V	.	.	.	−	A	Y	X
7	7	V	.	.	.	−	A	R	.
8	8	V	.	.	.	−	A	R	X
9	1	O	.	.	.	+	A	R	X
10	2	O	.	.	.	+	A	R	X
11	3	O	.	.	.	−	A	R	.
12	4	O	.	.	.	−	A	R	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	−	A	.	.
15	6	P	.	.	.	+	A	.	.
16	3	P	.	.	.	−	A	.	.

NOTICE PHASE 3 PED
ASSIGNED TO LD SWITCH 16 →

END PROGRAMMING

07-OCT-2016_08:24
S:*ITS&SU*ITS Signals*Working Groups*Sig MomArmstrong*061337_sm_ele_xxx.dgn
sormstrong