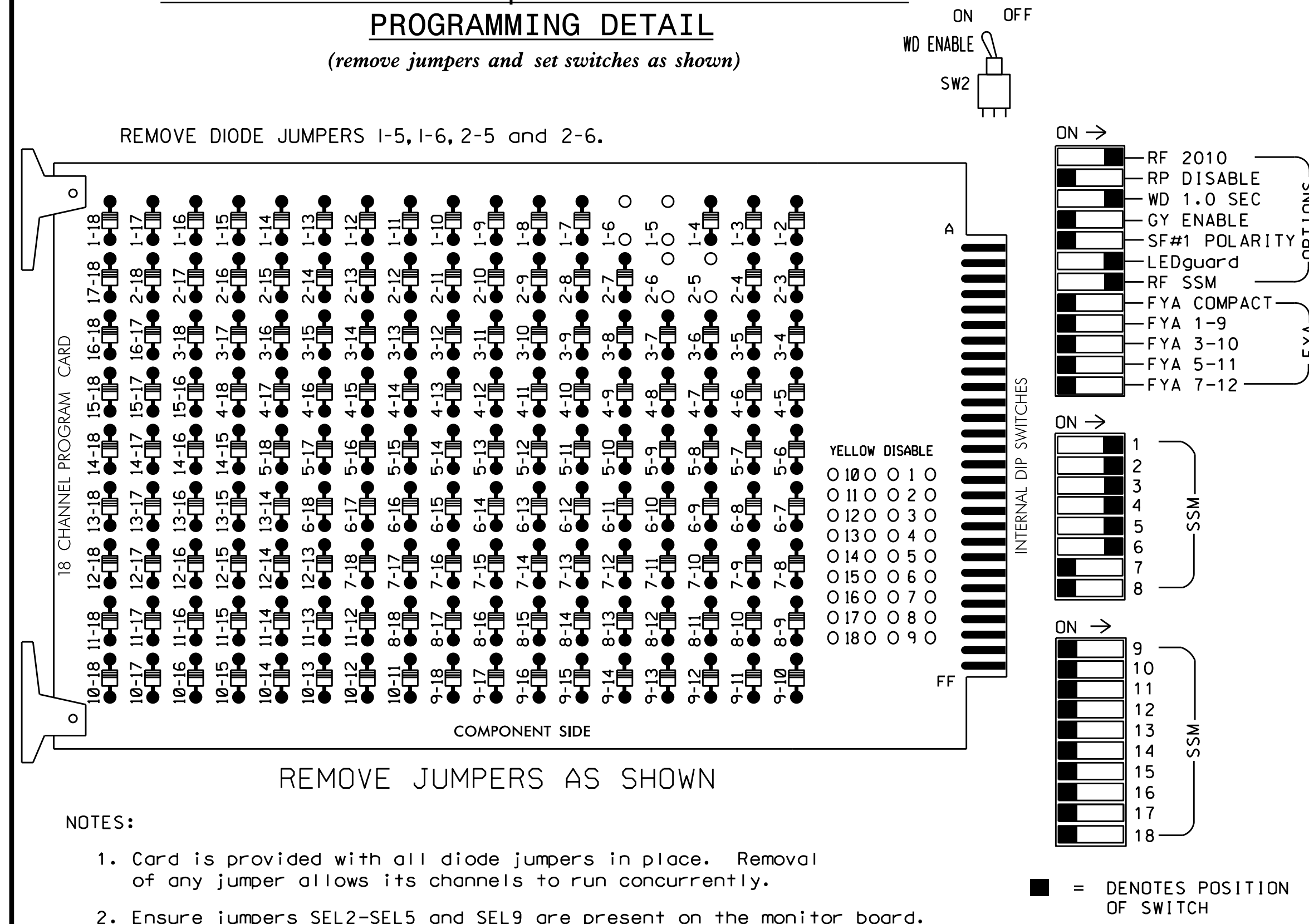


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8  
 PHASES USED.....1,2,3,4,5,6  
 OVERLAPS.....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12					
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16					
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED					
SIGNAL HEAD NO.	11,12	32	21,22 23	NU	31	32	41	42	62	NU	42	51	61,62	NU	NU	NU	NU
RED			128		116	116	101	101						134			
YELLOW			129		117	117	102	102						135			
GREEN			130		118	118	103	103						136			
RED ARROW	125													131			
YELLOW ARROW	126	126						102		132	132						
GREEN ARROW	127	127			118	103	103		133	133							

NU = Not Used

**INPUT FILE POSITION LAYOUT**

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I" UPPER	∅ 1 1A	∅ 1 1B	∅2/SYS 2A/S1	S -OF	∅ 3 3A	∅ 4 4A	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	FS ISOLATOR ST
FILE "I" LOWER	NOT USED	∅ 1 1C	∅2/SYS 2B/S2	S -OF	∅ 3 3B	NOT USED	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	DC ISOLATOR
FILE "J" UPPER	∅ 5 5A	∅ 5 5B	∅6/SYS 6A/S3	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF
FILE "J" LOWER	NOT USED	NOT USED	∅6/SYS 6B/S4	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF	S -OF

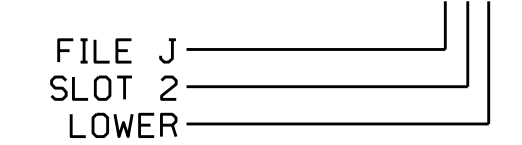
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES			S
1B	TB2-5,6	I2U	39	2	1	YES			S
1C	TB2-7,8	I2L	43	12	1	YES		15	S
2A/S1	TB2-9,10	I3U	63	32	2	YES			N
2B/S2	TB2-11,12	I3L	76	42	2	YES			N
3A	TB4-5,6	I5U	58	3	3	YES			S
3B	TB4-7,8	I5L	58	3	3	YES			S
4A	TB4-9,10	I6U	41	4	4	YES			S
5A	TB3-1,2	J1U	55	5	5	YES			S
5B	TB3-5,6	J2U	40	6	5	YES		15	S
6A/S3	TB3-9,10	J3U	64	36	6	YES			N
6B/S4	TB3-11,12	J3L	77	46	6	YES			N

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1277  
 DESIGNED: January 2016  
 SEALED: 4-28-16  
 REVISED: N/A

Electrical Detail

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

US 401 (South Raeford Road) at SR 3569 (Old Raeford Road) / Two Bale Lane

Division 6 Cumberland County Fayetteville

PLAN DATE: April 2016 REVIEWED BY: DTJ

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal: KEITH M. MINS, PROFESSIONAL ENGINEER, 036880

DocuSigned by: Keith M. Mins 5/24/2016

SIG. INVENTORY NO. 06-1277

24-MAY-2016 09:53  
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 J.peterson