(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu select 8. UTILITIES
- 2. From UTILITIES Submenu select | 1. COPY/CLEAR
- 3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

COPY / CLEAR UTILITY FROM TO PHASE TIMING.... > PHASE TIMING.... TIMING PLAN..... > TIMING PLAN..... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

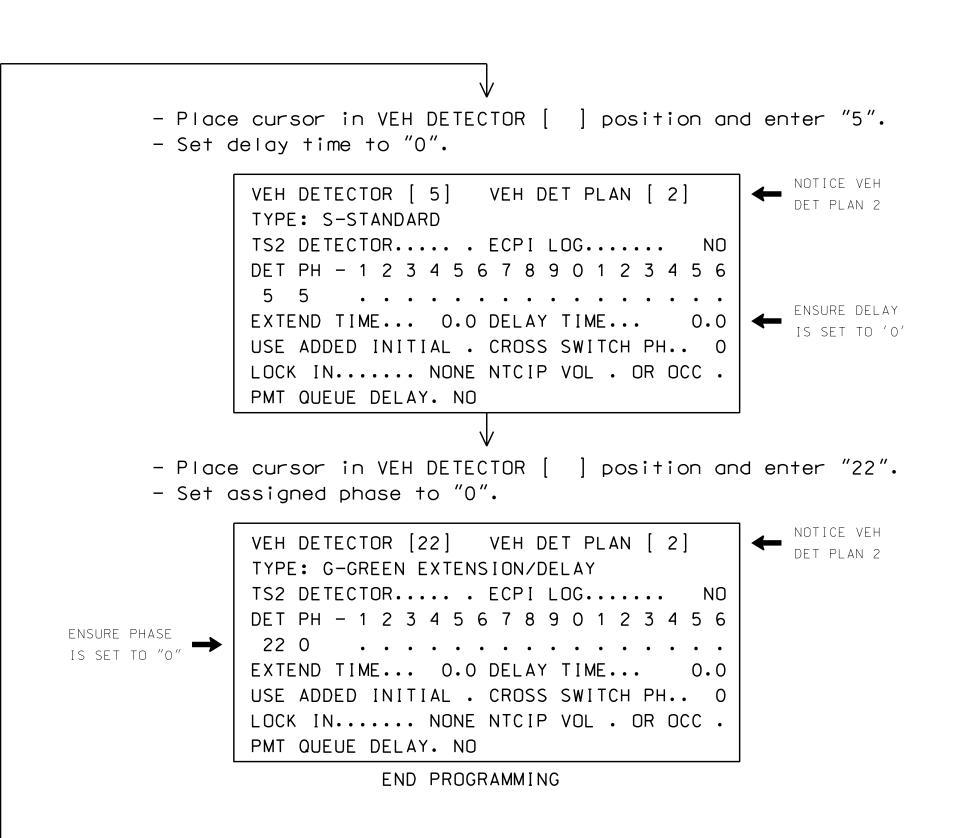
- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [] position and enter "2".
- Place cursor in VEH DETECTOR [] position and enter "1". - Set delay time to "0".

VEH DETECTOR [1] VEH DET PLAN [2] TYPE: S-STANDARD TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE DELAY EXTEND TIME... 0.0 DELAY TIME... 0.0 IS SET TO 'O'

USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [] position and enter "26". - Set assigned phase to "0".

VEH DETECTOR [26] VEH DET PLAN [2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO "O" EXTEND TIME... O.O DELAY TIME... O.O USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO



PROJECT REFERENCE NO.

U-5742

Sig 210 2

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE:PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 1 Toggle Twice

OVERLAP C

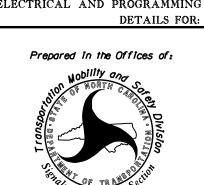
Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 5 ← NOTICE ACTION

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1224 DESIGNED: December 2015 SEALED: 8-24-16 REVISED: N/A





US 401 (Ramsey Street)

Lowe's Entrance Favetteville PLAN DATE: August 2016 REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL

UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 06-1224

Division 6 PREPARED BY: James Peterson | REVIEWED BY:

REVISIONS INIT. DATE