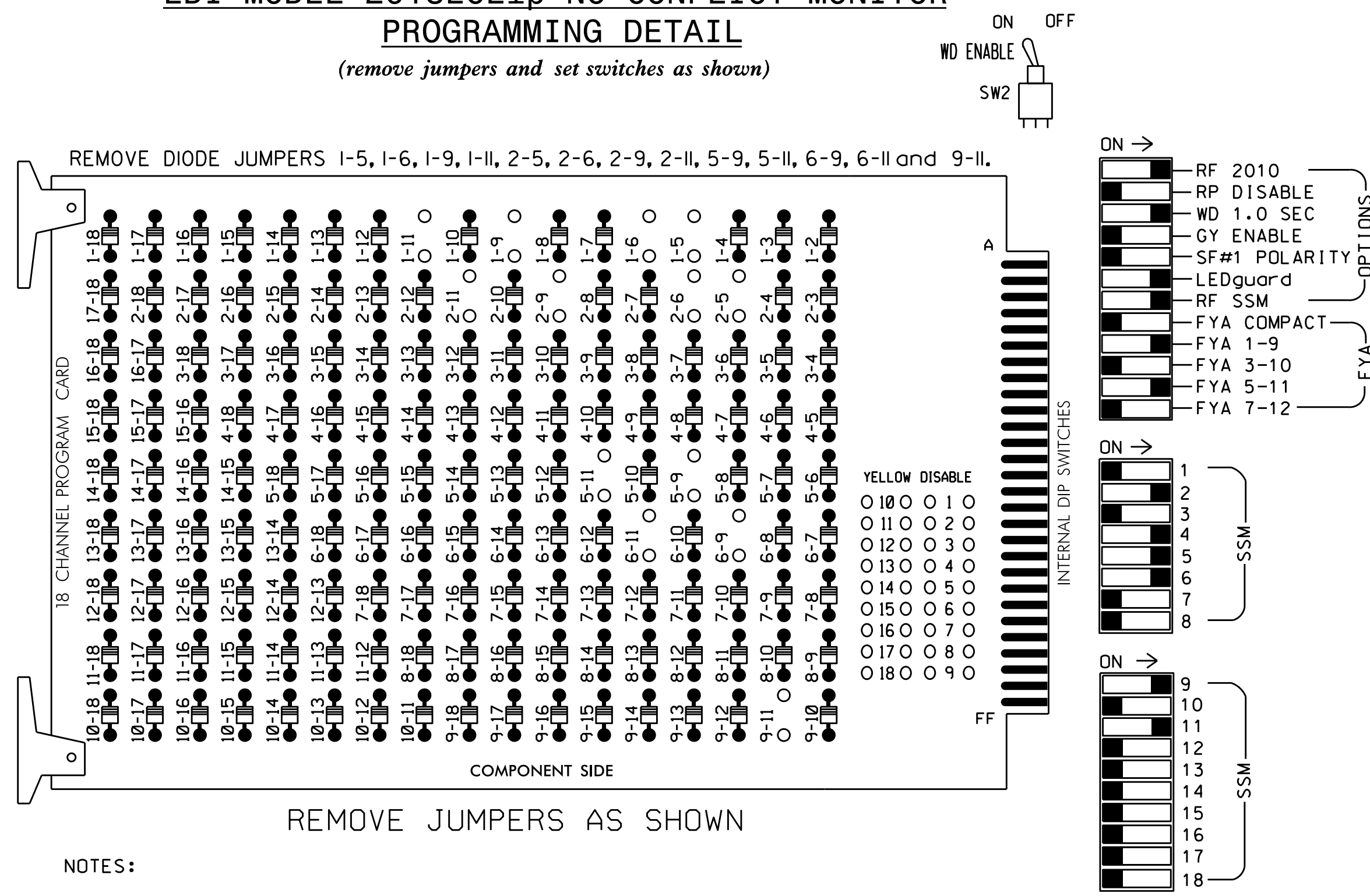


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,AUX S1,AUX S4
 PHASES USED.....1,2,4,5,6
 OVERLAP "A".....*
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED
 * See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	NU	41,42	NU	42	51	61,62 63	NU	NU	NU	11	NU	NU	51	NU	NU
RED		128			101			*	134									
YELLOW	*	129			102				135									
GREEN		130			103				136									
RED ARROW													A121				A114	
YELLOW ARROW							132						A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127							133	133									

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	FS
L	1A	2A,2B	∅ 3	4A	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	DC ISOLATOR
U	NOT USED	NOT USED	∅ 3	4B	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	DC ISOLATOR
L	NOT USED	NOT USED	∅ 3	4B	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

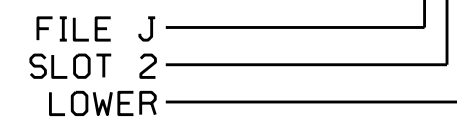
⊗ Wired Input - Do not populate slot with detector cord

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A ¹	TB2-1,2	I1U	56	1 *	1	YES		15	S
	-	J4U	48	26 *	6	YES		3	G
2A,2B	TB2-5,6	I2U	39	2	YES				N
4A	TB4-9,10	I6U	41	4	YES			3	S
4B	TB4-11,12	I6L	45	14	YES				S
5A ²	TB3-1,2	J1U	55	5 *	5	YES		15	S
	-	I4U	47	22 *	2	YES		3	G
5B	TB3-5,6	J2U	40	6	YES			15	S
6A,6B,6C,6D	TB3-9,10	J3U	64	36	YES				N

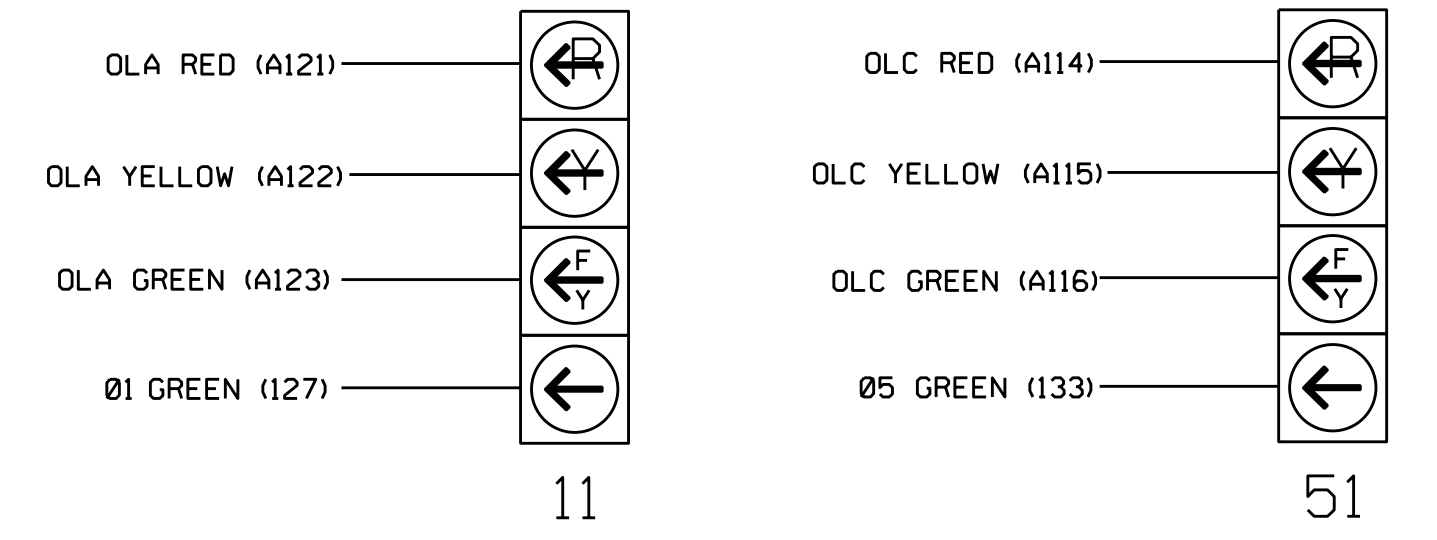
- Add jumper from I1-W to J4-W, on rear of input file.
 - Add jumper from J1-W to I4-W, on rear of input file.
- * See vehicle detector setup programming detail for alternate phasing on sheet 2.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

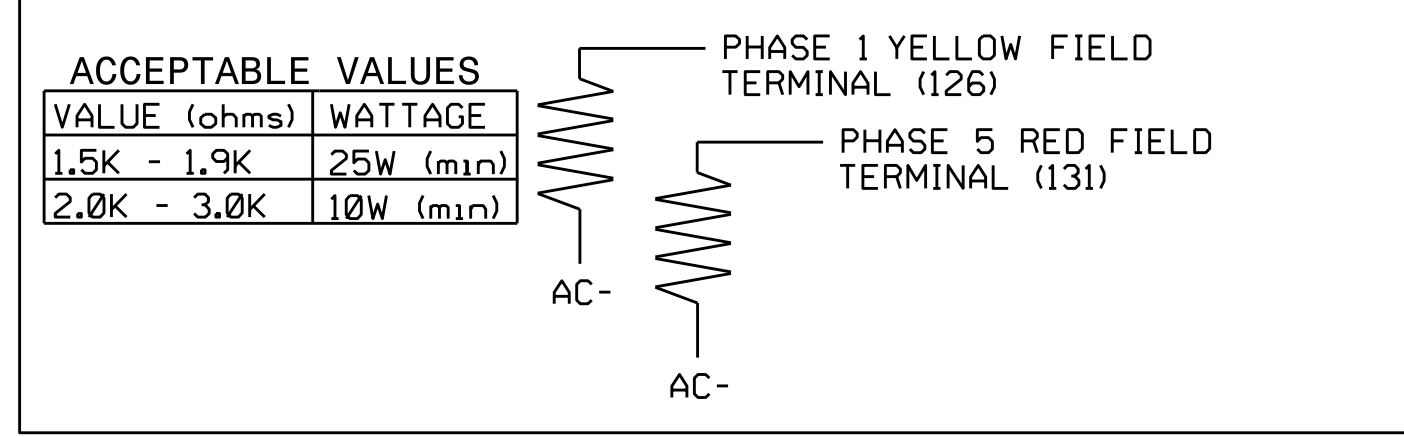
(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1224
 DESIGNED: December 2015
 SEALED: 8-24-16
 REVISED: N/A

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



Electrical Detail Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
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US 401 (Ramsey Street) at Lowe's Entrance

Division 6 Cumberland County Fayetteville

PLAN DATE: August 2016 REVIEWED BY: BAS

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: Keith M. Mims 9/20/2016

750 N. Greenfield Pkwy, Garner, NC 27529

SIG. INVENTORY NO. 06-1224

18-SEP-2016 14:36 S:\TITS\501\TIS - Signal\work\proj\061224_sm.ele_xxx.dgn J.peterson