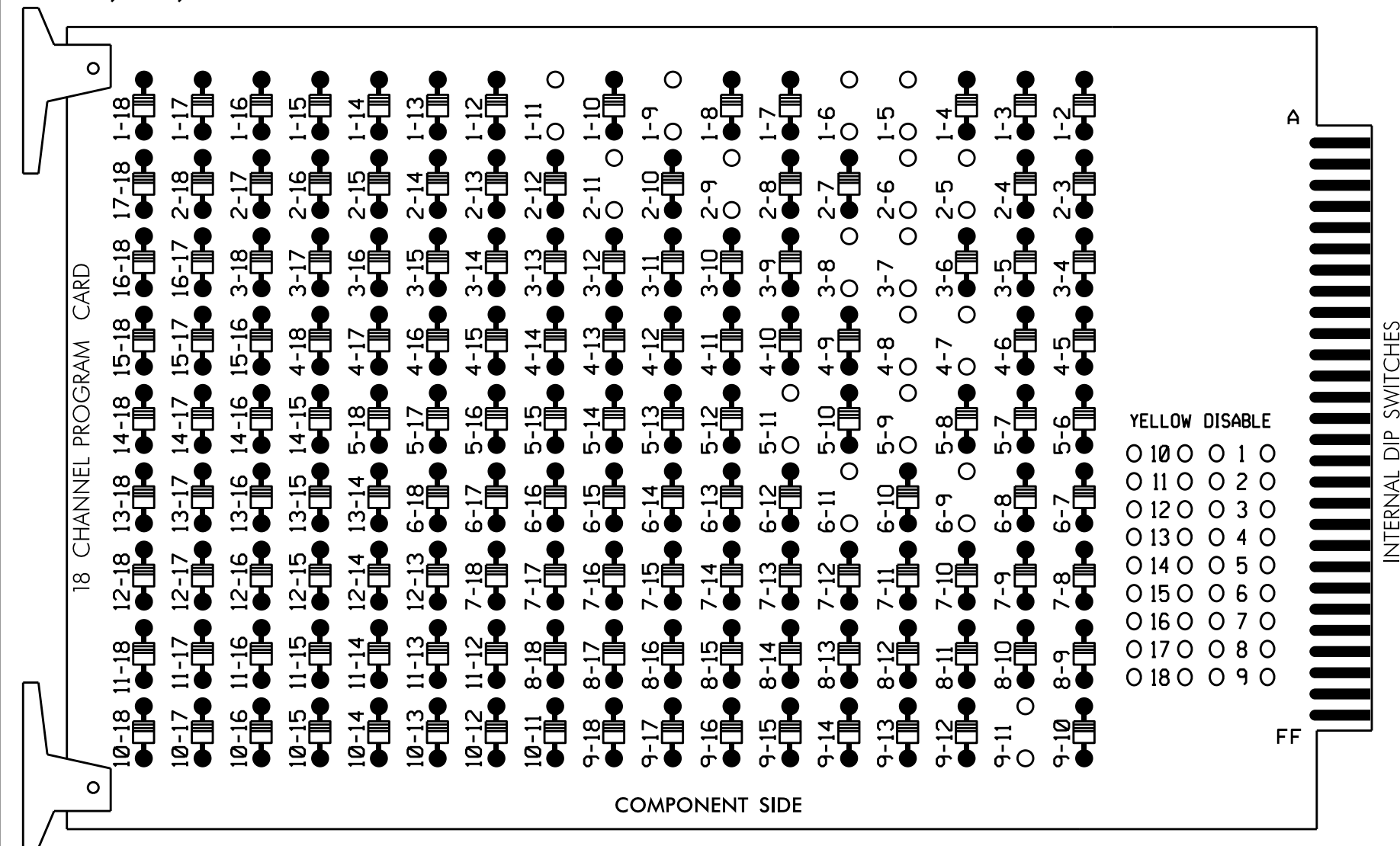


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

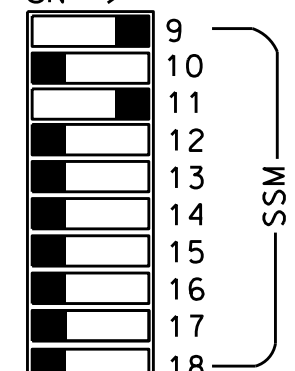
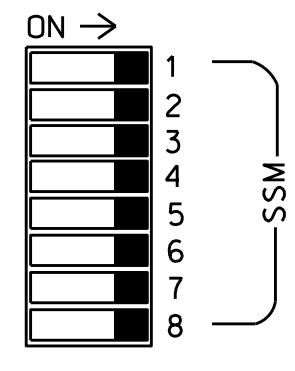
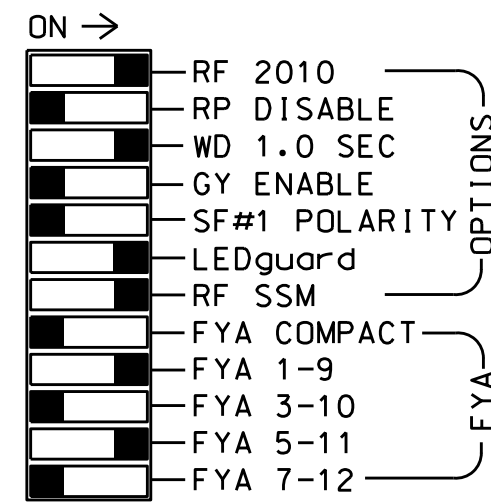
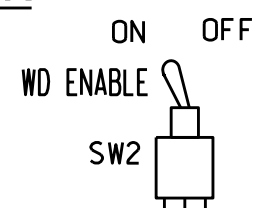
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 4-7, 4-8, 5-9, 5-11, 6-9, 6-11 and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 W/AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,  
 AUX S1,AUX S4  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAP "A".....\*  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....\*  
 OVERLAP "D".....NOT USED  
 \* See overlap programming detail on sheet 2

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	82	21,22	NU	31,32	41,42	NU	42	51	61,62	NU	62	71,72	81,82	NU	11	NU	51	NU
RED		*	128			101		*		134			107						
YELLOW			129			102				135			108						
GREEN			130			103				136			109						
RED ARROW						116						122			A121			A114	
YELLOW ARROW			126			117				132		123	123		A122			A115	
FLASHING YELLOW ARROW															A123			A116	
GREEN ARROW	127	127				118			133	133		124	124						

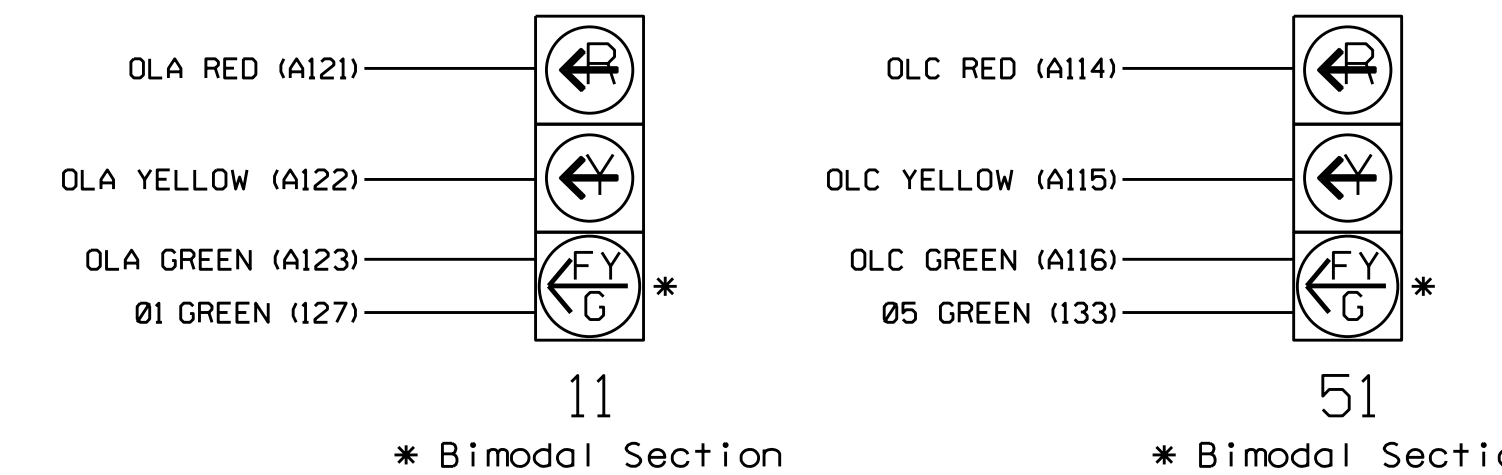
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail sheet 2.

★ See pictorial of head wiring in detail below.

**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

- The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

**INPUT FILE POSITION LAYOUT**

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	Ø 1	Ø 1	Ø 3	Ø 3	Ø 3	Ø 3	Ø 3	Ø 3	Ø 3	Ø 3	Ø 3	Ø 3	Ø 3	FS
I	1A	1B	3A	3B	3C	3D	3E	3F	3G	3H	3I	3J	3K	DC ISOLATOR
L	NOT USED	Ø 2	NOT USED	Ø 4	Ø 4	Ø 4	Ø 4	Ø 4	Ø 4	Ø 4	Ø 4	Ø 4	Ø 4	DC ISOLATOR
U	Ø 5	Ø 5	Ø 7	Ø 7	Ø 7	Ø 7	Ø 7	Ø 7	Ø 7	Ø 7	Ø 7	Ø 7	Ø 7	S
J	5A	5B	7A	7B	7C	7D	7E	7F	7G	7H	7I	7J	7K	ST
L	NOT USED	Ø 6	NOT USED	Ø 8	Ø 8	Ø 8	Ø 8	Ø 8	Ø 8	Ø 8	Ø 8	Ø 8	Ø 8	Y

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

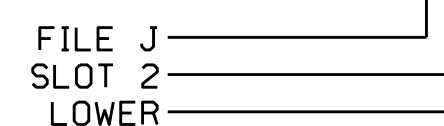
**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A <sup>1</sup>	TB2-1,2	J1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES		3	G
1B	TB2-5,6	J2U	39	2	1	YES		15	S
2A,2B	TB2-7,8	J2L	43	12	2	YES	0.9		N
3A	TB4-5,6	J5U	58	3	3	YES		3	S
3B	TB4-9,10	J6U	41	4	3	YES			S
4A	TB4-11,12	J6L	45	14	4	YES			S
5A <sup>2</sup>	TB3-1,2	J1U	55	5	5	YES		15	S
	-	J4U	47	22	2	YES		3	G
5B	TB3-5,6	J2U	40	6	5	YES		15	S
6A,6B	TB3-7,8	J2L	44	16	6	YES	0.9		N
7A	TB5-5,6	J5U	57	7	7	YES		3	S
7B	TB5-9,10	J6U	42	8	7	YES			S
8A	TB5-11,12	J6L	46	18	8	YES			S

<sup>1</sup>Add jumper from J1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to J4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1148  
 DESIGNED: August 2016  
 SEALED: 10/14/2016  
 REVISED:

Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	SR 1415 (Yadkin Road) at Walmart/Walgreens and Federal Credit Union Entrances		SEAL  Cary M. Little ENGINEER STATE OF NORTH CAROLINA LICENSE NO. 030530
	Division 6 Cumberland County Fayetteville PLAN DATE: October 2016 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY:	REVISIONS INIT. DATE DATE	