ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, 3, 5, and 7.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, 3, 5, and 7.

<u>PHAS I NG</u>	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1, 3, 5, 7

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1, 3, 5, AND 7 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,3,5,7: Modifies overlap parent phases

for heads 11, 31, 51, and 71 to

run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A

and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 8 call on loop 3A and reduces delay time for phase 3 call on loop 3A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

Disables phase 4 call on loop 7A and reduces delay time for phase 7 call on loop 7A to 3 seconds.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1131 DESIGNED: June 2016 SEALED: 10/11/2016 REVISED: N/A

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN

ACTION PLA	۸N.	[1]												
PATTERN		SYS	٥v	ERR	IDE		. N	0								
TIMING PLA	MING PLAN 0							SEQUENCE 0								
VEH DETECT		DET	LC	G			NON	Ε								
FLASH							RE	ST.			. N	0				
VEH DET D	IAG	PL	Ν	. 0		PED	DE	T D	IAG	PL	Ν	0				
DIMMING EN	NABI	_E.	•	NO		PR I	OR I	ΤΥ	RET	URN	. N	0				
PED PR RE	TURI	١		NO		QUE	UE	DEL	AY.		. N	0				
PMT COND [DEL	ΔY		NO												
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
PED RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
WALK 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEH RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
MAX RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
CS INH	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
OMIT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
SPC FCT	Χ	•	Χ	•	X	•	Χ	•	(1	-8)						
AUX FCT	•	•	•	(1	-3 X)										
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
LP 1-15	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 46-60	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 91-100	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

Electrical Detail Sheet 4 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR Prepared in the Offices of:

SR 1102 (Gillis Hill Road) SR 1112 (Stoney Point Road)

ivision 6 Cumberland County Fayetteville PLAN DATE: October 2016 REVIEWED BY: BAS PREPARED BY: S. Armstrong Reviewed BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 06-1131