

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

*OVERLAP A*

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: ....PPLT FYA

PROTECTED LEFT TURN.... PHASE 1

OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH9 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0

ACTION PLAN SF BIT DISABLE..... 1

← NOTICE ACTION  
PLAN SF BIT "1"

Toggle Once

*OVERLAP B*

Select TMG VEH OVLP [B] and 'PPLT FYA'

TMG VEH OVLP...[B] TYPE: ....PPLT FYA

PROTECTED LEFT TURN.... PHASE 3

OPPOSING THROUGH..... PHASE 4

FLASHING ARROW OUTPUT....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0

ACTION PLAN SF BIT DISABLE..... 3

← NOTICE ACTION  
PLAN SF BIT "3"

Toggle Once

*OVERLAP C*

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: ....PPLT FYA

PROTECTED LEFT TURN.... PHASE 5

OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0

ACTION PLAN SF BIT DISABLE..... 5

← NOTICE ACTION  
PLAN SF BIT "5"

Toggle Once

*OVERLAP D*

Select TMG VEH OVLP [D] and 'PPLT FYA'

TMG VEH OVLP...[D] TYPE: ....PPLT FYA

PROTECTED LEFT TURN.... PHASE 7

OPPOSING THROUGH..... PHASE 8

FLASHING ARROW OUTPUT....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0

ACTION PLAN SF BIT DISABLE..... 7

← NOTICE ACTION  
PLAN SF BIT "7"

END PROGRAMMING

## FLASHER CIRCUIT MODIFICATION DETAIL

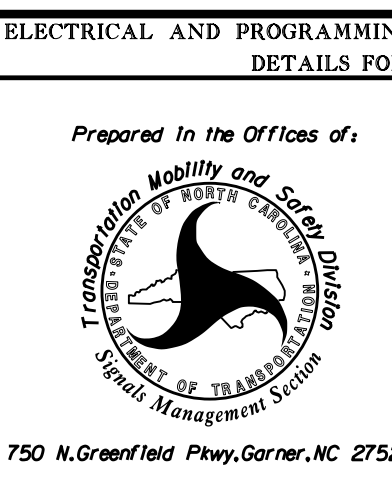

In order to ensure that signals flash concurrently on the same approach, make the following flasher circuit changes:

1. On rear of PDA - remove wire from Term. T2-4 and terminate on T2-2.
2. On rear of PDA - remove wire from Term. T2-5 and terminate on T2-3.
3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 06-1131  
DESIGNED: June 2016  
SEALED: 10/11/2016  
REVISED: N/A

11-007-2016 15:08  
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Electrical Detail Sheet 2 of 4		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED						
	<p style="text-align: center;">SR 1102 (Gillis Hill Road) at SR 1112 (Stoney Point Road)</p> <p style="font-size: x-small;">Division 6 Cumberland County Fayetteville</p> <p>PLAN DATE: October 2016 REVIEWED BY: BAS</p> <p>PREPARED BY: S. Armstrong REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse; font-size: x-small;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>	REVISIONS	INIT.	DATE				<p>SEAL</p>  <p>DocuSigned by: <i>Keith M. Mins</i> 10/12/2016 2F8078E6C3D445 DATE</p> <p style="font-size: x-small;">SIG. INVENTORY NO. 06-1131</p>
REVISIONS	INIT.	DATE						