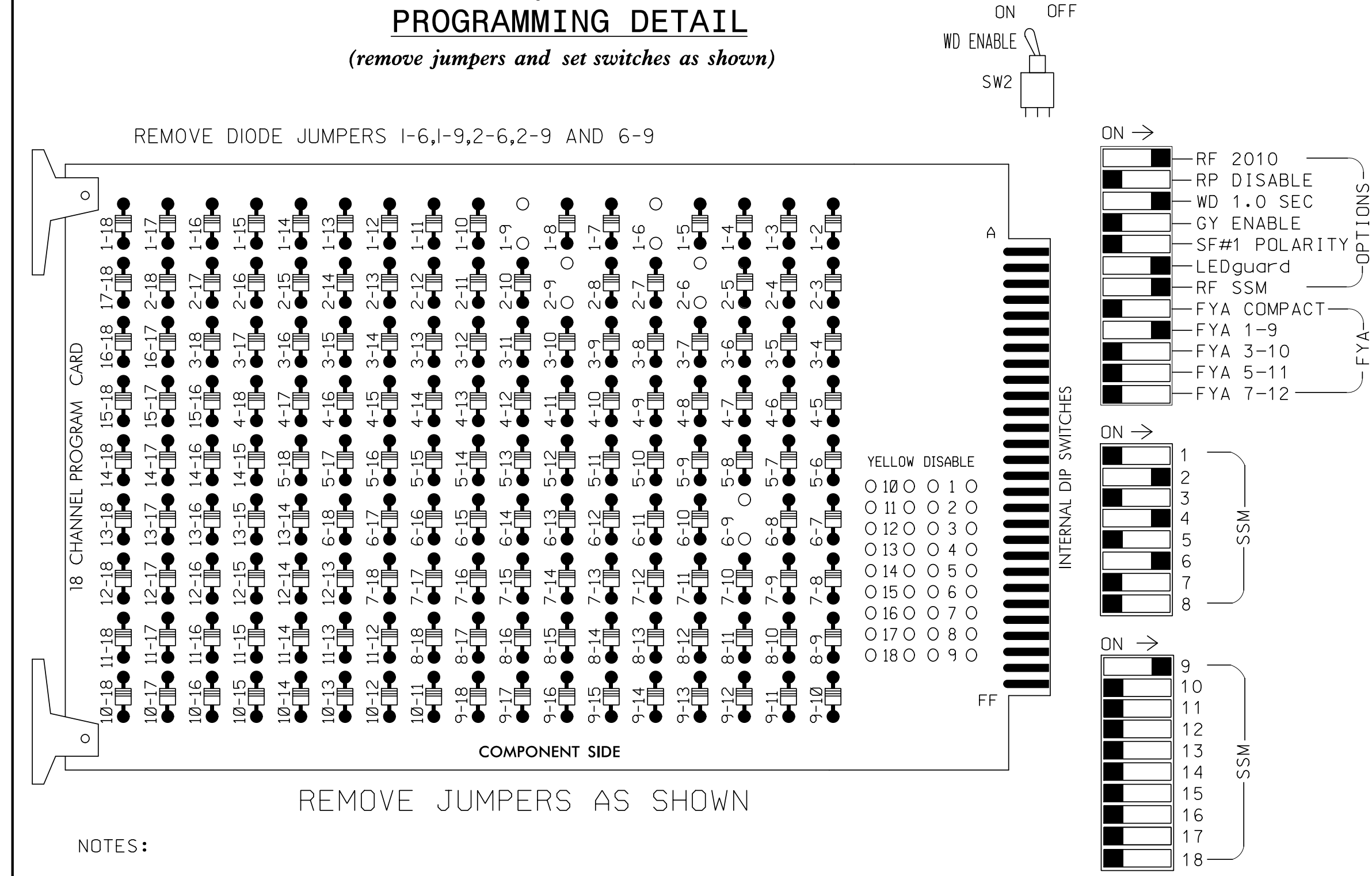


**EDI MODEL 2018EClip-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for volume density operation.
4. Program controller to start up in phase 2 Green and 6 Green.
5. The cabinet and controller are part of the Fayetteville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 W/AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S8,AUX S1  
 PHASES USED.....1,2,4,6  
 OVERLAP "A".....\*  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....NOT USED  
 \* See overlap programming detail this sheet

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22,23	NU	NU	23	41,42	NU	NU	61,62	NU	NU	NU	11	NU	NU	NU	NU	NU
RED	128				101				134									
YELLOW	*	129			102				135									
GREEN		130			103				136									
RED ARROW													A121					
YELLOW ARROW														A122				
FLASHING YELLOW ARROW															A123			
GREEN ARROW	127				103													

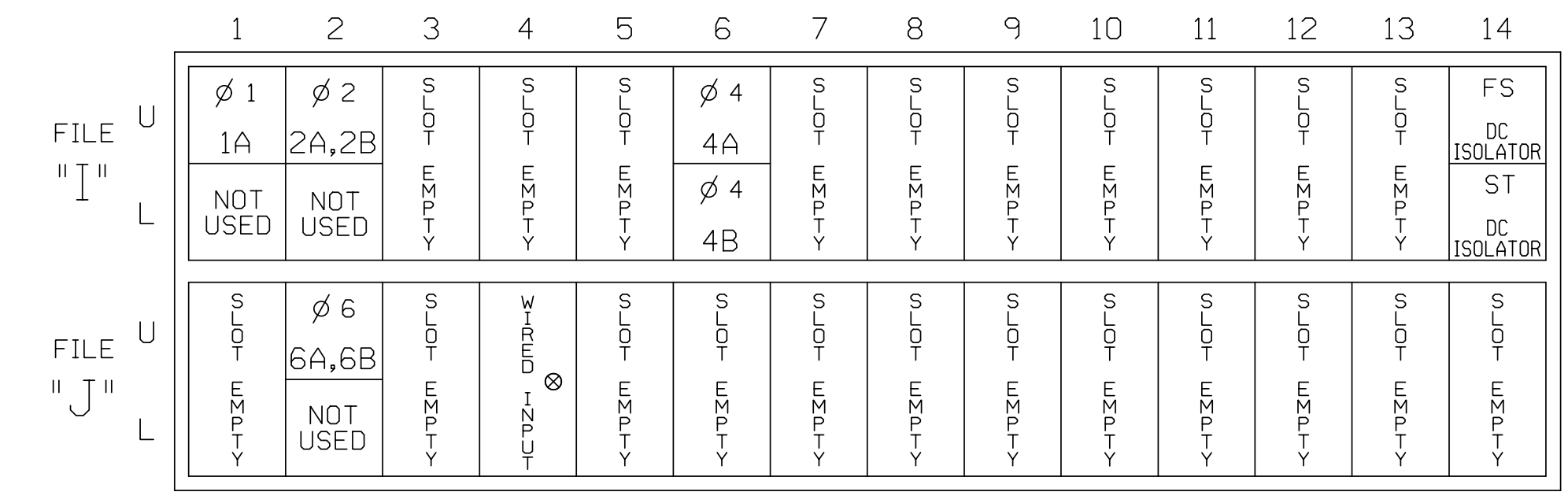
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

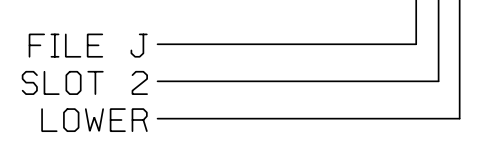
⊗ Wired Input - Do not populate slot with detector card

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A <sup>1</sup>	TB2-1,2	I1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES		3	G
2A, 2B	TB2-5,6	I2U	39	2	2	YES			N
4A	TB4-9,10	I6U	41	4	4	YES			S
4B	TB4-11,12	I6L	45	14	4	YES		15	S
6A, 6B	TB3-5,6	J2U	40	6	6	YES			N

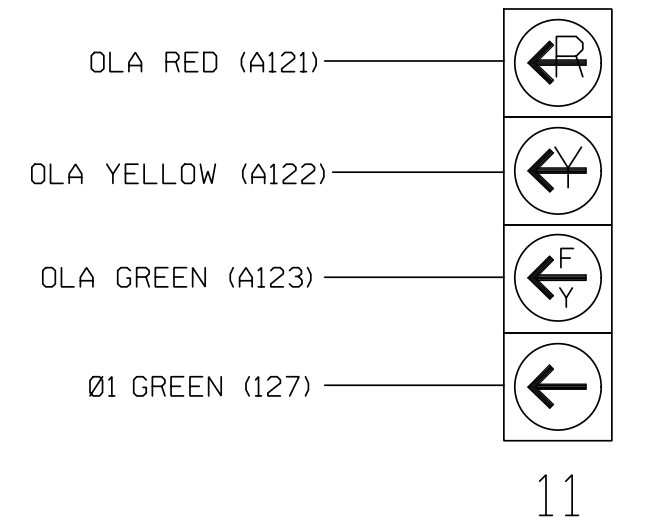
<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

**INPUT FILE POSITION LEGEND: J2L**



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)

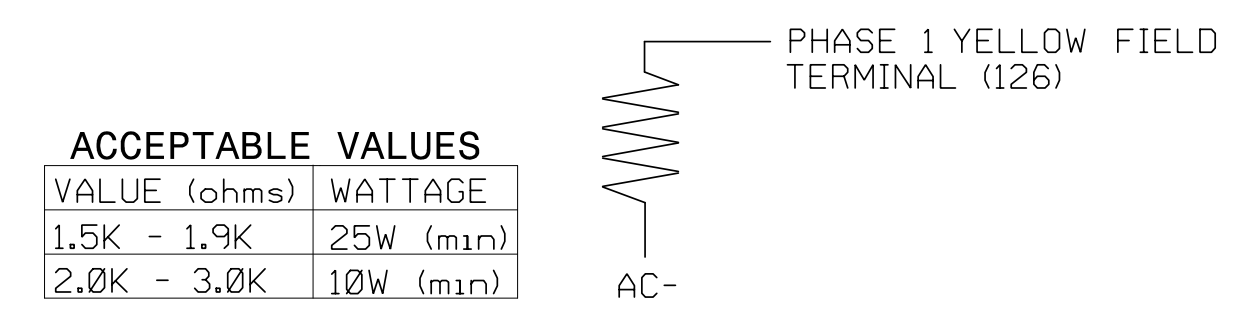


11

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0688  
 DESIGNED: March 2016  
 SEALED: 5/6/2016  
 REVISED:

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown)



**ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL**

(program controller as shown)

1. From Main Menu select **2. CONTROLLER**
2. From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: ....PPLT FYA  
 PROTECTED PHASE (LEFT TURN)..... 1  
 PERMISSIVE PHASE (OPPOSING THRU).... 2  
 FLASHING ARROW OUTPUT.....CH9 ISOLATE  
 DELAY START OF: FYA..0.0 CLEARANCE..0.0  
 ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

**Electrical Detail**

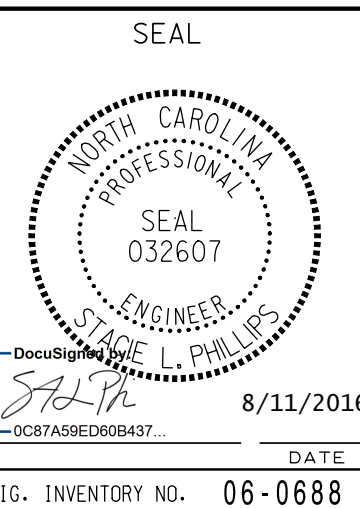
ELECTRICAL AND PROGRAMMING DETAILS FOR:



SR 1404 (Morganton Road)  
 at  
 SR 1458 (Ruritan Drive)

REVISIONS	INIT.	DATE

**DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED**



8/11/2016  
 DATE  
 SIG. INVENTORY NO. 06-0688