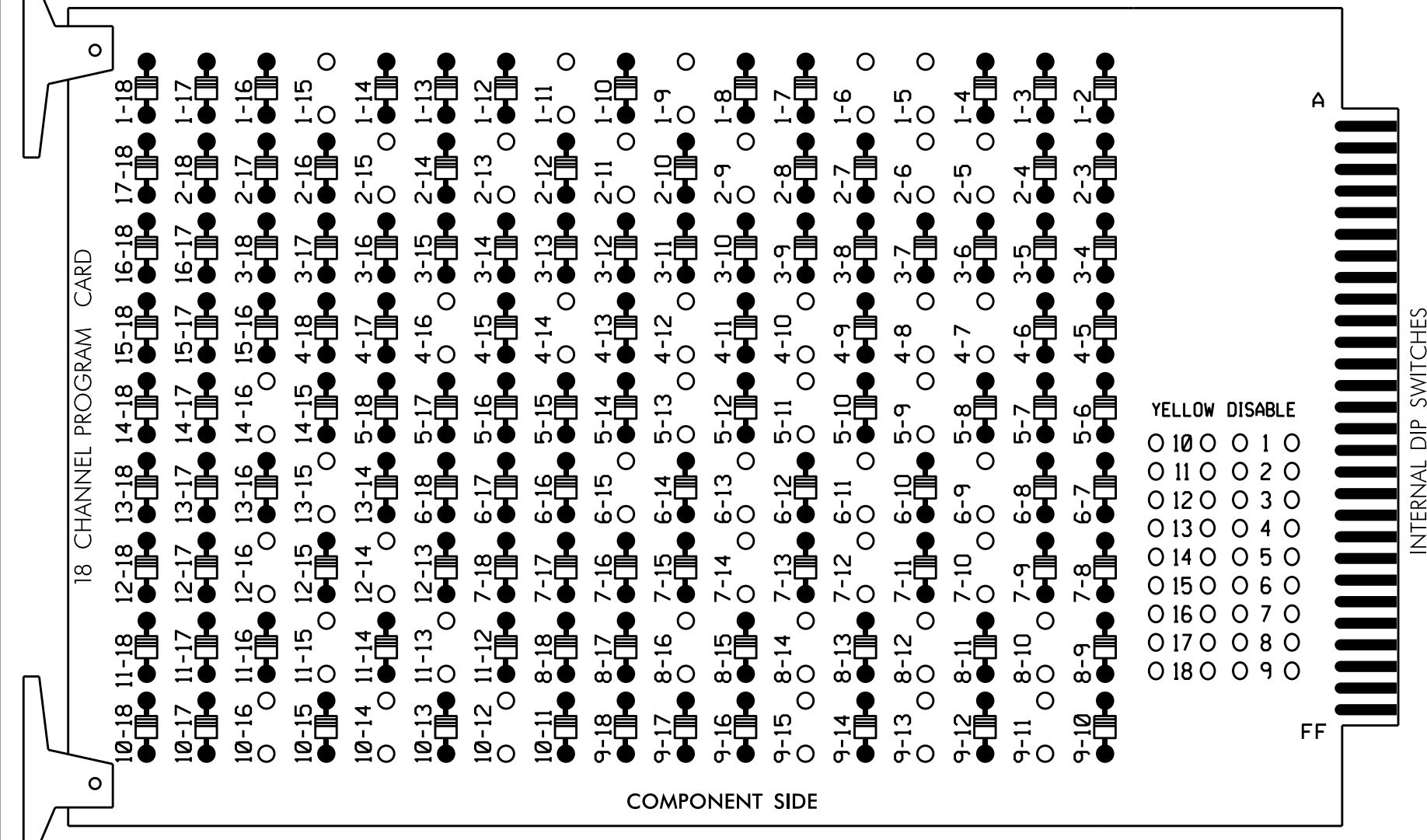


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-7, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phases 2 and 6 Walk.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S10,S11,
 S12,AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,2 PED,4,4 PED,5,6,6 PED,7,
 8,8 PED
 OVERLAP "A".....*
 OVERLAP "B".....*
 OVERLAP "C".....*
 OVERLAP "D".....*
 * See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21, 22,23	P21, P22	NU	41,42	P41, P42	51	61, 62,63	P61, P62	71	81,83	P81, P82	11	81	NU	51	71	NU
RED		128			101			134				107						
YELLOW	*	129			102		*	135		*		108						
GREEN		130			103			136				109						
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127							133			124							
Hand				113		104		119			110							
Walking				115		106		121			112							

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 2	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13
L	1A	2A	2C	3A	4A	5A	6A	7A	8A	9A	10A	11A	12A	13A
U	NOT USED	∅ 2	NOT USED	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13
L	2B	2B	2B	3B	4B	5B	6B	7B	8B	9B	10B	11B	12B	13B

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

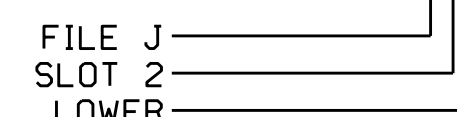
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A ¹	TB2-1,2	I1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES		3	G
2A	TB2-5,6	I2U	39	2	2	YES			N
	TB2-7,8	I2L	43	12	2	YES			N
2C	TB2-9,10	I3U	63	32	2	YES			N
4A	TB4-9,10	I6U	41	4	4	YES		10	S
	-	I4U	47	22	2	YES		3	G
5A ²	TB3-1,2	J1U	55	5	5	YES		15	S
	-	I4U	47	22	2	YES		3	G
6A	TB3-5,6	J2U	40	6	6	YES			N
6B	TB3-7,8	J2L	44	16	6	YES			N
6C	TB3-9,10	J3U	64	36	6	YES			N
7A ³	TB5-5,6	J5U	57	7	7	YES		15	S
	-	I8U	49	24	4	YES		3	S
8A	TB5-9,10	J6U	42	8	8	YES		3	S
8B	TB5-11,12	J6L	46	18	8	YES		10	S
PED PUSH BUTTONS									
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED				
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED				
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED				
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED				

NOTE:

INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- Add jumper from I1-W to J4-W, on rear of input file.
 - Add jumper from J1-W to I4-W, on rear of input file.
 - Add jumper from J5-W to I8-W, on rear of input file.
- * See vehicle detector setup programming detail for alternate phasing on sheet 2.

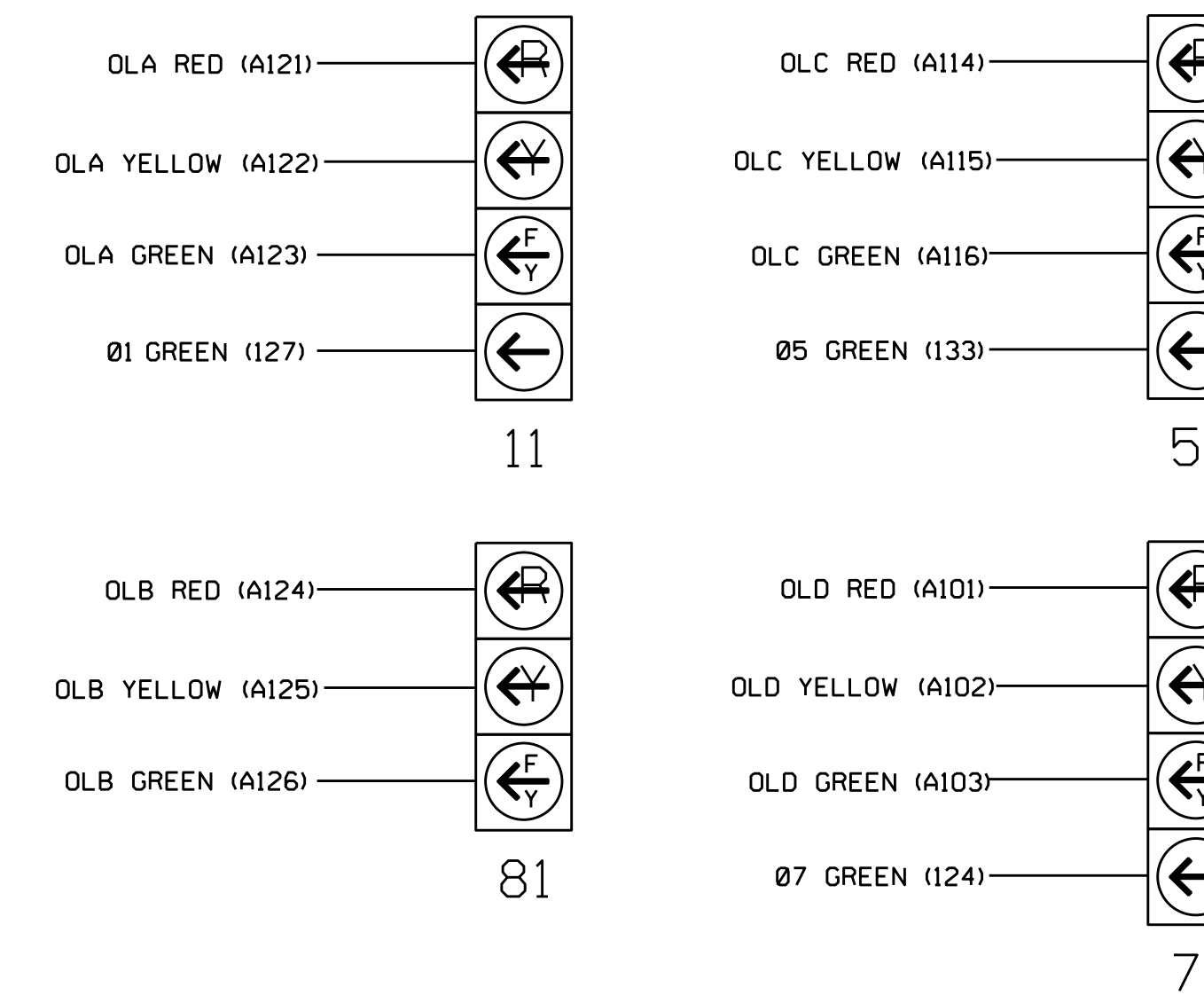
INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0565
 DESIGNED: January 2016
 SEALED: 6/1/2016
 REVISED:

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Electrical Detail - Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
 TRANSPORTATION MOBILITY AND SAFETY SOLUTIONS
 750 N. Greenfield Pkwy, Garner, NC 27529

NC 24-87 (Bragg Boulevard) at Ames Street

Division 6 Cumberland County Fayetteville
 PLAN DATE: May 2016 REVIEWED BY: BAS
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS: _____ INIT. DATE _____

Seal of the State of North Carolina, Professional Engineer, Seal 030530, Cary M. Little, License No. 12174.

DocuSigned by: Cary M. Little 6/8/2016
 DATE: _____
 SIG. INVENTORY NO. 06-0565