ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select [2. CONTROLLER]

2. From CONTROLLER Submenu select [2. VEHICLE OVERLAPS]

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: ....[PPLT FYA]

PROTECTED PHASE (LEFT TURN)......... 1
PERMISSIVE PHASE (OPPOSING THRU).... 2
FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```
                                    Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

```
TMG VEH OVLP...[B] TYPE: ....[PPLT FYA]

PROTECTED PHASE (LEFT TURN)......... 3
PERMISSIVE PHASE (OPPOSING THRU).... 4
FLASHING ARROW OUTPUT.....CH10 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```
                                    Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: ....[PPLT FYA]

PROTECTED PHASE (LEFT TURN)......... 5
PERMISSIVE PHASE (OPPOSING THRU).... 6
FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```
                                    Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'OTHER/ECONOLITE'

```
TMG VEH OVLP...[D] TYPE:[OTHER/ECONOLITE]
       PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . . . X . . . . . . . .
PROTECT  . . . . . . . . . . . . . . . .
PED PRTC . . . . . . . . . . . . . . . .
NOT OVLP . . . . . . . . . . . . . . . .
FLSH GRN . . . . . . . 1 . . . . . . . .
LAG X PH . . . . . . . . . . . . . . . .
LAG 2 PH . . . . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
```

END PROGRAMMING
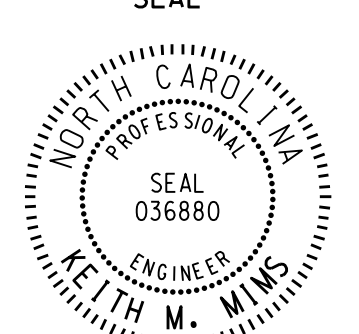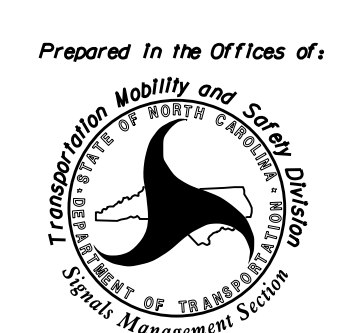
FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the Same approach, make the following flasher circuit changes:

1. On rear of PDA – remove wire from Term. T2-4 and terminate on T2-2.

2. On rear of PDA – remove wire from Term. T2-5 and terminate on T2-3.

3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0542
DESIGNED: October 2015
SEALED: 3/8/2016
REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

SR 1400 (Cliffdale Road)
at
SR 1410 (Bunce Road)/
Eagle Point Apartment Entrance

Prepared In the Offices of:

Transportation Mobility and Safety Division
DEPARTMENT OF TRANSPORTATION
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

Division 6	Cumberland County	Fayetteville
PLAN DATE: March 2016	REVIEWED BY: BAS	
PREPARED BY: S. Armstrong	REVIEWED BY:	

REVISIONS

	INIT.	DATE

SEAL

NORTH CAROLINA
PROFESSIONAL
SEAL
036880
ENGINEER
KEITH M. MIMS

DocuSigned by:
Keith M. Mims   3/18/2016
2F8078EE9CD34A5
DATE

SIG. INVENTORY NO. 06-0542

08-MAR-2016 11:34
S:*ITS&SU*ITS Signals\Working groups\Sig Mon\Armstrong\06-0542_sh_ele_xxx.dgn
sarmstrong