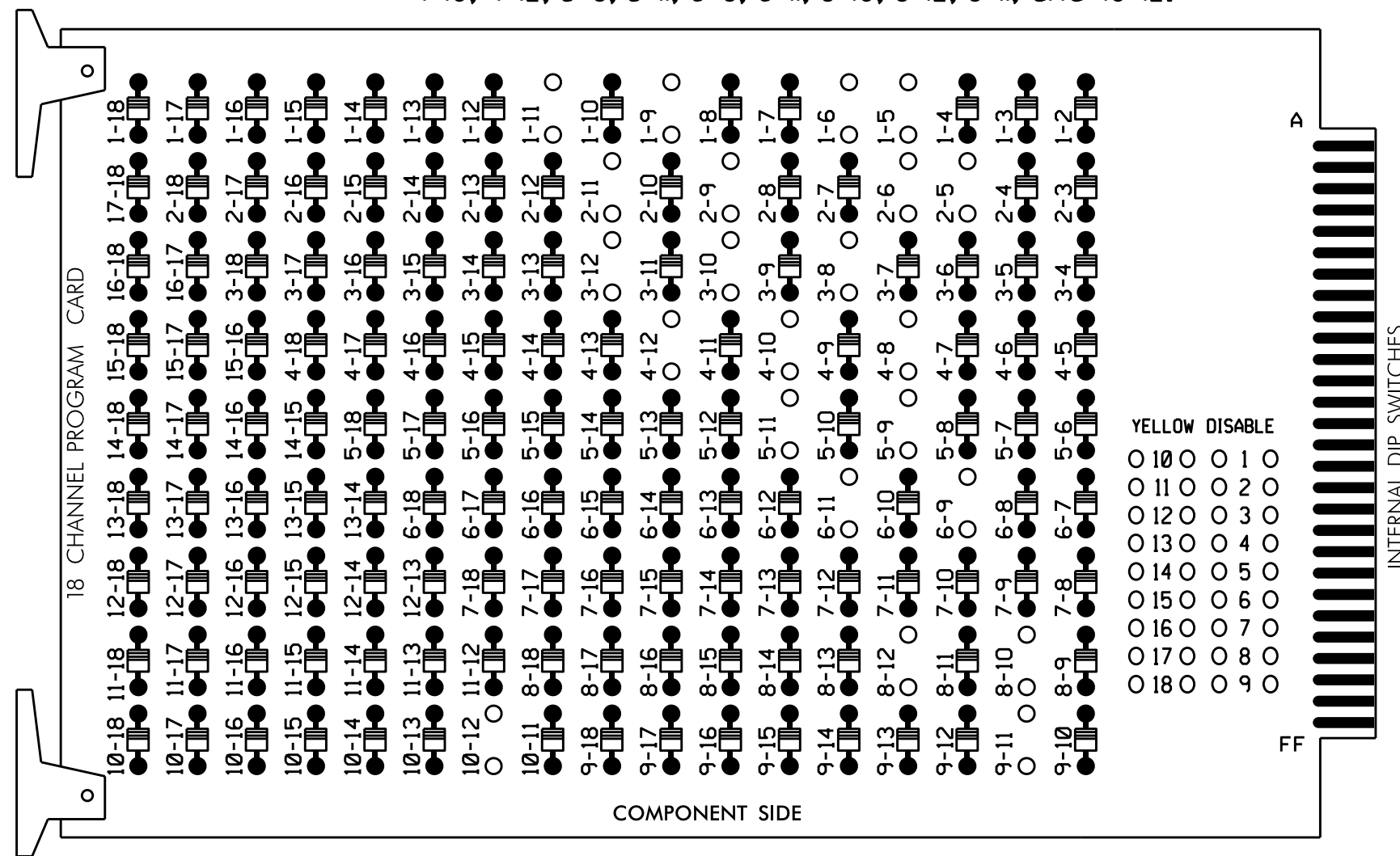


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-8, 3-10, 3-12, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 8-10, 8-12, 9-11, and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S11,
 AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,8
 OVERLAP "A".....*
 OVERLAP "B".....*
 OVERLAP "C".....*
 OVERLAP "D".....*
 * See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6				
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18				
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE				
SIGNAL HEAD NO.	11*	82	21,22	NU	22	31*	42,43	NU	43	51*	61,62	NU	NU	81,82	NU	11*	31*	NU	51*	41*	NU	
RED		*	128		*	101		*	134		107											
YELLOW			129			102			135		108											
GREEN			130			103			136		109											
RED ARROW																A121	A124		A114	A101		
YELLOW ARROW			126			117			132							A122	A125		A115	A102		
FLASHING YELLOW ARROW																A123	A126		A116	A103		
GREEN ARROW	127	127				118	118		133	133												

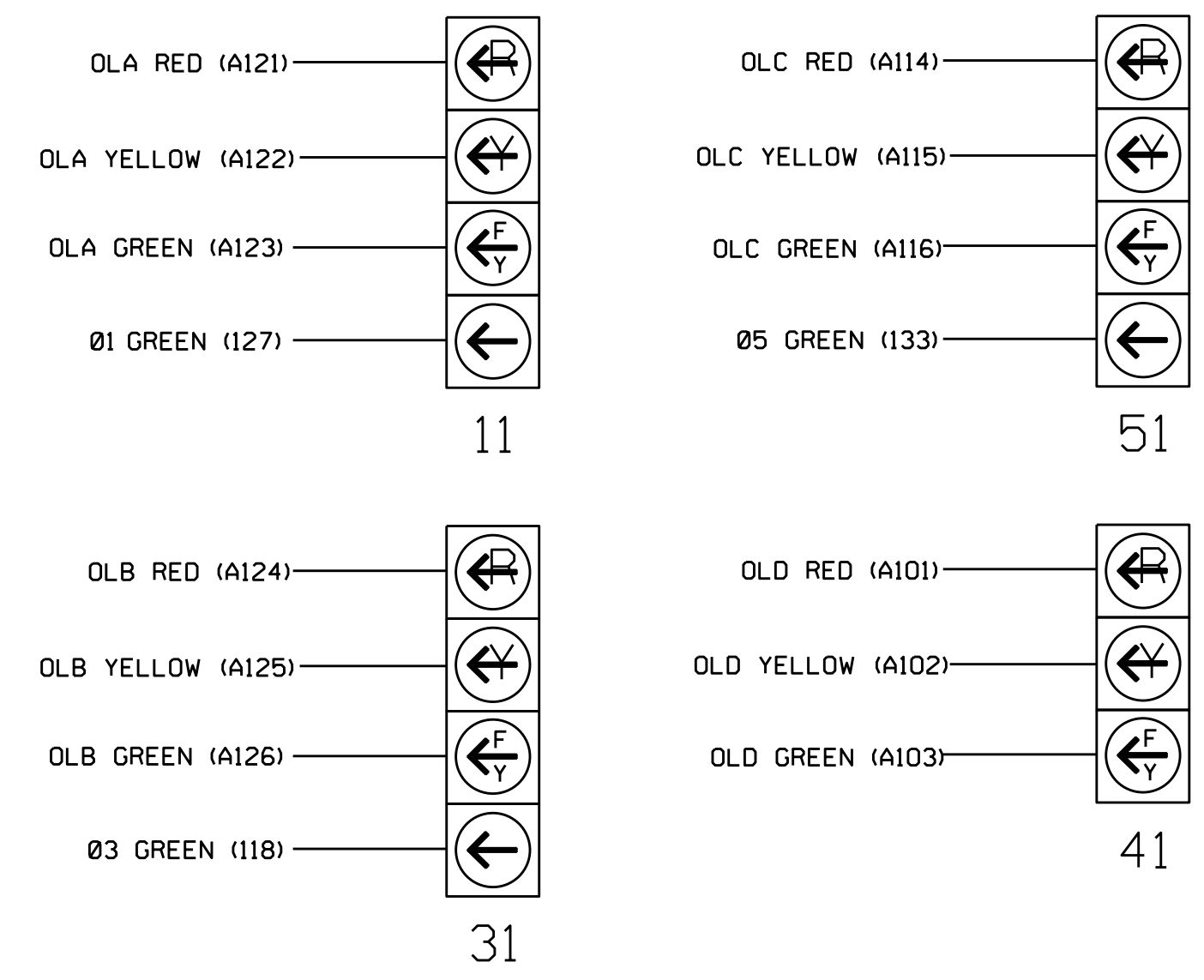
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

FILE	U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	U	∅ 1	∅ 2	-ORS	-ORS	∅ 3	∅ 4	∅ 4	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	FS
	L	1A	2A	NOT USED	NOT USED	3A	4A	4C	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	DC ISOLATOR
"J"	U	∅ 5	∅ 6	-ORS	-ORS	-ORS	∅ 8	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	FS
	L	5A	6A	NOT USED	NOT USED	NOT USED	8A	8B	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

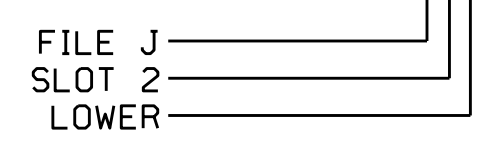
* Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A ¹	TB2-1,2	I1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES		3	G
2A	TB2-5,6	I2U	39	2	2	YES			N
	TB2-7,8	I2L	43	12	2	YES			N
3A ²	TB4-5,6	I5U	58	3	3	YES		15	S
	-	J8U	50	28	8	YES		3	S
4A	TB4-9,10	I6U	41	4	4	YES		3	S
	TB4-11,12	I6L	45	14	4	YES			S
4C	TB6-1,2	I7U	65	34	4	YES		15	S
	TB3-1,2	J1U	55	5	5	YES		15	S
5A ³	-	I4U	47	22	2	YES		3	G
	TB3-5,6	J2U	40	6	6	YES			N
6A	TB3-7,8	J2L	44	16	6	YES			N
	TB5-9,10	J6U	42	8	8	YES			S
8B	TB5-11,12	J6L	46	18	8	YES		15	S

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from I5-W to J8-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.

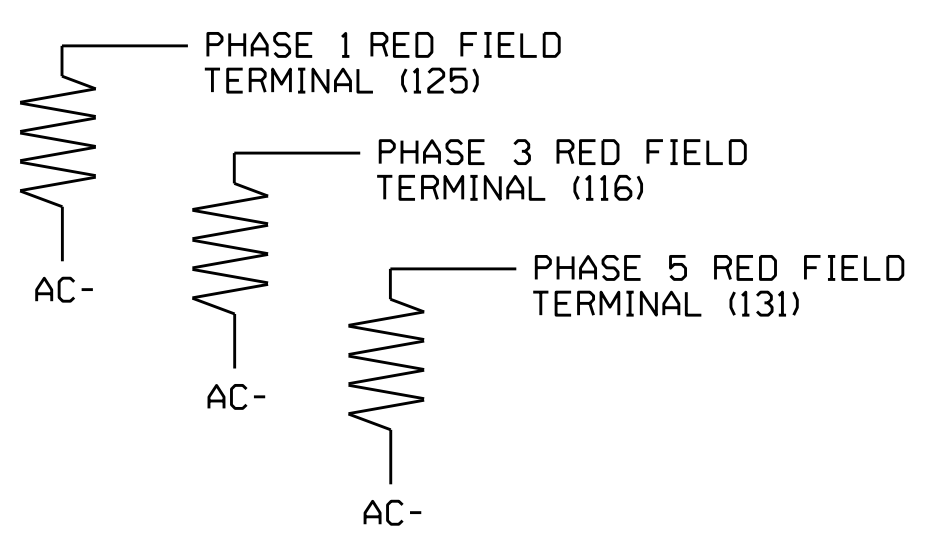
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
 TRANSPORTATION MOBILITY AND SAFETY ADMINISTRATION
 FEDERAL BUREAU OF INVESTIGATION
 Signal Management Section
 750 N. Greenfield Pkwy, Garner, NC 27529

SR 1400 (Cliffdale Road) at SR 1410 (Bunce Road)/Eagle Point Apartment Entrance

Division 6 Cumberland County Fayetteville

PLAN DATE: March 2016 REVIEWED BY: BAS

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS: INIT. DATE

Seal: KEITH M. MINAS, PROFESSIONAL ENGINEER, No. 036880

DocuSigned by: Keith M. Minas 3/18/2016

SIG. INVENTORY NO. 06-0542

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