ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select | 2. CONTROLLER |

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

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TMG VEH OVLP...[A] TYPE: ....|PPLT FYA|

PROTECTED LEFT TURN....    PHASE    1
OPPOSING THROUGH.......    PHASE    2

FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```
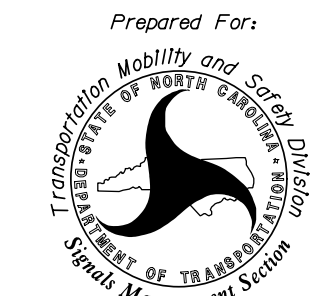
Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

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TMG VEH OVLP...[C] TYPE: ....|PPLT FYA|

PROTECTED LEFT TURN....    PHASE    5
OPPOSING THROUGH.......    PHASE    6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

END PROGRAMMING

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THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0788
DESIGNED: April 2016
SEALED: 7/7/2016
REVISED:
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