

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE:PPLT FYA

PROTECTED PHASE (LEFT TURN)..... 1

PERMISSIVE PHASE (OPPOSING THRU).... 2

FLASHING ARROW OUTPUT.....CH9 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0

ACTION PLAN SF BIT DISABLE..... 0

↓ Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE:PPLT FYA

PROTECTED PHASE (LEFT TURN)..... 5

PERMISSIVE PHASE (OPPOSING THRU).... 6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0

ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

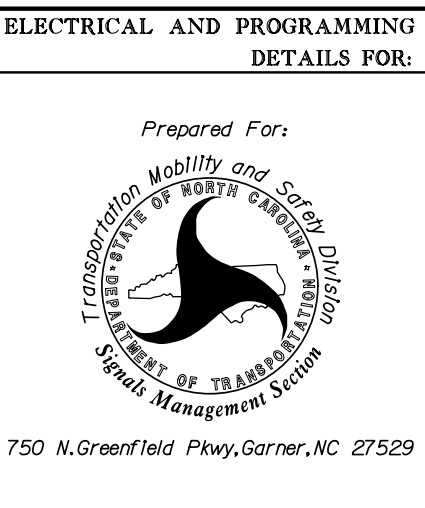
THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 06-0785
 DESIGNED: January 2016
 SEALED: 5/11/2016
 REVISED:

8/11/2016
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 Susan_Pennington

Electrical Detail Sheet 2 of 2

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

PLANS PREPARED IN THE OFFICE OF:
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PLAN DATE: July 2016	REVIEWED BY: KP Baumann
PREPARED BY: SP Pennington	REVIEWED BY: SL Phillips
REVISIONS	INIT. DATE

SEAL SEAL 032607 ENGINEER SL PHILLIPS	DATE 8/11/2016
SIG. INVENTORY NO. 06-0785	