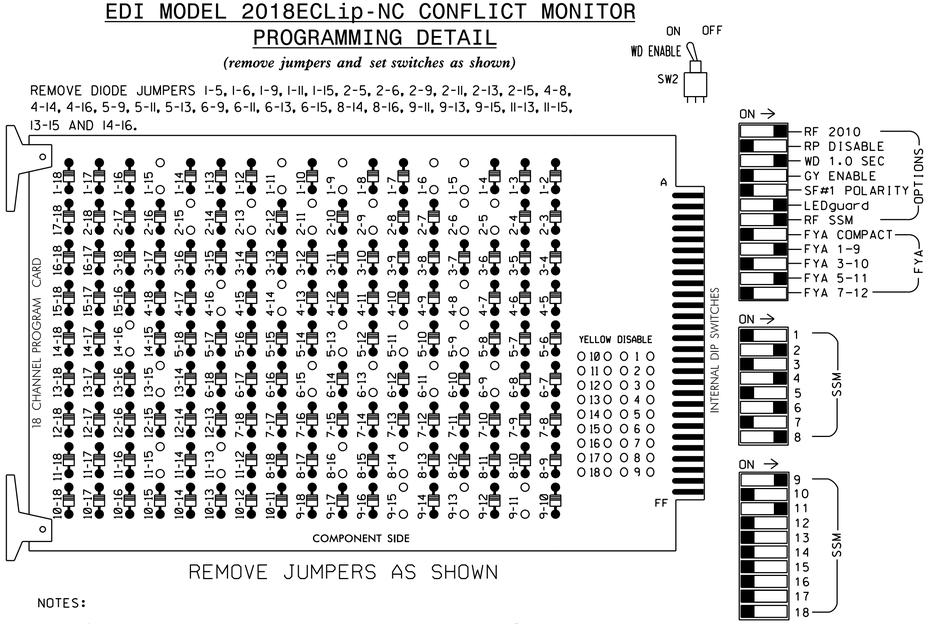
PROJECT REFERENCE NO.	SHEET NO.
U-5742	Sig. 171.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS I-5, I-6, I-9, I-11, I-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 13-15 AND 14-16.



REMOVE JUMPERS AS SHOWN

■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for volume density operation.
5. Program controller to start up in phase 2 Walk and 6 Walk.
6. The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
CABINET.....332 W/AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S11, S12,AUX S1,AUX S4
PHASES USED.....1,2,4,5,6,8,2 PED,4 PED,6 PED,8 PED
OVERLAP "A".....*
OVERLAP "B".....NOT USED
OVERLAP "C".....*
OVERLAP "D".....NOT USED

* See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21,22	P21, P22	NU	41,42	P41, P42	51★	61,62	P61, P62	NU	81,82	P81, P82	11★	NU	NU	51★	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114		
YELLOW ARROW													A122			A115		
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW	127						133											
✋			113		104			119			110							
🚶			115		106			121			112							

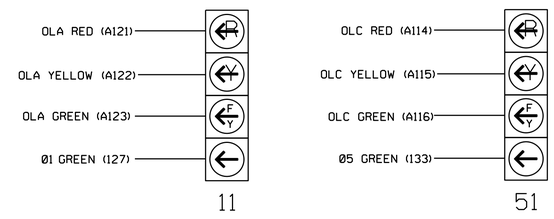
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø2 2A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	Ø4 4A	SLOT EMPTY	SLOT EMPTY ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	Ø6 PED DC ISOLATOR	FS DC ISOLATOR
	L	NOT USED	Ø2 2B				Ø4 4B						Ø4 PED DC ISOLATOR	Ø8 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6 6A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	Ø8 8A	SLOT EMPTY	SLOT EMPTY ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	Ø6 6B				Ø8 8B								

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A [1]	TB2-1,2	I1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES		3	G
2A	TB2-5,6	I2U	39	2	2	YES			N
2B	TB2-7,8	I2L	43	12	2	YES			N
4A	TB4-9,10	I6U	41	4	4	YES		3	S
4B	TB4-11,12	I6L	45	14	4	YES		3	S
5A [2]	TB3-1,2	J1U	55	5	5	YES		15	S
	-	I4U	47	22	2	YES		3	G
6A	TB3-5,6	J2U	40	6	6	YES			N
6B	TB3-7,8	J2L	44	16	6	YES			N
8A	TB5-9,10	J6U	42	8	8	YES		3	S
8B	TB5-11,12	J6L	46	18	8	YES		3	S
PED PUSH BUTTONS									
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED				
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED				
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED				
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED				

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

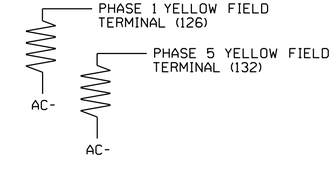
[1] Add jumper from I1-W to J4-W, on rear of input file.

[2] Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0751
DESIGNED: April 2016
SEALED: 6-02-16
REVISED: N/A

Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signals Management Section
750 N.Greenfield Pkwy.,Garner,NC 27529

US 401 Bypass (Pamalee Drive) at Blanton Road

Division 6 | Cumberland County | Fayetteville

PLAN DATE: May 2016 | REVIEWED BY: DTJ

PREPARED BY: James Peterson | REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 036880 ENGINEER KEITH M. MIMS

DocuSigned by: Keith M. Mims 6/13/2016 DATE

SIG. INVENTORY NO. 06-0751

13-JUN-2016 07:42
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jtpeterson