

## ECONOLITE ASC/3 LOGIC PROCESSOR PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

From the LOGIC PROCESSOR Submenu select:

2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)
IF PMT PREEMPT ACTIVE 2 IS OFF

THEN CTR OMIT PHASE 9 ON

ELSE
    
```

LOGIC TO OMIT PHASE 9 WHILE NOT IN PREEMPT 1 (AND ALSO OMIT PHASE 9 AT CONTROLLER STARTUP)

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 2 COPY FROM: 1 ACTIVE: M (T/F)
IF CTR PH NEXT ON PHS 9 IS OFF
AND CTR PHASE TIMING 9 IS OFF
AND LP COB CODE OFF 546

THEN SIG SET OLP RED 3 OFF

ELSE
    
```

FLASHES SIGNAL HEAD 55 IF CONTROLLER IS NOT TRANSITIONING INTO PREEMPTION (PHASE 9 NEXT IS OFF) AND IS NOT IN PREEMPTION (PHASE 9 IS NOT TIMING)

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 3 COPY FROM: 1 ACTIVE: M (T/F)
IF VEH YELLOW ON PH 8 IS ON
AND CTR PH NEXT ON PHS 9 IS ON
AND CTR PHASE TIMING 4 IS ON

THEN SIG SET OLP RED 8 OFF
SIG SET OLP YELLOW 8 ON
SIG SET OVLP GREEN 8 OFF

ELSE
    
```

PROGRAMS SIGNAL HEADS 81 AND 82 TO CLEAR TO ALL RED BEFORE TRANSITIONING INTO PREEMPTION, WHEN LEAVING 4+8.

ENTER A "4" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 4 COPY FROM: 1 ACTIVE: M (T/F)
IF VEH RED ON PHASE 8 IS ON
AND CTR PH NEXT ON PHS 9 IS ON
AND CTR PHASE TIMING 4 IS ON

THEN SIG SET OLP RED 8 ON
SIG SET OLP YELLOW 8 OFF
SIG SET OVLP GREEN 8 OFF

ELSE
    
```

END PROGRAMMING

From the LOGIC PROCESSOR Submenu select:

1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-4 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

LOGIC STATEMENT CONTROL												
	1	2	3	4	5	6	7	8	9	0	1	2
LP 1-15	E	E	E	E	.	.	.	.	.	.	.	.
LP 16-30	.	.	.	.	.	.	.	.	.	.	.	.
LP 31-45	.	.	.	.	.	.	.	.	.	.	.	.
LP 46-60	.	.	.	.	.	.	.	.	.	.	.	.
LP 61-75	.	.	.	.	.	.	.	.	.	.	.	.
LP 76-90	.	.	.	.	.	.	.	.	.	.	.	.

END PROGRAMMING

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle Twice

### OVERLAP C

Select TMG VEH OVLP [C] and 'NORMAL'

```

TMG VEH OVLP...[C] TYPE: .....[NORMAL]
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

### OVERLAP D

Select TMG VEH OVLP [D] and 'OTHER/ECONOLITE'

```

TMG VEH OVLP...[D] TYPE:[OTHER/ECONOLITE]
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . X X X . X . X . . . . .
PROTECT . . . . .
PED PRTC . . . . .
NOT OVLP . . . . .
FLSH GRN . 1 1 1 . 1 . 1 . . . . .
LAG X PH . . . . .
LAG 2 PH . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
    
```

Toggle Three Times

### OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```

TMG VEH OVLP...[G] TYPE: .....[NORMAL]
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

### OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```

TMG VEH OVLP...[H] TYPE: .....[NORMAL]
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

END PROGRAMMING

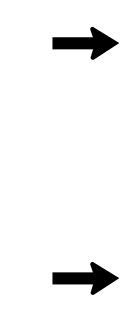
## ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switches S4 and S11 as OLG and OLH, program LD SWITCH 3 as OVLP '7' TYPE '0' and LD SWITCH 8 as OVLP '8' TYPE '0' as shown below.

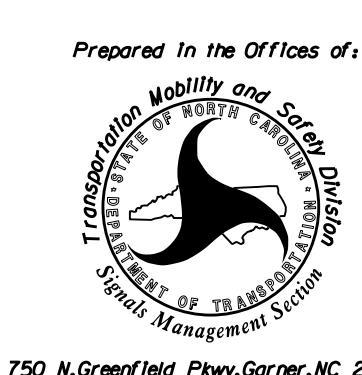
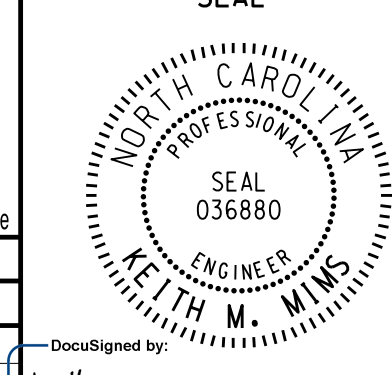
1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

LD SWITCH ASSIGN									
	PHASE	DIMMING	---FLASH---						
	/OVLP	TYPE	R	Y	G	D	PWR	AUT	TGR
1	1	V	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	Y	.
3	7	0	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
5	5	V	.	.	.	-	A	R	.
6	6	V	.	.	.	-	A	Y	X
7	7	V	.	.	.	-	A	R	.
8	8	0	.	.	.	-	A	R	X
9	1	0	.	.	.	+	A	R	X
10	2	0	.	.	.	+	A	R	X
11	3	0	.	.	.	-	A	R	.
12	4	0	.	.	.	-	A	Y	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	-	A	.	.
15	6	P	.	.	.	+	A	.	.
16	8	P	.	.	.	-	A	.	.



Electrical Detail Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:  Prepared In the Offices of: 	SR 1408 (Bonanza Drive) at SR 1437/SR 2750 (Santa Fe Drive) Division 6 Cumberland County Fayetteville PLAN DATE: October 2016 REVIEWED BY: PREPARED BY: James Peterson REVIEWED BY: REVISIONS: _____ INIT: _____ DATE: _____	SEAL  DocuSigned by: Keith M. Mims 10/21/2016 DATE: _____ SIG. INVENTORY NO. 06-0453
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THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0453  
DESIGNED: March 2016  
SEALED: 10-19-16  
REVISED: N/A

24-0037-2016 10/27  
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 J. Peterson