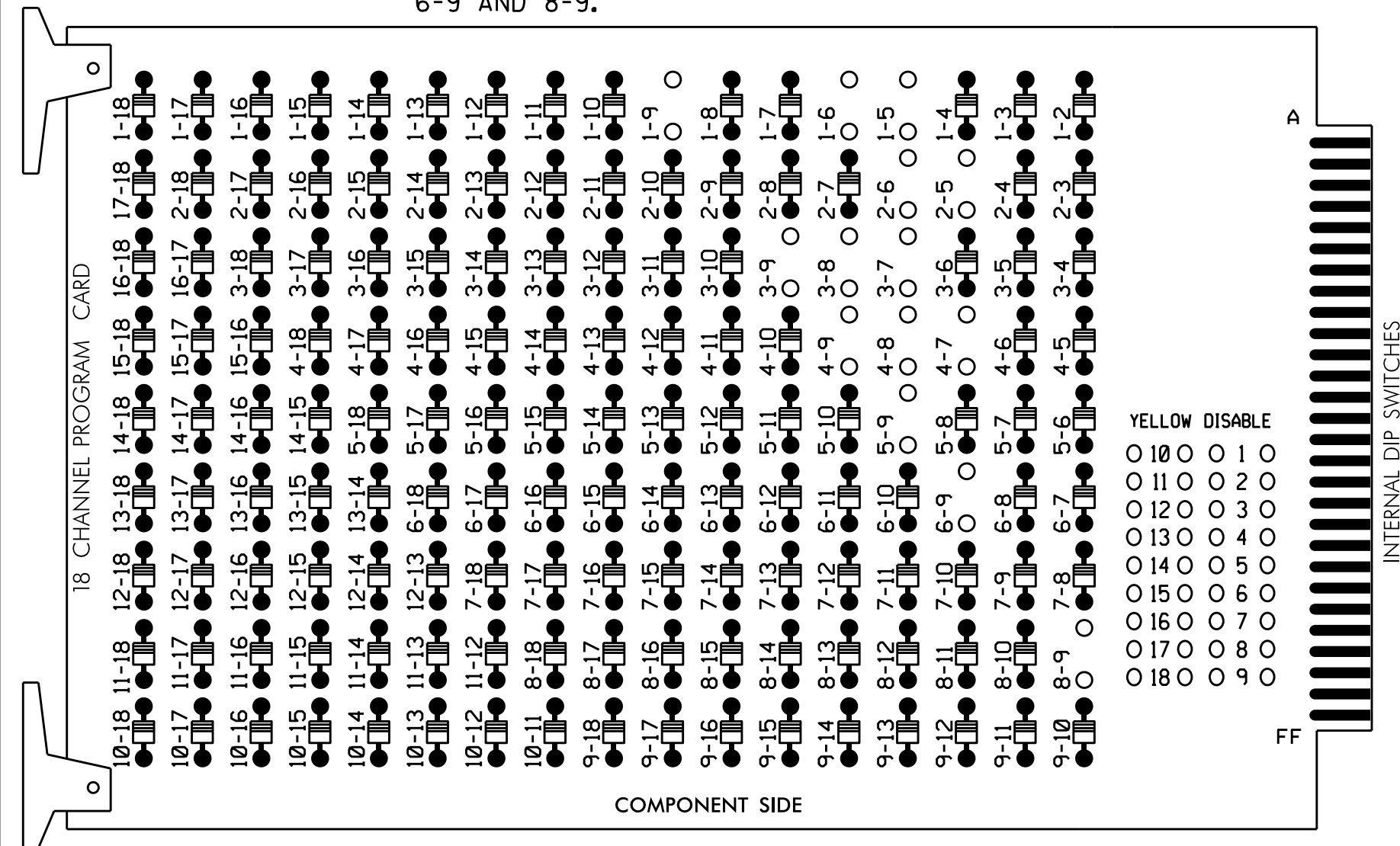


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

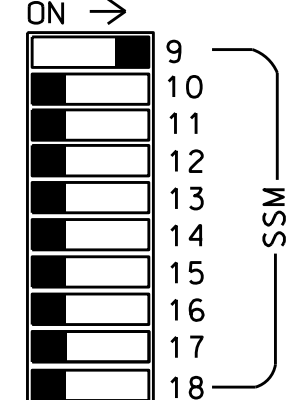
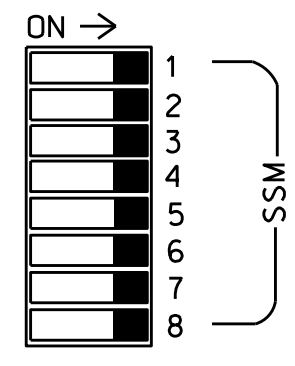
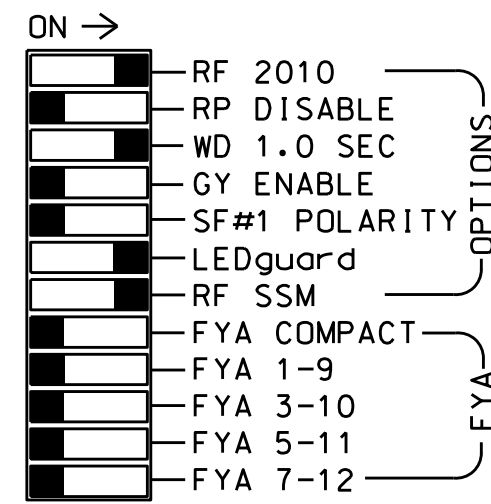
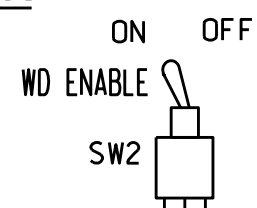
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 2-5, 2-6, 3-7, 3-8, 3-9, 4-7, 4-8, 4-9, 5-9, 6-9 AND 8-9.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 W/AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,  
 AUX S1  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAP "A".....\*  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....NOT USED  
 \* See overlap programming detail on this sheet.

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	21,22,23	NU	22	31	41,42	NU	42	51,52	61,62,63	NU	62	71,72	81,82	NU	83,84	NU	NU
RED		128				101				134				107		A121		
YELLOW		129				102				135				108				
GREEN		130				103				136				109				
RED ARROW	125					116				131				122				
YELLOW ARROW	126			117	117			132	132			123	123			A122		
GREEN ARROW	127			118	118			133	133			124	124			A123		

NU = Not Used

**ECONOLITE ASC/3-2070 OVERLAP  
PROGRAMMING DETAIL**

(program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

**INPUT FILE POSITION LAYOUT**

(front view)

FILE "I" L	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	S	∅ 3	∅ 4	S	S	SYS. DET. S6A	S	S	S	S	FS
L	1A	1C	2A	∅ 2	3A	4A	∅ 4	∅ 4	SYS. DET. S6A	∅ 4	∅ 4	∅ 4	∅ 4	DC ISOLATOR
U	∅ 1	∅ 1	∅ 2	NOT USED	∅ 4	∅ 4	∅ 4	∅ 4	SYS. DET. S6B	∅ 4	∅ 4	∅ 4	∅ 4	ST
L	1B	1D	2B	∅ 2	4B	4B	4B	4B	SYS. DET. S6B	4B	4B	4B	4B	DC ISOLATOR
U	∅ 5	∅ 5	∅ 6	S	∅ 7	∅ 8	S	S	SYS. DET. S8A	S	S	S	S	S
L	5A	5B	6A	∅ 6	7A	8A	∅ 8	∅ 8	SYS. DET. S8A	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8
U	NOT USED	∅ 5	∅ 6	∅ 6	∅ 7	∅ 8	∅ 8	∅ 8	SYS. DET. S8B	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8
L	5C	6B	6B	∅ 6	7B	8B	∅ 8	∅ 8	SYS. DET. S8B	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8

EX.: 1A, 2A, ETC. = LOOP NO.'S

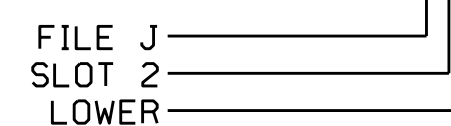
FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES			S
1B	TB2-3,4	I1L	56	1	1	YES			S
1C	TB2-5,6	I2U	39	2	1	YES		15	S
1D	TB2-7,8	I2L	43	12	1	YES		15	S
2A	TB2-9,10	I3U	63	32	2	YES			N
2B	TB2-11,12	I3L	76	42	2	YES			N
3A	TB4-5,6	I5U	58	3	3	YES		3	S
4A	TB4-9,10	I6U	41	4	4	YES			S
4B	TB4-11,12	I6L	45	14	4	YES			S
*S6A	TB6-9,10	I9U	60	11	SYS	NO			N
*S6B	TB6-11,12	I9L	62	13	SYS	NO			N
5A	TB3-1,2	J1U	55	5	5	YES		3	S
5B	TB3-5,6	J2U	40	6	5	YES			S
5C	TB3-7,8	J2L	44	16	5	YES		15	S
6A	TB3-9,10	J3U	64	36	6	YES			N
6B	TB3-11,12	J3L	77	46	6	YES			N
7A	TB5-5,6	J5U	57	7	7	YES			S
7B	TB5-7,8	J5L	57	7	7	YES			S
8A	TB5-9,10	J6U	42	8	8	YES			S
8B	TB5-11,12	J6L	46	18	8	YES			S
*S8A	TB7-9,10	J9U	59	15	SYS	NO			N
*S8B	TB7-11,12	J9L	61	17	SYS	NO			N

\* System detector only. Remove any assigned vehicle phase.

INPUT FILE POSITION LEGEND: J2L



**FLASHER CIRCUIT MODIFICATION DETAIL**

In order to ensure that signals flash concurrently on the same approach, make the following flasher circuit changes:

- On rear of PDA - remove wire from Term. T2-4 and terminate on T2-2.
- On rear of PDA - remove wire from Term. T2-5 and terminate on T2-3.
- Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

**OVERLAP A**

Select TMG VEH OVLP [A] and 'NORMAL'

TMG VEH OVLP...[A] TYPE: .....[NORMAL]  
 PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6  
 INCLUDED X . . . . . X . . . . .  
 LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0412  
 DESIGNED: June 2016  
 SEALED: 10-13-16  
 REVISED: N/A

**Electrical Detail**

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  
 TRANSPORTATION MOBILITY AND SAFETY DIVISION  
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION  
 Signal Management Section  
 750 N. Greenfield Pkwy, Garner, NC 27529

US 301/I-95 Business at SR 2260 (Airport Road) / SR 1344 (Black and Decker Road)  
 Division 6 Cumberland County Fayetteville  
 PLAN DATE: October 2016 REVIEWED BY: BAS  
 PREPARED BY: James Peterson REVIEWED BY:  
 REVISIONS INIT. DATE  
 Keith M. Mims 10/19/2016  
 SIG. INVENTORY NO. 06-0412

18-007-2016 06-18  
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 J.peterson