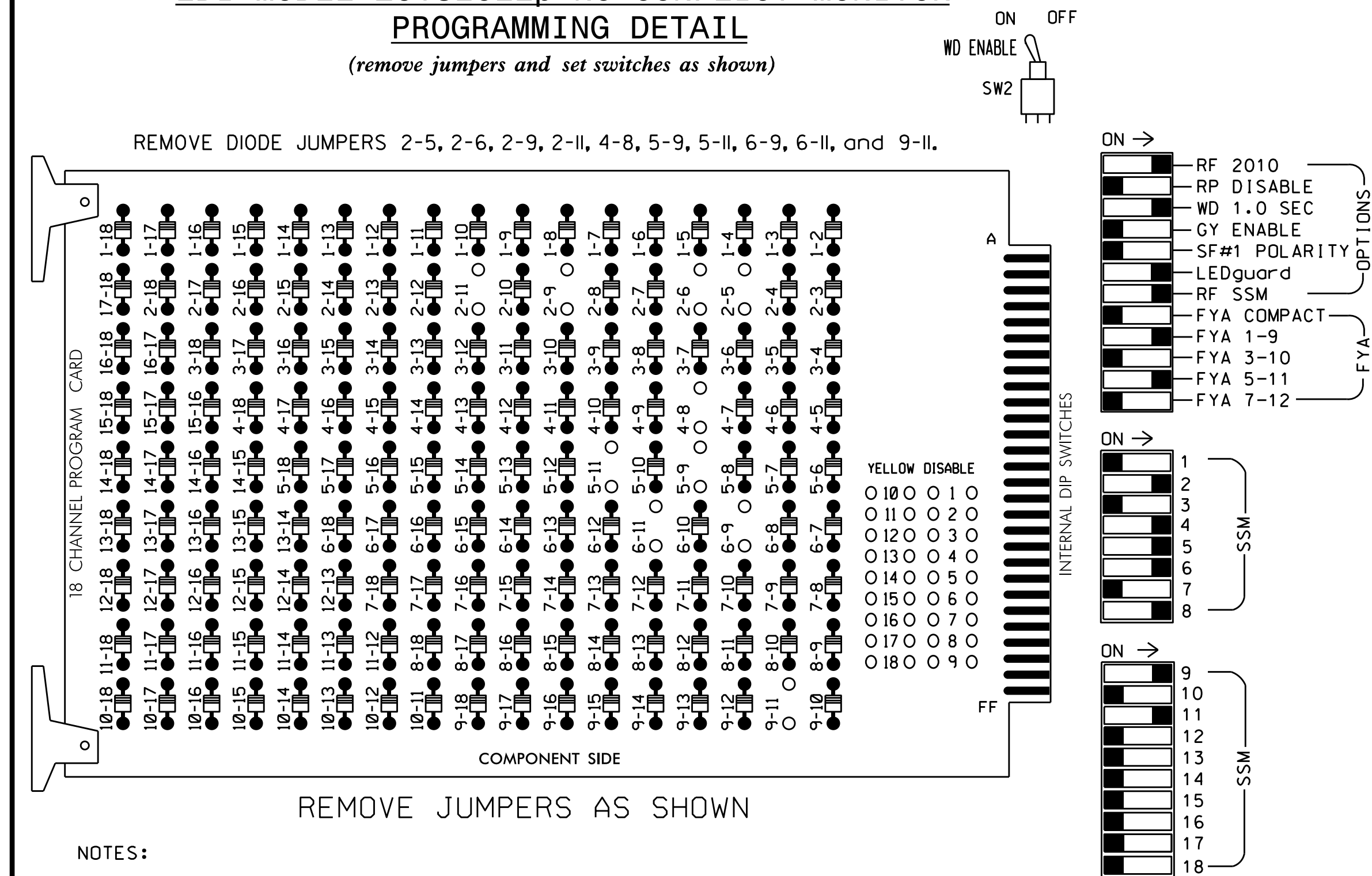


EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S5,S7,S8,S11,AUX S1,AUX S4
 PHASES USED.....2,4,5,6,8
 OVERLAP "A".....*
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED

* See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	42	51*	62,63	NU	NU	81,82	NU	61*	NU	NU	51*	NU
RED		128			101		*		134			107						
YELLOW		129			102				135			108						
GREEN		130			103				136			109						
RED ARROW														A121			A114	
YELLOW ARROW							132							A122			A115	
FLASHING YELLOW ARROW														A123			A116	
GREEN ARROW							133	133										

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 2	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14
L	NOT USED	2A,2B	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
U	∅ 5	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17
L	NOT USED	5A	5B	6A,6B	8A	8B	8B	8B	8B	8B	8B	8B	8B	8B

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

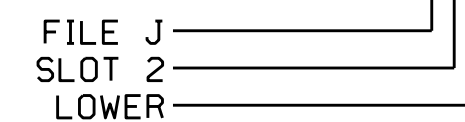
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
2A,2B	TB2-5,6	I2U	39	2	2	YES			N
4A	TB4-9,10	I6U	41	4	4	YES		3	S
5A ¹	TB3-1,2	J1U	55	5	5	YES		15	S
		I4U	47	22	2	YES		3	G
5B	TB3-5,6	J2U	40	6	5	YES		15	S
6A,6B	TB3-7,8	J2L	44	16	6	YES			N
8A	TB5-9,10	J6U	42	8	8	YES		3	S
8B	TB5-11,12	J6L	46	18	8	YES		15	S
*56A	TB6-9,10	I9U	60	11	SYS	NO			N
*56B	TB6-11,12	I9L	62	13	SYS	NO			N

* System detector only. Remove any assigned vehicle phase.

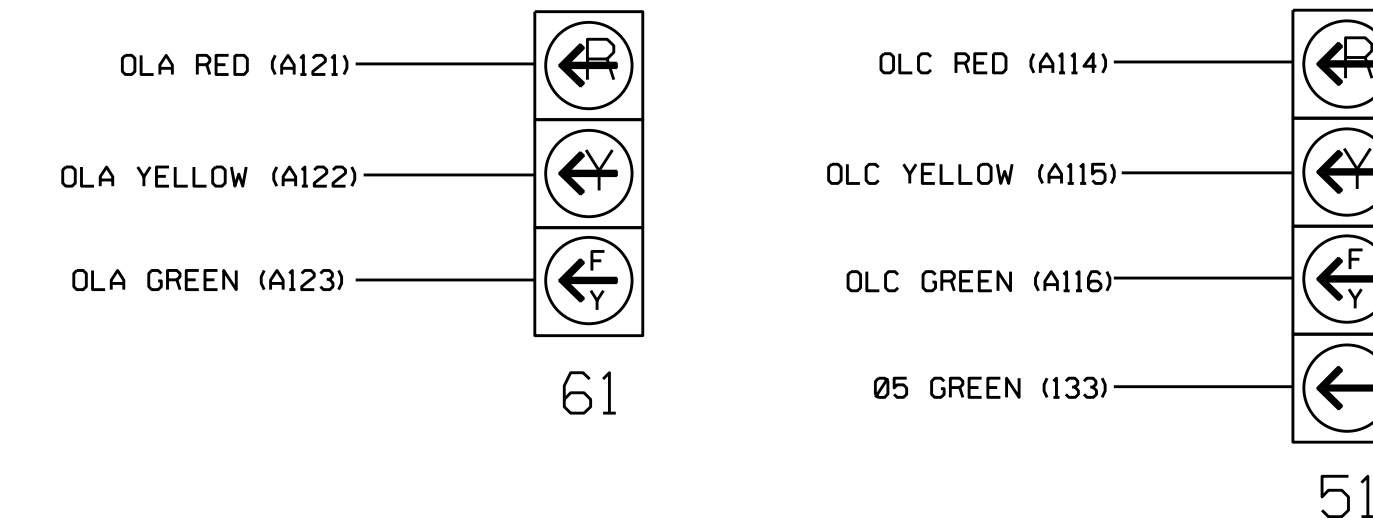
¹Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

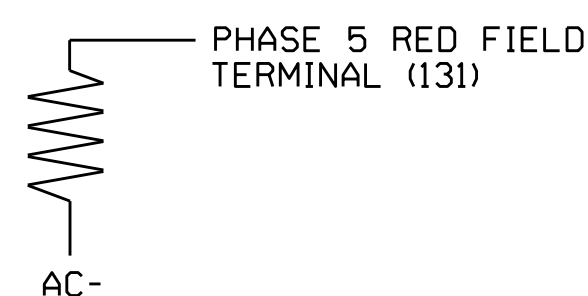
(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical and Programming Details for: **NC 24-87 (N Bragg Boulevard) at NC 690 (Vass Road)**

Prepared in the Offices of: **Transportation Mobility and Safety Solutions**

Division 6 Cumberland County Spring Lake

PLAN DATE: **March 2016** REVIEWED BY: **BAS**

PREPARED BY: **S. Armstrong** REVIEWED BY:

REVISIONS: _____ INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: **KEITH M. MINS** PROFESSIONAL ENGINEER 036880

DocuSigned by: **Keith M. Mins** 3/18/2016

SIG. INVENTORY NO. **06-0385**

08-MAR-2016 11:40 S:\MITS\15\SIGNAL\work\hgr\cds\g_Minh\mstronng\60385_sml.e xxx.dgn sarmstrong