# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL
*(program controller as shown)*

1. From Main Menu select [ 2. CONTROLLER ]

2. From CONTROLLER Submenu select [ 2. VEHICLE OVERLAPS ]

### *OVERLAP A*

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: ....[PPLT FYA]

PROTECTED PHASE (LEFT TURN)......... 1
PERMISSIVE PHASE (OPPOSING THRU).... 2
FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 1    ◄── NOTICE ACTION
                                              PLAN SF BIT "1"
```
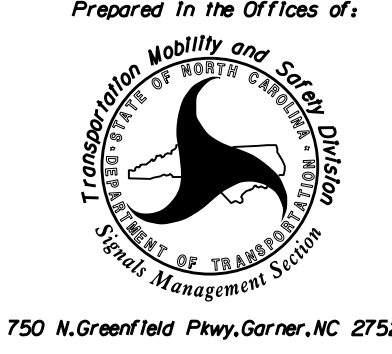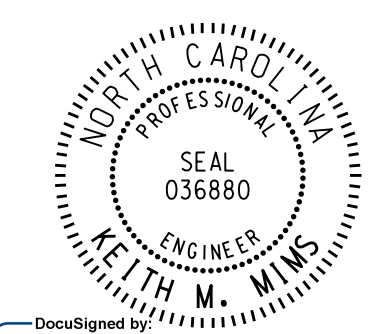Toggle Twice

### *OVERLAP C*

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: ....[PPLT FYA]

PROTECTED PHASE (LEFT TURN)......... 5
PERMISSIVE PHASE (OPPOSING THRU).... 6
FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 5    ◄── NOTICE ACTION
                                              PLAN SF BIT "5"
```

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0360
DESIGNED: December 2015
SEALED: 5/9/2016
REVISED: N/A

Electrical Detail - Sheet 2 of 4

**DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED**