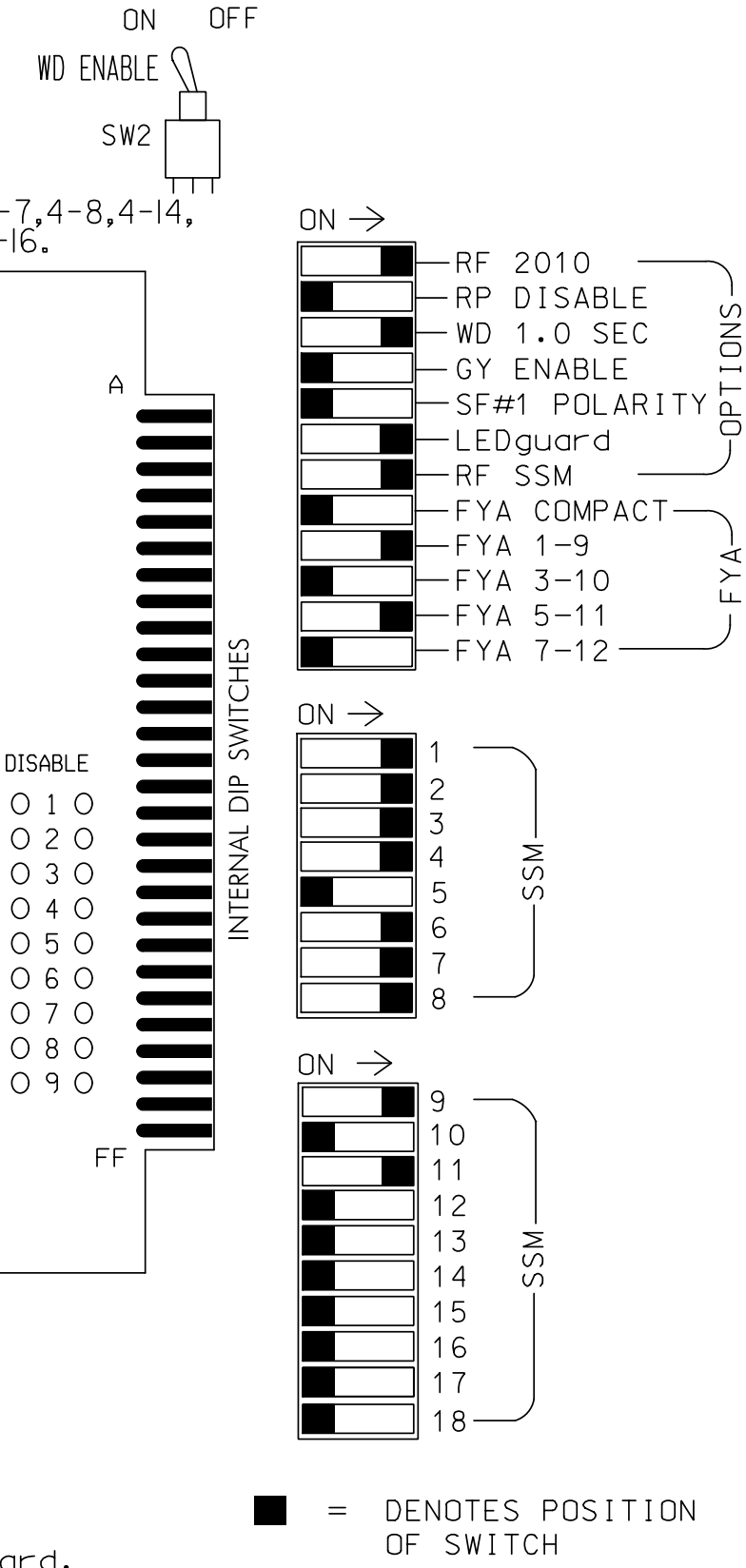
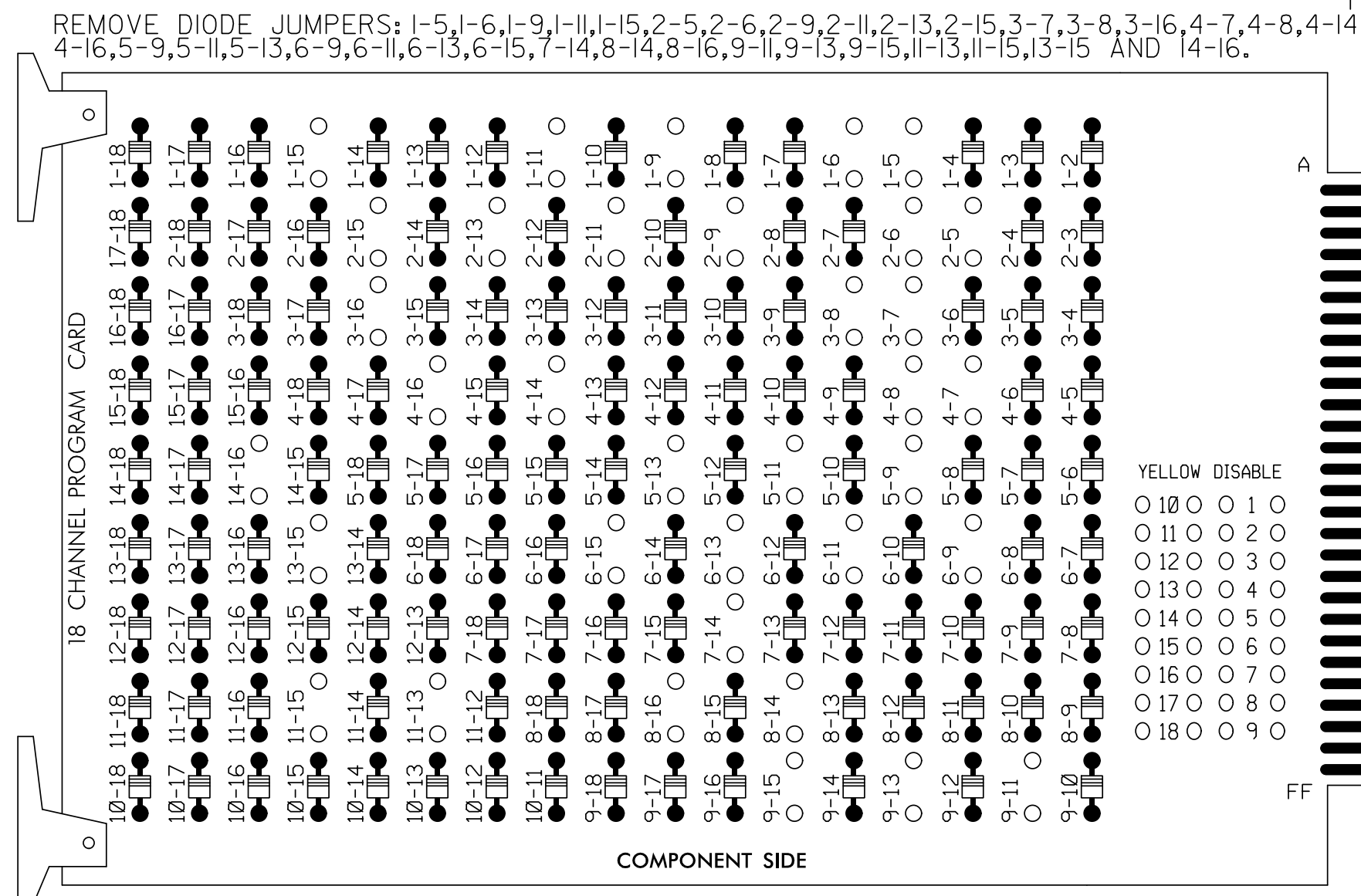


EDI MODEL 2018EClip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:**
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that Red Enable is active at all times during normal operation.
 - Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2,4,6 and 8 for volume density operation.
- Program controller to start up in phase 2 Walk and 6 Walk.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,S11,S12,AUX S1,AUX S4
 PHASES USED.....1,2,2PED,3,4,4PED,5,6,6PED,7,8,8PED
 OVERLAP "A".....*
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED
 * See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	21,22	P21, P22	31,32	41,42, 43	P41, P42	51	61,62	P61, P62	71,72	81,82	P81, P82	11	NU	NU	51	NU
RED	*	128			101			134		107								
YELLOW		129			102		*	135		108								
GREEN		130			103			136		109								
RED ARROW				116						122			A121					A114
YELLOW ARROW	126			117						123			A122					A115
FLASHING YELLOW ARROW													A123					A116
GREEN ARROW	127	127		118			133			124								
Hand icon				113			104		119			110						
Foot icon				115			106		121			112						

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

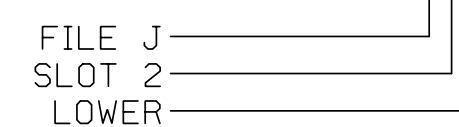
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A ¹	TB2-1,2	I1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES		3	G
1B	TB2-5,6	I2U	39	2	1	YES		15	S
2A	TB2-9,10	I3U	63	32	2	YES			N
2B	TB2-11,12	I3L	76	42	2	YES			N
3A	TB4-5,6	I5U	58	3	3	YES			S
3B	TB4-9,10	I6U	41	3	3	YES			S
4A	TB4-11,12	I6L	45	14	4	NO			N
4B	TB6-1,2	I7U	65	34	4	NO			N
4C	TB6-3,4	I7L	78	44	4	NO			N
4D	TB6-5,6	I8U	49	24	4	YES	2.0	5	G
4E	TB3-9,10	J3U	64	36	4	YES	2.0	5	G
4F	TB3-11,12	J3L	77	46	4	YES	2.0	5	G
5A ²	TB3-1,2	J1U	55	5	5	YES		15	S
	-	I4U	47	22	2	YES		3	G
6A	TB3-5,6	J2U	40	6	6	YES			N
6B	TB3-7,8	J2L	44	16	6	YES			N
7A	TB5-5,6	J5U	57	7	7	YES		3	S
7B	TB5-9,10	J6U	42	8	7	YES			S
8A	TB5-11,12	J6L	46	18	8	NO			N
8B	TB7-1,2	J7U	66	38	8	NO			N
8C	TB7-3,4	J7L	79	48	8	YES	2.0	5	G
8D	TB7-5,6	J8U	50	28	8	YES	2.0	5	G
*S2A	TB6-9,10	I9U	60	11	SYS	NO			N
*S2B	TB6-11,12	I9L	62	13	SYS	NO			N
*S6A	TB7-9,10	J9U	59	15	SYS	NO			N
*S6B	TB7-11,12	J9L	61	17	SYS	NO			N
PED PUSH BUTTONS									
P21,P22	TB8-4,6	I12U	67	PED 2	2	PED			
P41,P42	TB8-5,6	I12L	69	PED 4	4	PED			
P61,P62	TB8-7,9	I13U	68	PED 6	6	PED			
P81,P82	TB8-8,9	I13L	70	PED 8	8	PED			

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

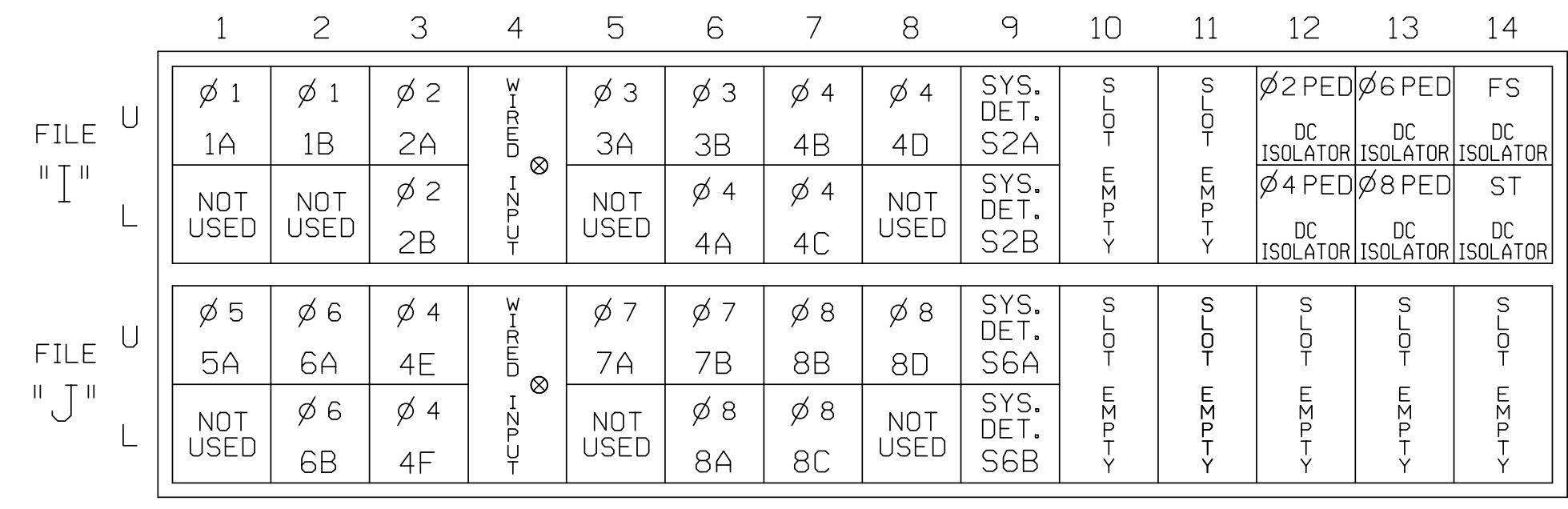
- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- * System detector only. Remove any assigned vehicle phase.

INPUT FILE POSITION LEGEND: J2L



INPUT FILE POSITION LAYOUT

(front view)



EX: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

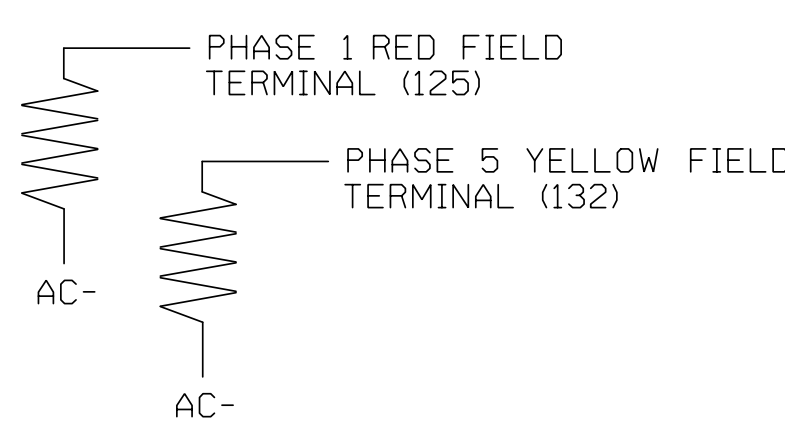
⊗ Wired Input - Do not populate slot with detector card

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

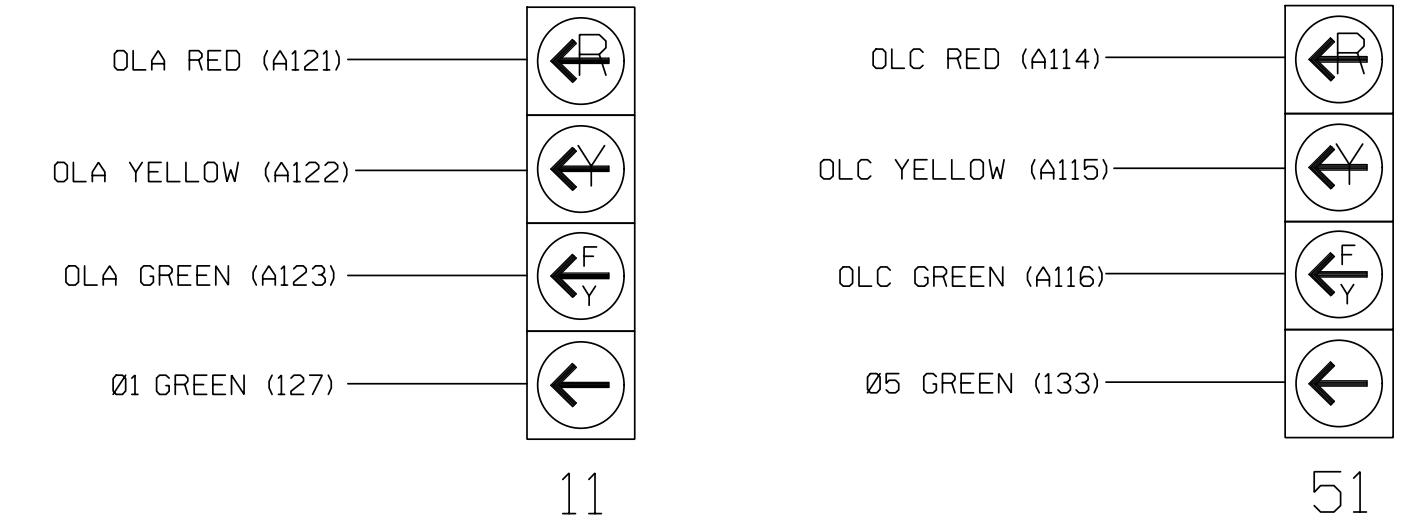
ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0275
 DESIGNED: May 2016
 SEALED: 7/20/2016
 REVISED:

Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared For: 750 N. Greenfield Pkwy, Garner, NC 27529	SR 1344 (Black & Decker Road / Mid Pine Road) at SR 1132 (Legion Road)	SEAL
	Division 6 Cumberland County Fayetteville PLAN DATE: August 2016 REVIEWED BY: KP Baumann PREPARED BY: SP Pennington REVIEWED BY: SL Phillips	REVISIONS INIT. DATE

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