ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: .....PPLT FYA

PROTECTED PHASE (LEFT TURN)......... 1
PERMISSIVE PHASE (OPPOSING THRU).... 2
FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```
Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .....PPLT FYA

PROTECTED PHASE (LEFT TURN)......... 5
PERMISSIVE PHASE (OPPOSING THRU).... 6
FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```

END PROGRAMMING

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING
DETAIL FOR PHASE 3 OMIT

(program controller as shown)

The following logic processor programming ensures
phase 3 is only serviced during preemption. This logic
will ensure phase 3 is not served during startup.

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND
PROGRAM AS SHOWN.

```
LP#:   1  COPY FROM:   1  ACTIVE:  M  (T/F)
IF    PMT PREEMPT ACTIVE    1   IS  OFF
THEN  CTR OMIT PHASE        3      ON
```

LOGIC FOR OMITTING
PHASE 3 WHILE
NOT IN PREEMPT 1

END PROGRAMMING

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENT 1 BY POSITIONING
THE CURSOR OVER THE FIELD SHOWN BELOW AND USING
THE TOGGLE KEY TO ENABLE IT.
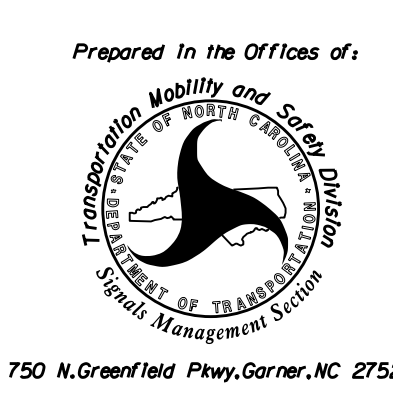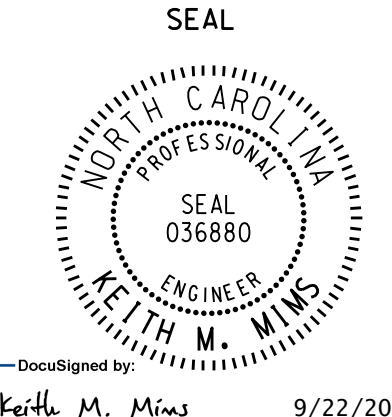
```
LOGIC STATEMENT CONTROL
          1 2 3 4 5 6 7 8 9 0 1 2 3 4 5
LP 1-15   E . . . . . . . . . . . . . .
LP 16-30  . . . . . . . . . . . . . . .
LP 31-45  . . . . . . . . . . . . . . .
LP 46-60  . . . . . . . . . . . . . . .
LP 61-75  . . . . . . . . . . . . . . .
LP 76-90  . . . . . . . . . . . . . . .
```

END PROGRAMMING

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0333
DESIGNED: March 2016
SEALED: 8-24-16
REVISED: N/A
```

Electrical Detail Sheet 2 of 3

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

Prepared In the Offices of:

Transportation Mobility and Safety Division
DEPARTMENT OF TRANSPORTATION
Signal Management Section

750 N.Greenfield Pkwy,Garner,NC 27529

**NC 210 (Murchison Road)
at
SR 1614 (Shaw Mill Road)/
SR 2734 (Hogan Street)**

Division 6	Cumberland County	Fayetteville
PLAN DATE: July 2016	REVIEWED BY: BAS	
PREPARED BY: James Peterson	REVIEWED BY:	

REVISIONS		INIT.	DATE

SEAL

NORTH CAROLINA
PROFESSIONAL
SEAL
036880
KEITH M. MIMS
ENGINEER

DocuSigned by:
Keith M. Mims   9/22/2016
2F8078EE8CCD34A5   DATE

SIG. INVENTORY NO. 06-0333