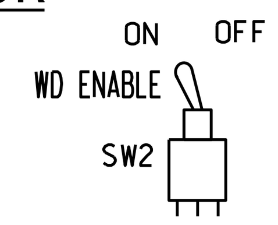
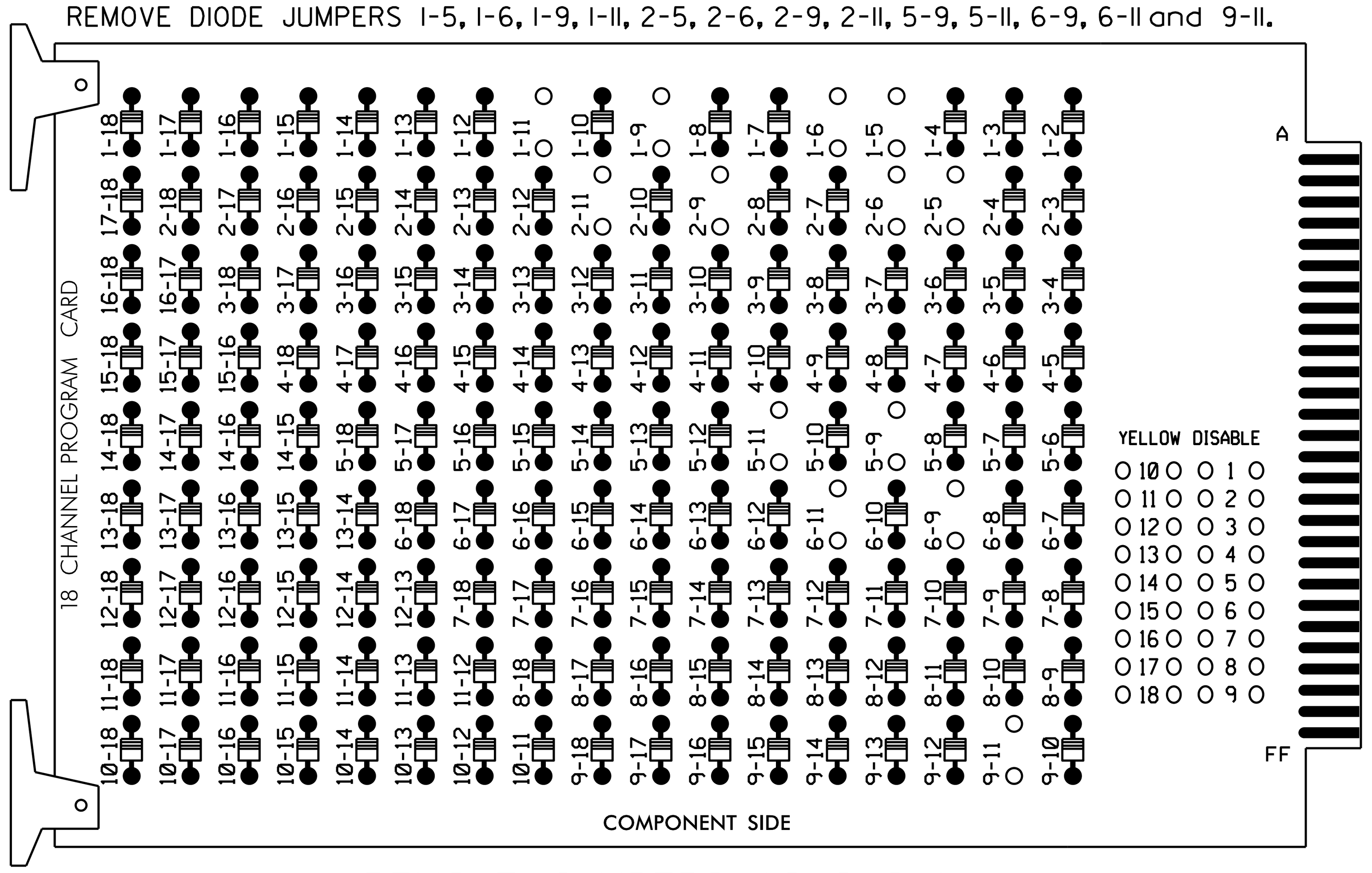
EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

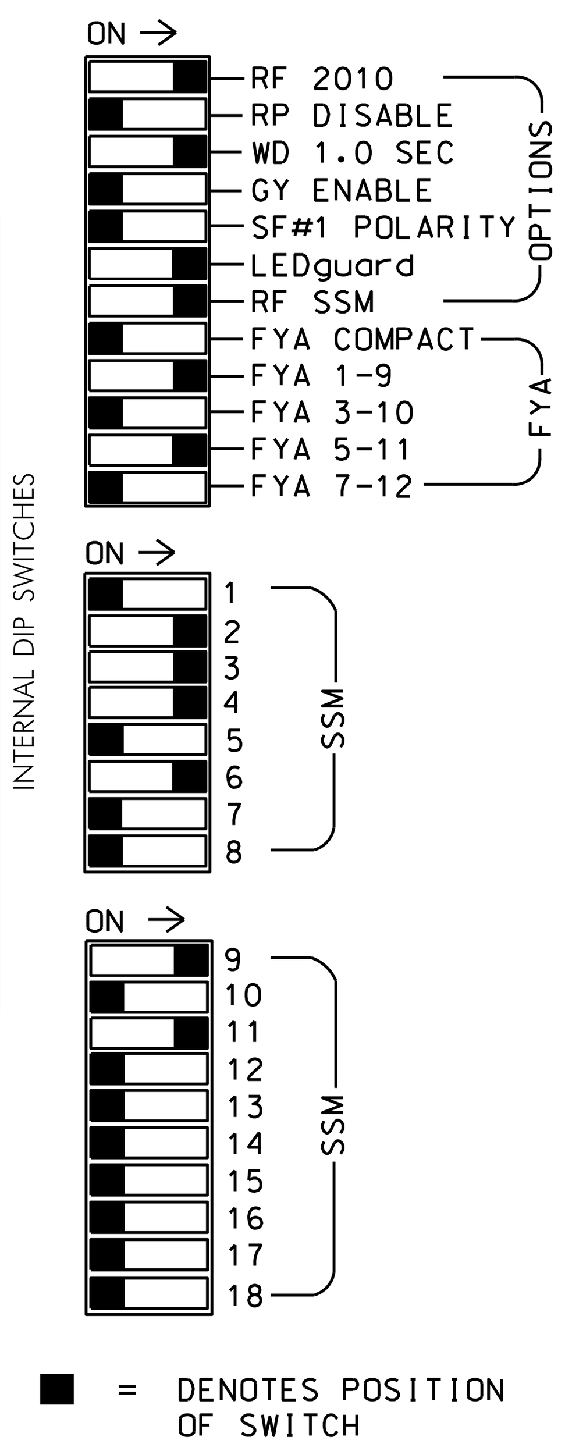
(remove jumpers and set switches as shown)



REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 5-9, 5-11, 6-9, 6-11 and 9-11.



REMOVE JUMPERS AS SHOWN



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for volume density operation.
4. Program controller to start up in phase 2 Green and 6 Green.
5. The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER..............2070E
CABINET.................332 W/AUX
SOFTWARE................ECONOLITE ASC/3-2070
CABINET MOUNT...........BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED......S1,S2,S4,S5,S7,S8,AUX S1,AUX S4
PHASES USED.............1,2,3,4,5,6
OVERLAP "A".............*
OVERLAP "B".............NOT USED
OVERLAP "C".............*
OVERLAP "D".............NOT USED

* See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4		S5			S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3		4			14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3		4			4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21,22	NU	31	32	41,42	43,44	62	NU	51★	61,62	NU	NU	NU	NU	11★	NU	NU	51★	NU	NU
RED		128		116	116		101				134										
YELLOW	*	129		117	117		102			*	135										
GREEN		130		118	118		103				136										
RED ARROW						101										A121			A114		
YELLOW ARROW						102		102								A122			A115		
FLASHING YELLOW ARROW																A123			A116		
GREEN ARROW	127			118		103		103		133											

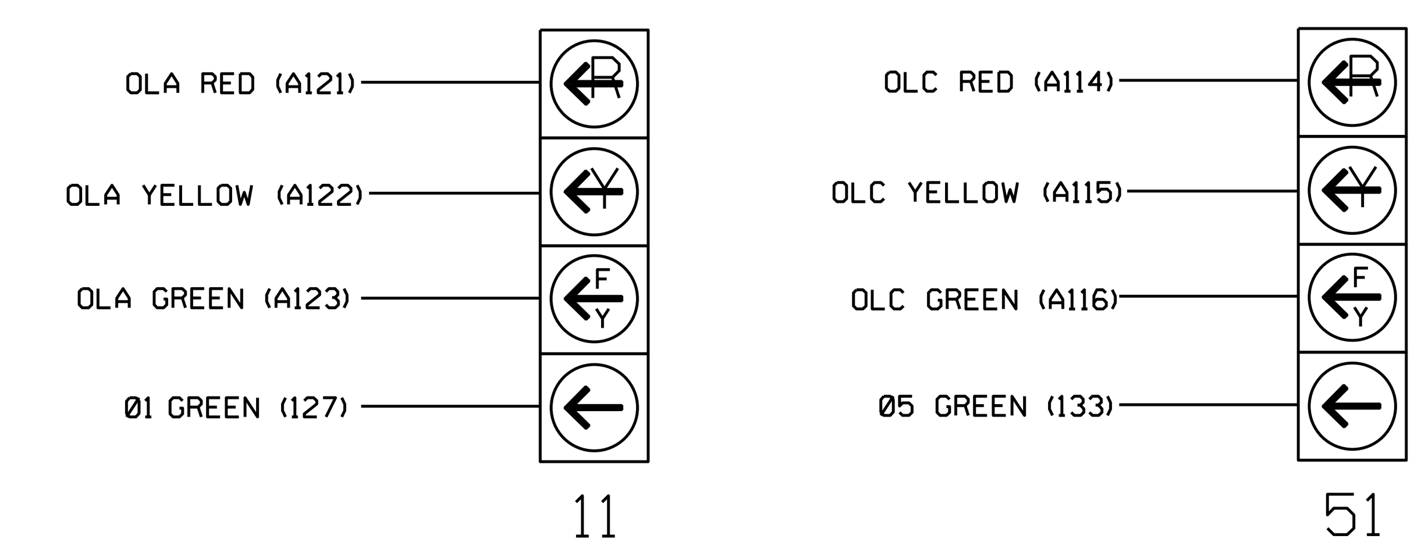
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

FILE		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø2 2A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	Ø3 3A	Ø4 4A	SLOT EMPTY	Ø4 4C	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L	NOT USED	Ø2 2B				Ø3 3B	Ø4 4B		Ø4 4D					ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6 6A	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	Ø6 6B												

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A [1]	TB2-1,2	I1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES		3	G
2A	TB2-5,6	I2U	39	2	2	YES			N
2B	TB2-7,8	I2L	43	12	2	YES			N
3A	TB4-9,10	I6U	41	4	3	YES		3	S
3B	TB4-11,12	I6L	45	14	3	YES		5	S
4A	TB6-1,2	I7U	65	34	4	YES		3	S
4B	TB6-3,4	I7L	78	44	4	YES			S
4C	TB6-9,10	I9U	60	11	4	YES		5	S
4D	TB6-11,12	I9L	62	13	4	YES		10	S
5A [2]	TB3-1,2	J1U	55	5	5	YES		15	S
	-	I4U	47	22	2	YES		3	G
6A	TB3-5,6	J2U	40	6	6	YES			N
6B	TB3-7,8	J2L	44	16	6	YES			N

[1] Add jumper from I1-W to J4-W, on rear of input file.
[2] Add jumper from J1-W to I4-W, on rear of input file.

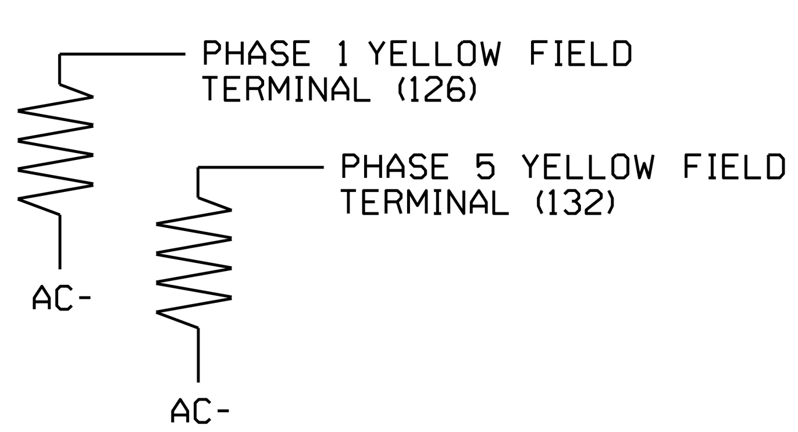
INPUT FILE POSITION LEGEND: J2L

FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0073
DESIGNED: February 2016
SEALED: 3-08-16
REVISED: N/A

Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
Signals Management Section
State of North Carolina Department of Transportation
750 N.Greenfield Pkwy, Garner, NC 27529

NC 24-87 (N. Bragg Boulevard)
at
SR 1451 (Manchester Road)

Division 6 Cumberland County Spring Lake

PLAN DATE: March 2016	REVIEWED BY: DTJ
PREPARED BY: James Peterson	REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 036880 ENGINEER KEITH M. MIMS

DocuSigned by: Keith M. Mims 2F80786E8CD34A5... 3/14/2016 DATE

SIG. INVENTORY NO. 06-0073