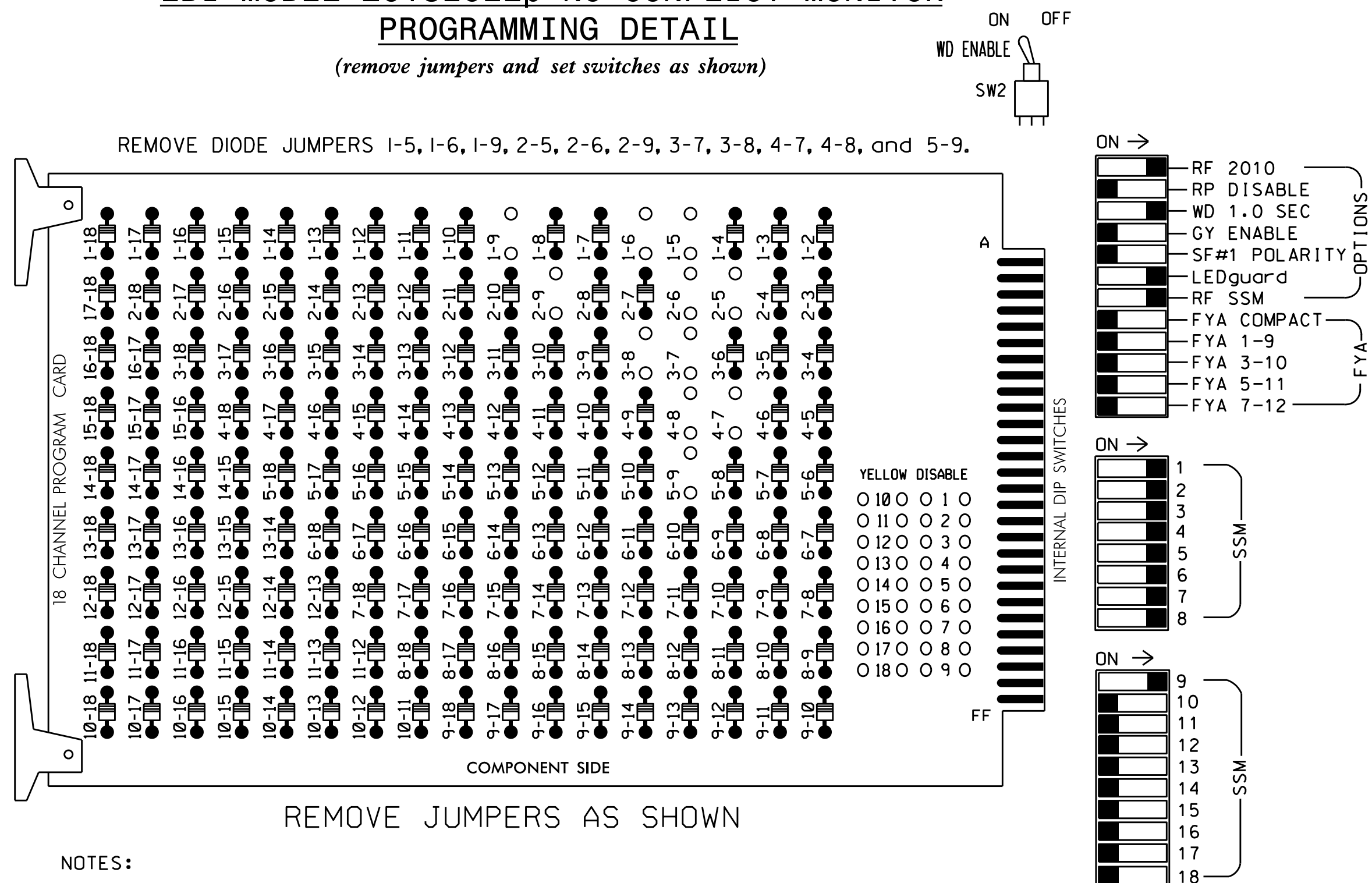


**EDI MODEL 2018ECLip-NC CONFLICT MONITOR**

**PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program controller to start up in phase 2 Green and 6 Green.
4. The cabinet and controller are part of the Fayetteville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,  
 S11,AUX S1  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAP A.....5  
 OVERLAP B.....NOT USED  
 OVERLAP C.....NOT USED  
 OVERLAP D.....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	31	41,42	NU	51	61,62	NU	71,72	81,82	NU	42	NU	NU	NU	NU	NU
RED		128			101			134			107		*					
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW	125				116			131			122							
YELLOW ARROW	126				117			132			123			A122				
GREEN ARROW	127				118			133			124			A123				

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.

**ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL**

(program controller as shown)

1. From Main Menu select **2. CONTROLLER**
2. From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

**OVERLAP A**

Select TMG VEH OVLP [A] and 'NORMAL'  
 TMG VEH OVLP...[A] TYPE: .....**NORMAL**  
 PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6  
 INCLUDED . . . . . x . . . . .  
 LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0054  
 DESIGNED: January 2016  
 SEALED: 9/19/2016  
 REVISED: N/A

**INPUT FILE POSITION LAYOUT**

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1 1A	∅ 2 2A,2C	SYS. DET. S2B	S	∅ 3 3A	∅ 4 4A	SYS. DET. S4A	S	SYS. DET. S6A	S	S	S	S	FS DC ISOLATOR
L	NOT USED	SYS. DET. S2A	SYS. DET. S2C	←-→	NOT USED	∅ 4 4B	SYS. DET. S4B	←-→	SYS. DET. S6B	←-→	←-→	←-→	←-→	ST DC ISOLATOR
U	S	∅ 5 5A	∅ 6 6A,6C	S	S	∅ 7 7A	∅ 8 8A	∅ 8 8C	SYS. DET. S8A	S	S	S	S	RR1 AC ISOLATOR
L	←-→	∅ 5 5B	SYS. DET. S6C	←-→	←-→	∅ 7 7B	∅ 8 8B	NOT USED	SYS. DET. S8B	←-→	←-→	←-→	←-→	RR2 AC ISOLATOR

EX. : 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME  
 RR1, RR2 = RAILROAD PREEMPTS

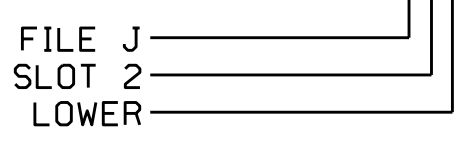
NOTE: The RR1 and RR2 preempt inputs have been remapped as detector inputs for use by the Logic Processor. See sheet 5 for details.

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES		3	S
2A,2B,2C	TB2-5,6	I2U	39	2	2	YES			S
3A	TB4-5,6	I5U	58	3	3	YES		3	S
4A	TB4-9,10	I6U	41	4	4	YES			S
4B	TB4-11,12	I6L	45	14	4	YES			S
5A	TB3-5,6	J2U	40	6	5	YES		3	S
5B	TB3-7,8	J2L	44	16	5	YES		15	S
6A,6B,6C	TB3-9,10	J3U	64	36	6	YES			S
7A	TB5-9,10	J6U	42	8	7	YES		3	S
7B	TB5-11,12	J6L	46	18	7	YES			S
8A	TB7-1,2	J7U	66	38	8	YES			S
8B	TB7-3,4	J7L	79	48	8	YES		10	S
8C	TB7-5,6	J8U	50	28	8	YES		15	S
*S2A	TB2-7,8	I2L	43	12	SYS	NO			N
*S2B	TB2-9,10	I3U	63	32	SYS	NO			N
*S2C	TB2-11,12	I3L	76	42	SYS	NO			N
*S4A	TB6-1,2	I7U	65	34	SYS	NO			N
*S4B	TB6-3,4	I7L	78	44	SYS	NO			N
*S6A	TB6-9,10	I9U	60	11	SYS	NO			N
*S6B	TB6-11,12	I9L	62	13	SYS	NO			N
*S6C	TB3-11,12	J3L	77	46	SYS	NO			N
*S8A	TB7-9,10	J9U	59	15	SYS	NO			N
*S8B	TB7-11,12	J9L	61	17	SYS	NO			N

\* System detector only. Remove any assigned vehicle phase.

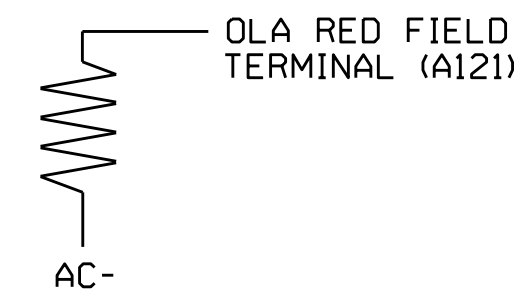
**INPUT FILE POSITION LEGEND: J2L**



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistor as shown)

ACCEPTABLE VALUES	VALUE (ohms)	WATTAGE
	1.5K - 1.9K	25W (min)
	2.0K - 3.0K	10W (min)



DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

US 401 Business (Raeford Road) at McPherson Church Road/ Owen Drive

Division 6 Cumberland County Fayetteville

PLAN DATE: September 2016 REVIEWED BY: BAS

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal of Keith M. Mims, Professional Engineer, No. 036880

DocuSigned by: Keith M. Mims 10/12/2016

SIG. INVENTORY NO. 06-0054

11-007-2016 07:44 S:\115351\15\_Sigmod\work\groups\51g\_Monitors\strong60004\_sml.e.xxx.dgn somstrong