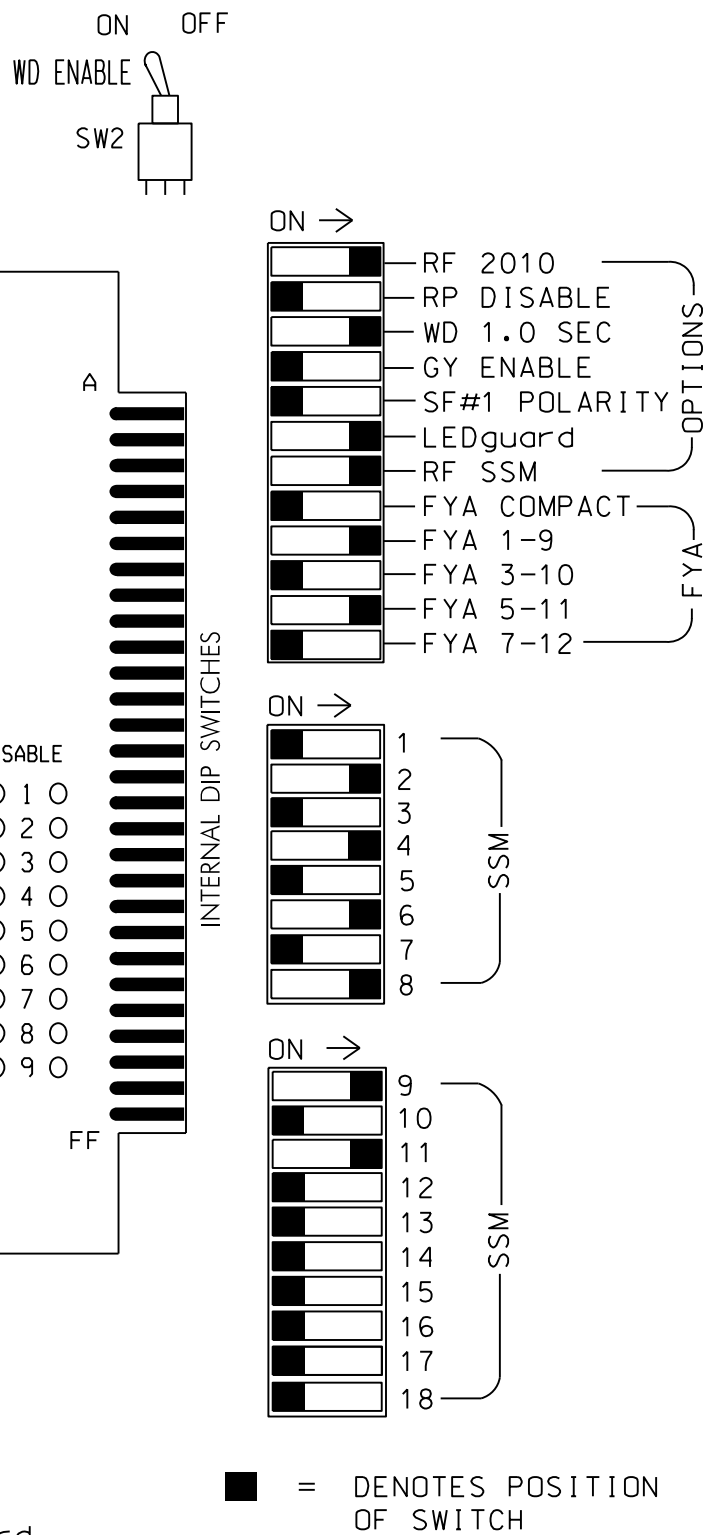
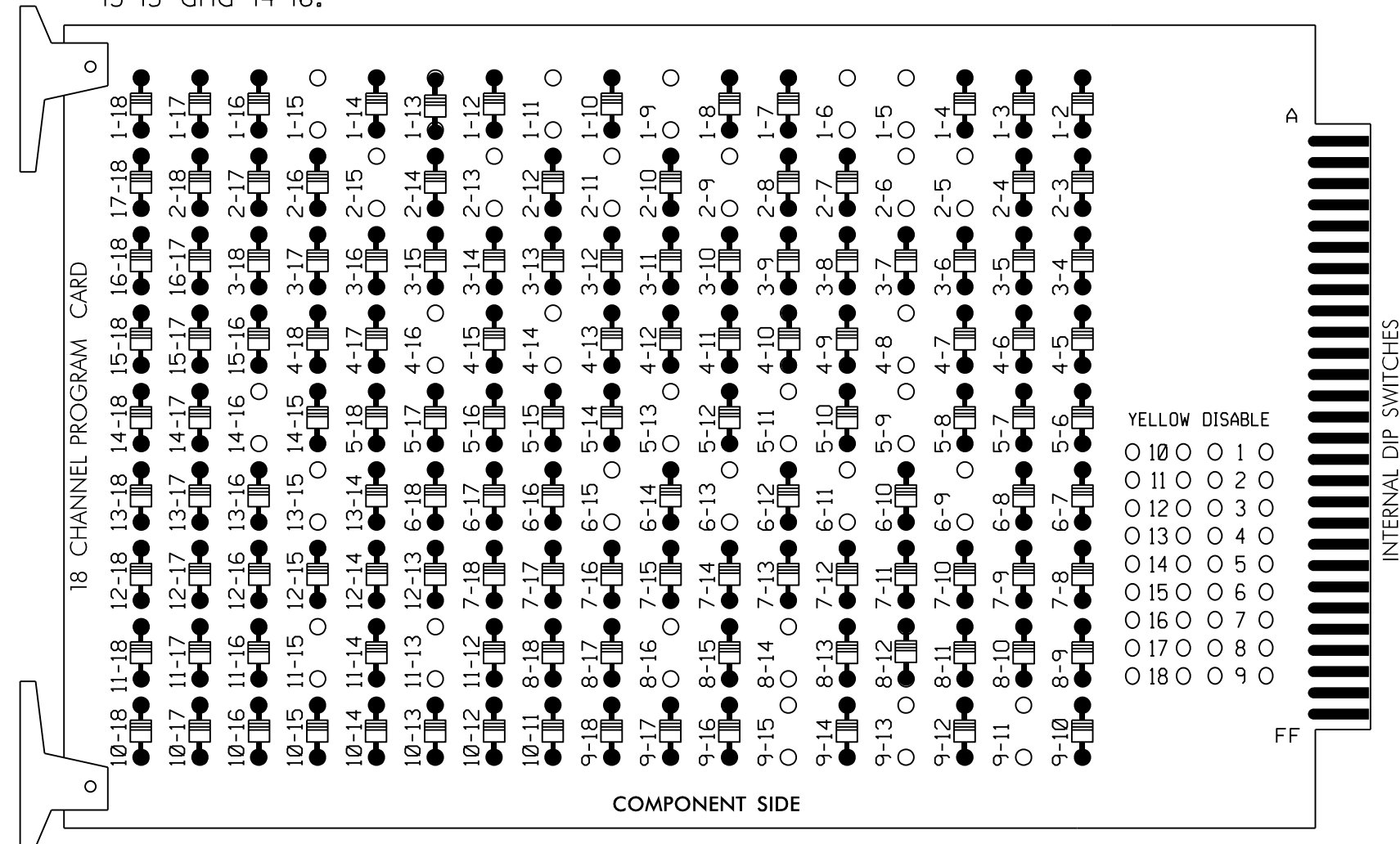


EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 4, 6 and 8 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Concord City Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 (12-STD; 6 AUX)
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S11,S12, AUX S1,AUX S4
 PHASES USED.....1,2,4,5,6,8,2PED,4PED,6PED,8PED
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

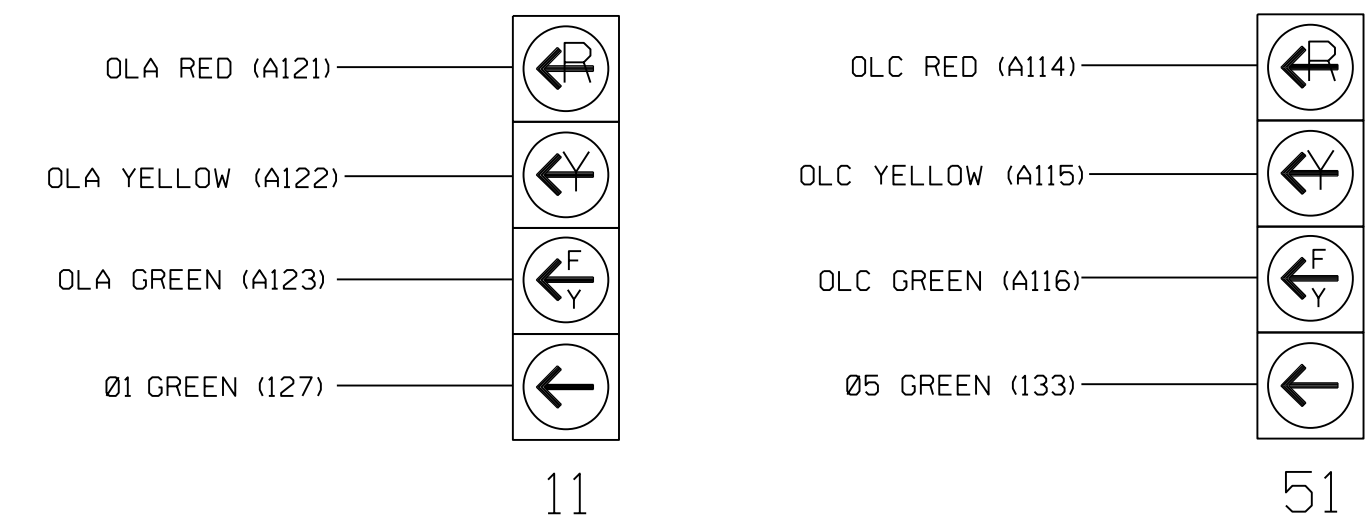
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11*	21,22	P21, P22	NU	41,42	P41, P42	51*	61,62	P61, P62	NU	81,82	P81, P82	11*	NU	NU	51*	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127							133										
Hand					113			104			119			110				
Foot								106			121			112				

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE U "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
	Ø 1 1A	Ø 2 2A	Ø 3 3A	Ø 4 4A	Ø 5 5A	Ø 6 6A	Ø 7 7A	Ø 8 8A	SYS. DET. S33	Ø 2 PED ISOLATOR	Ø 6 PED ISOLATOR	DC ISOLATOR	DC ISOLATOR	FS ISOLATOR
	NOT USED	Ø 2 2B		Ø 4 4B		Ø 8 8B		SYS. DET. S34	SYS. DET. S35	Ø 4 PED ISOLATOR	Ø 8 PED ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
FILE U "J"	Ø 5 5A	Ø 6 6A	Ø 7 7A	Ø 8 8A				SYS. DET. S36						

EX. : 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
*S33	TB6-9,10	I9U	60	22	11	SYS					
*S34	TB6-11,12	I9L	62	24	13	SYS					
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
*S35	TB7-9,10	J9U	59	21	15	SYS					
*S36	TB7-11,12	J9L	61	23	17	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.

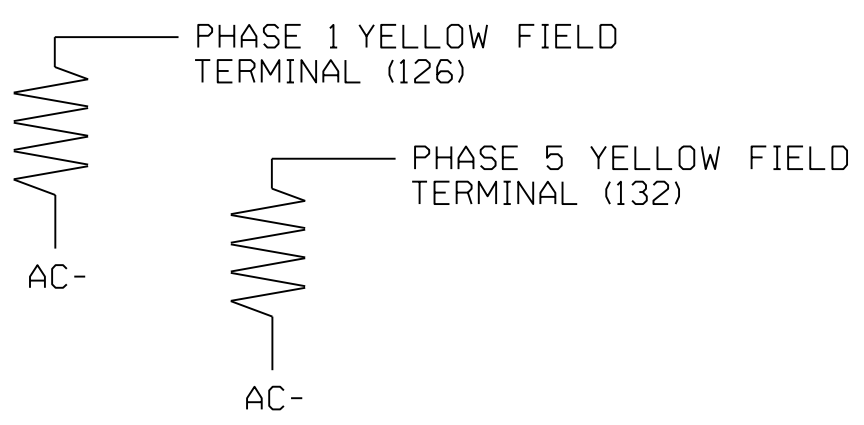
* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L
 FILE J
 SLOT 2
 LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	VALUE (ohms)	WATTAGE
	1.5K - 1.9K	25W (min)
	2.0K - 3.0K	10W (min)



New Installation
 Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	SR 1445 (Derita Road) at West Winds Boulevard / Aviation Boulevard		Division 10 Cabarrus County Concord PLAN DATE: March 2016 REVIEWED BY: J O Deaton PREPARED BY: M W Yalch REVIEWED BY:	Seal of the State of North Carolina Professional Engineer James O. Deaton License No. 05110
	DocuSigned by: James O. Deaton 40FFBAC30BE0CF	INIT. DATE REVISIONS		