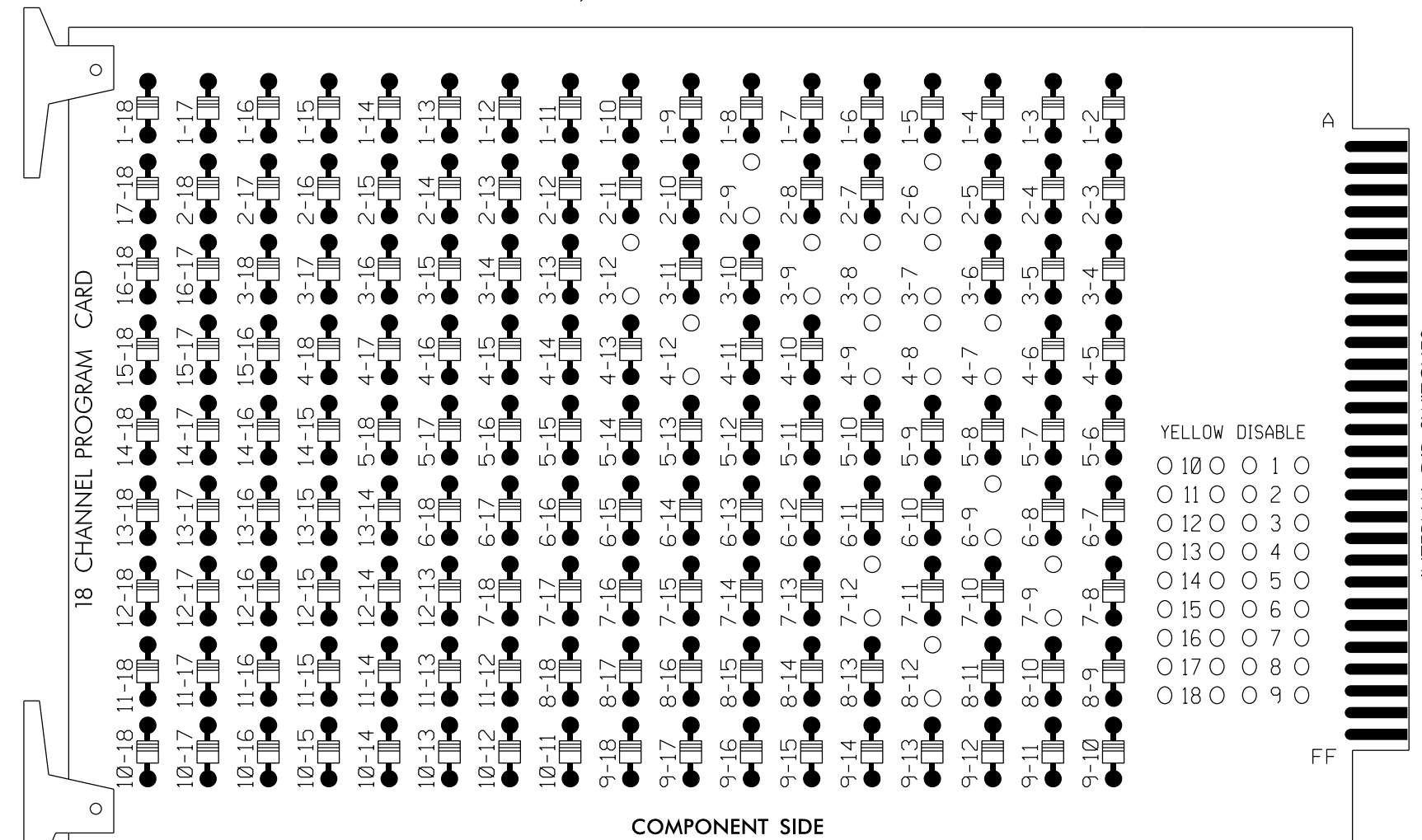


### EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

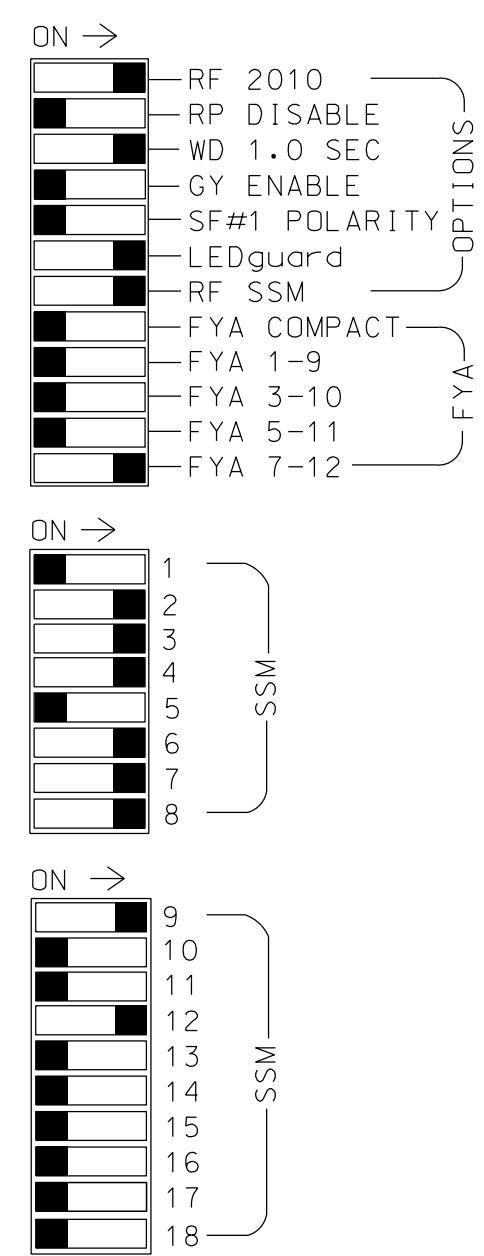
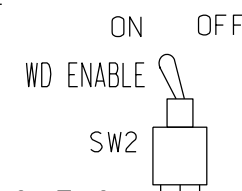
REMOVE DIODE JUMPERS 2-6, 2-9, 3-7, 3-8, 3-9, 3-12, 4-7, 4-8, 4-9, 4-12, 6-9, 7-9, 7-12, and 8-12.



REMOVE JUMPERS AS SHOWN

#### NOTES:

- 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

#### NOTES

- 1. To prevent 'flash-conflict' problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phase 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Start Up In Green.
6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as wag overlap.
7. The cabinet and controller are part of the Wilmington Signal System.

#### EQUIPMENT INFORMATION

CONTROLLER.....EXISTING 2070L
CABINET.....EXISTING 332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S4,S5,S8,S10,S11,AUX S1,AUX S5.
PHASES USED.....2,3,4,6,7,8.
OVERLAP 'A'.....6+7
OVERLAP 'B'.....NOT USED
OVERLAP 'C'.....NOT USED
OVERLAP 'D'.....7+8

Table with Project Reference No. 17.BP.3.R.28 and Sheet No. Sig. 12

#### SIGNAL HEAD HOOK-UP CHART

Table mapping Load Switch No. to Signal Head No. and various phase/arrow configurations.

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

NOTE: Load Switch S1 require output assignment remapping. See Sheet 2 of this electrical detail for instructions.

#### INPUT FILE POSITION LAYOUT

(front view)

Grid table showing input file positions for loops 1-14, including file U, L, and FS.

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card
Reuse existing detector cards for temporary and final signal designs.

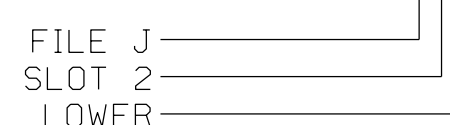
#### INPUT FILE CONNECTION & PROGRAMMING CHART

Table mapping Loop No., Loop Terminal, Input File Pos., Pin No., Input Assignment No., Detector No., Nema Phase, Call, Extend, Full Time Delay, Stretch Time, and Delay Time.

\* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

1 Add jumpers from J6-F to 18-W, on rear of input file..

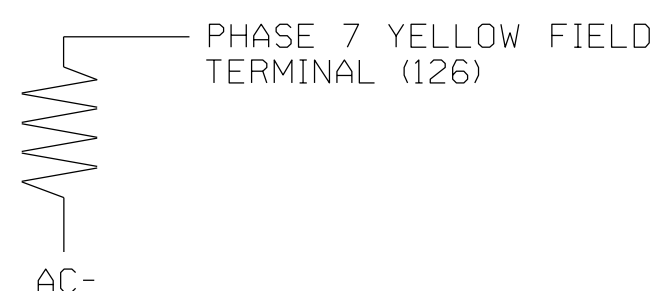
#### INPUT FILE POSITION LEGEND: J2L



#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

Table for acceptable resistor values: VALUE (ohms) and WATTAGE (25W, 10W).



#### 4 SECTION FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

- 1. The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0019
DESIGNED: May 2014
SEALED: July 21, 2016
REVISED:

#### Signal Upgrade - Final Design (Electrical Detail Sheet 1 of 2)

Project information block including electrical and programming details for US 421, prepared by AM Encarnacion and reviewed by LM Moon and MB Toth.