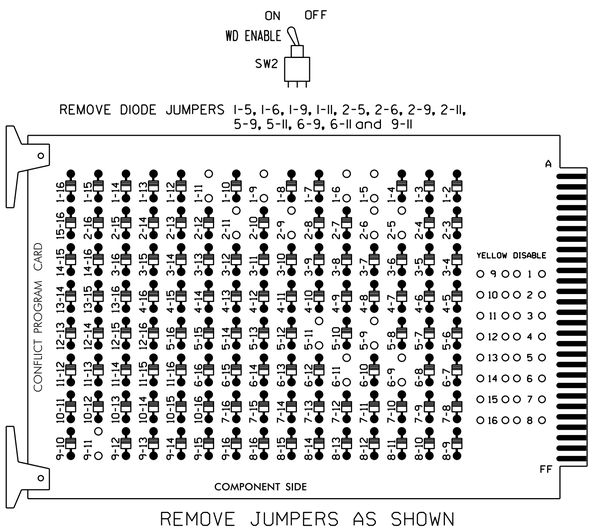
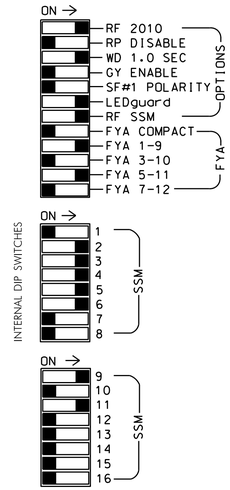
PROJECT REFERENCE NO.	SHEET NO.
B-4490	Sig. 7.1

EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN



■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on used monitor channels, tie unused red monitor inputs 1,7,8 10,12,13,14,15 & 16 to load switch AC+ per the cabinet manucturer's instructions.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the Fayetteville Signal System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3		S4		S4P	S5		S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3		4		4 PED	5		6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21,22	NU	31,32	33,34	41	42	NU	51★	34	61,62	NU	NU	NU	NU	11★	NU	NU	51★	NU	NU
RED		128			131	101	101			*	134										
YELLOW	*	129			132	102	102				135										
GREEN		130			133	103	103				136										
RED ARROW				131												A121			A114		
YELLOW ARROW				132						132						A122			A115		
FLASHING YELLOW ARROW																A123			A116		
GREEN ARROW	127			133		103			133	133											

NU = Not Used

* Denotes install load resistor. See Load Resistor Installation Detail this sheet.

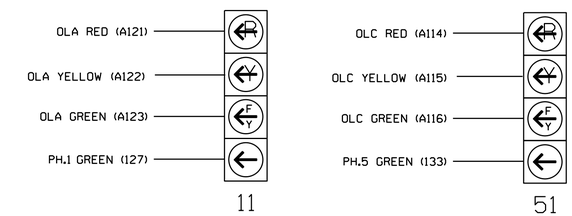
★ See pictorial of head wiring in detail below.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 (12-STD,6-AUX)
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S9,S12
PHASES USED.....1,2,3,4,5,6
OVERLAP'A'.....1+2
OVERLAP'B'.....NOT USED
OVERLAP'C'.....5+6
OVERLAP'D'.....NOT USED

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE:

The display sequence for signal heads 11 and 51 require special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø2 2A	Ø2 2C	WIRED INPUT ⊗	Ø3 3A	Ø4 4A	SLOT EMPTY	SLOT EMPTY	Ø3 3C	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L	NOT USED	Ø2 2B	NOT USED		NOT USED	Ø4 4B			Ø3 3B					ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6 6A,6B 6C	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø5 5B	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	NOT USED							NOT USED					

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
2C	TB2-9,10	I3U	63	25	32	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
3B	TB6-11,12	I9L	62	24	13	3	Y	Y			3
3C	TB6-9,10	I9U	60	22	11	3	Y	Y			10
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
5A[2]	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
5B	TB7-9,10	J9U	59	21	15	5	Y	Y			15
6A,6B,6C	TB3-5,6	J2U	40	2	6	6	Y	Y			

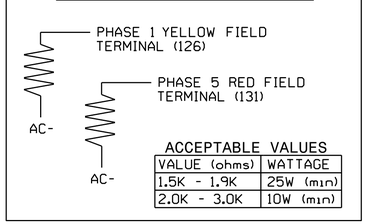
[1] Add jumper from I1-W to J4-W, on rear of input file.

[2] Add jumper from I4-W to J1-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0037T2
DESIGNED: July 2015
SEALED: 8/25/15
REVISED: N/A

LOAD RESISTOR INSTALLATION DETAIL



ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

Signal Upgrade - Sheet 1 of 2 - Temp 2 Phase 1 Steps 6-8

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 24 (Bragg Boulevard)
at
West Rowan Street

Division 6 Cumberland County Fayetteville

PLAN DATE: July 2015 REVIEWED BY:
PREPARED BY: B. Simmons REVIEWED BY:

REVISIONS	INIT.	DATE

Prepared in the Offices of:
Traffic Engineering and Safety Systems Branch
Signals Management Section
750 N. Greenfield Pkwy, Garner, NC 27529

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022013 ENGINEER GEORGE C. BROWN

DocuSigned by: George C. Brown 8/27/2015
F12601E0DBEB434... DATE

SIG. INVENTORY NO. 06-0037T2

25-AUG-2015 11:49
S:\TISSAL\ITS Signals\Workgroups\Sig Mon\Simmons\Working Folder\Electrical Details\division 06\060037_sm_ele_xxx.dgn
bsimmons