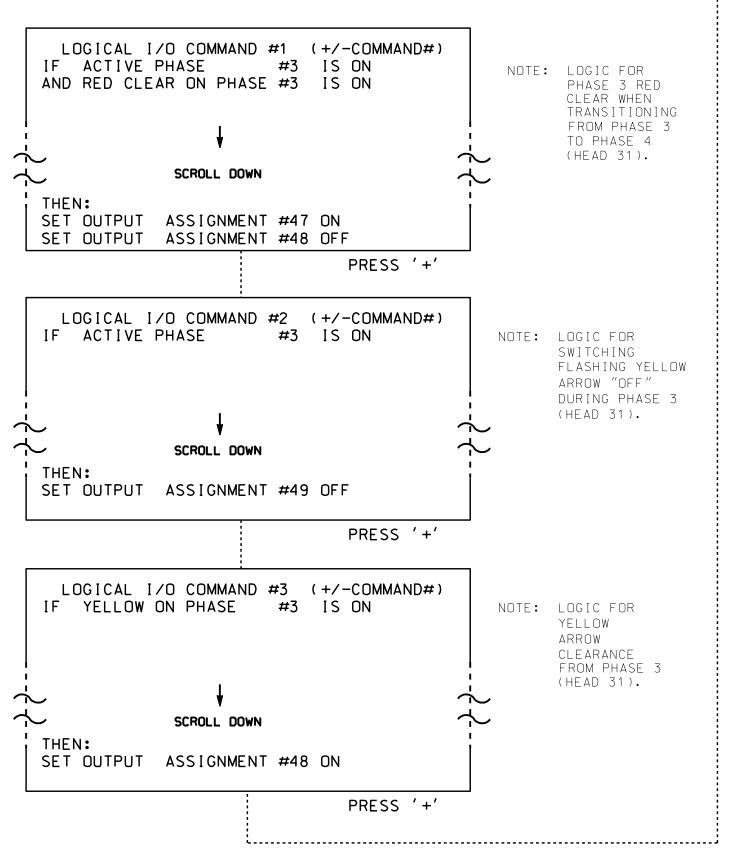
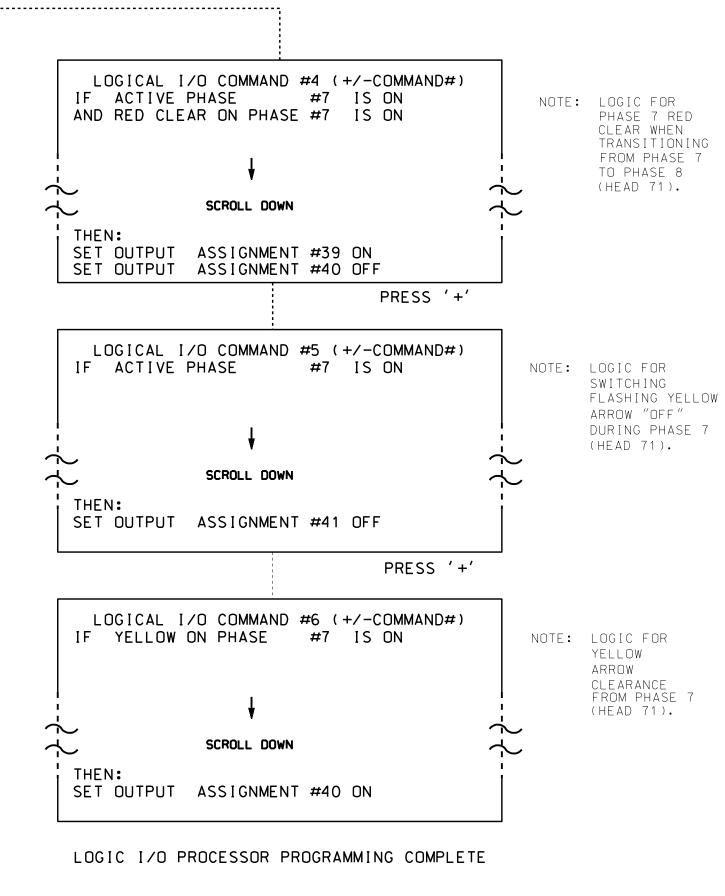
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).





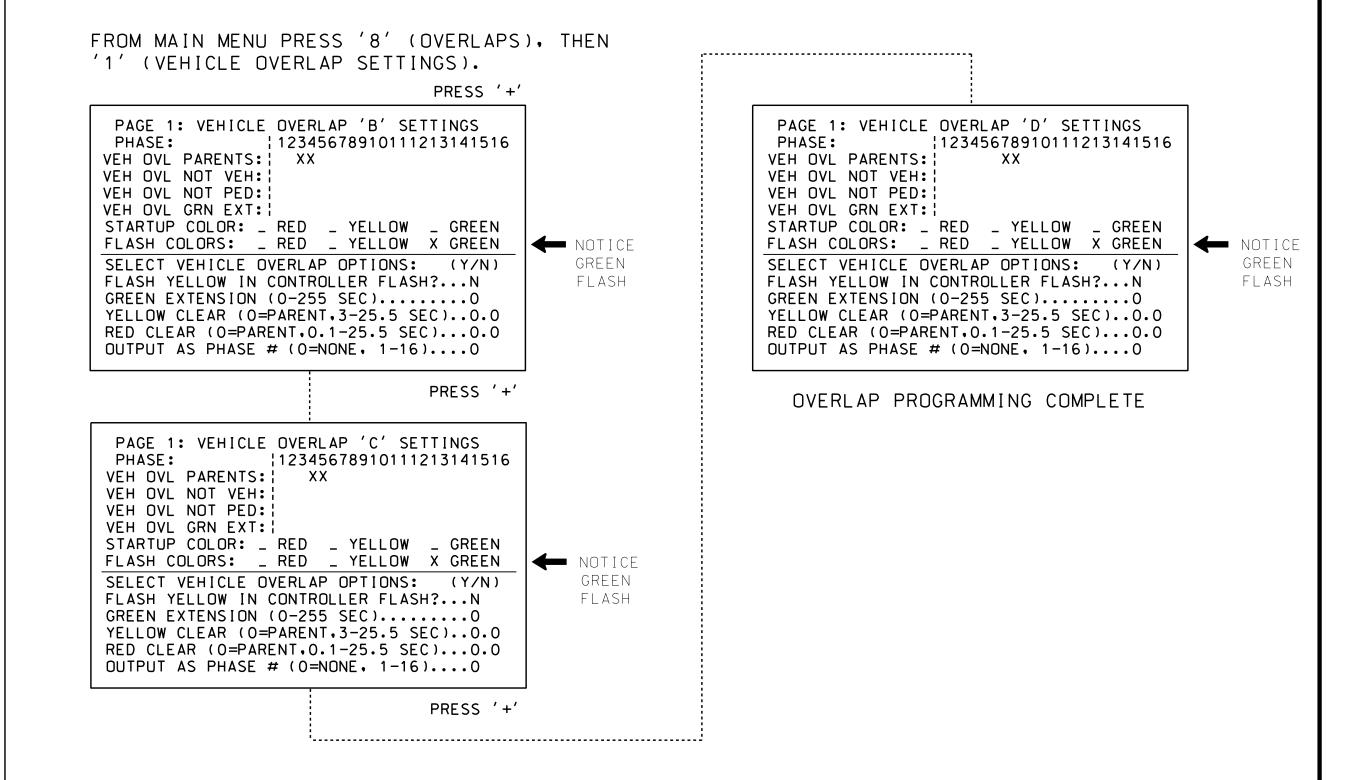
OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)



PROJECT REFERENCE NO.

2017CPT.03.01.10651| Sig. 7.2

## COUNTDOWN PEDESTRIAN SIGNAL OPERATION

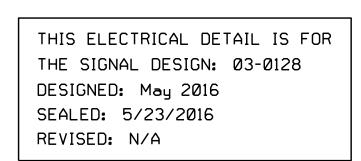
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

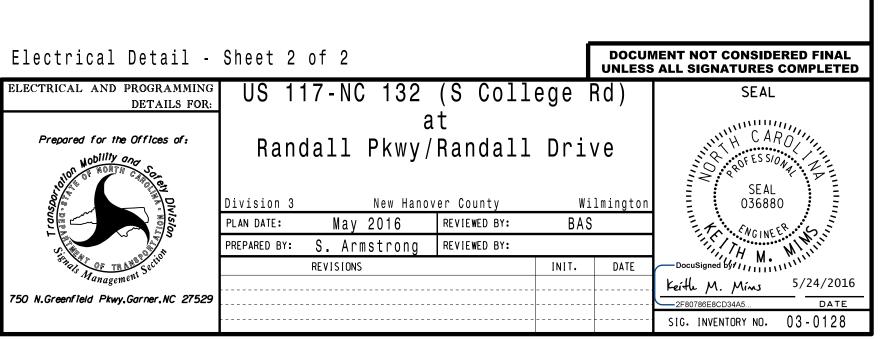
## FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the same approach, make the following flasher circuit changes:

- 1. On rear of PDA Remove wire from Term. T2-4 and terminate on T2-2.
- 2. On rear of PDA Remove wire from Term. T2-5 and terminate on T2-3.
- 3. Remove Flasher Unit 2.

The changes listed above ties all phases and overlaps to Flasher Unit 1.





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