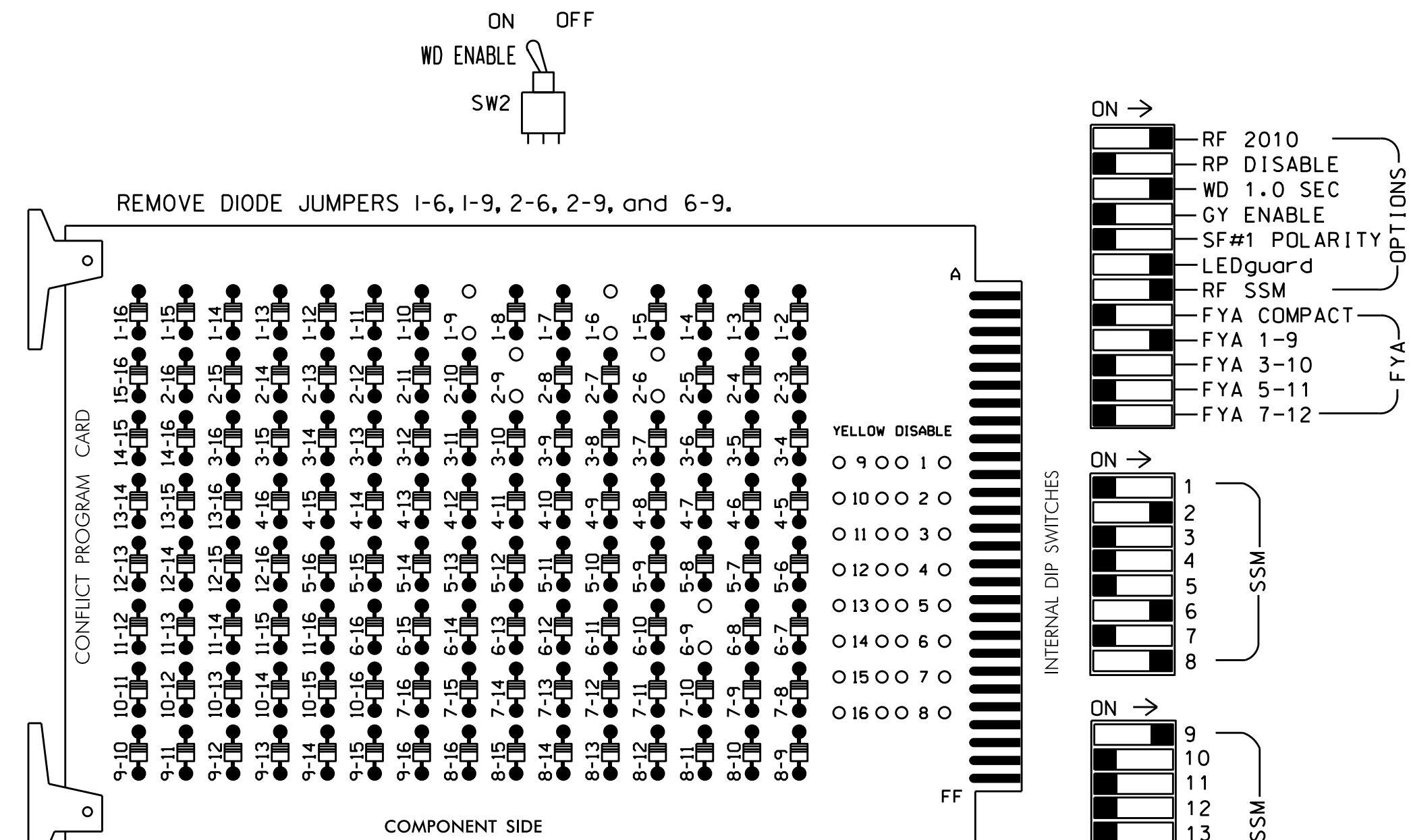


### EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- To prevent red failures on unused monitor channels, see Red Monitor Board Programming Detail this sheet.
- Program phases 2 and 6, on the controller unit, for Start Up In Green.
- Enable Simultaneous Gap-Out, on the controller unit, for all phases.
- Program phases 2 and 6, on the controller unit, for Variable Initial and Gap Reduction.
- The cabinet and controller are part of the NC 133 (Castle Hayne Rd.) CLS.

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11*	21,22	NU	NU	NU	NU	NU	61,62	NU	NU	22	81,82	NU	11*	NU	NU	NU	NU
RED		128						134			107							
YELLOW	*	129						135			108							
GREEN		130						136			109							
RED ARROW																		A121
YELLOW ARROW											108							A122
FLASHING YELLOW ARROW																		A123
GREEN ARROW	127										109							

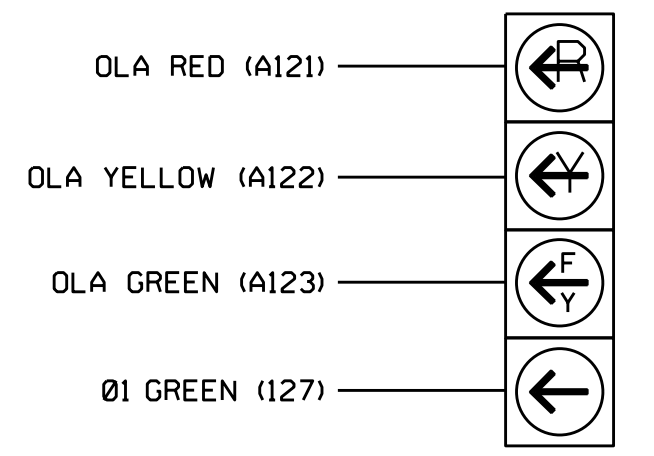
NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail below.

### EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....McCAIN/CONTROL TECHNOLOGIES  
 DWG.NO.9500-332-NC DOT /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S6,S8,S9  
 PHASES USED.....1,2,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....NOT USED

### 4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal head as shown)

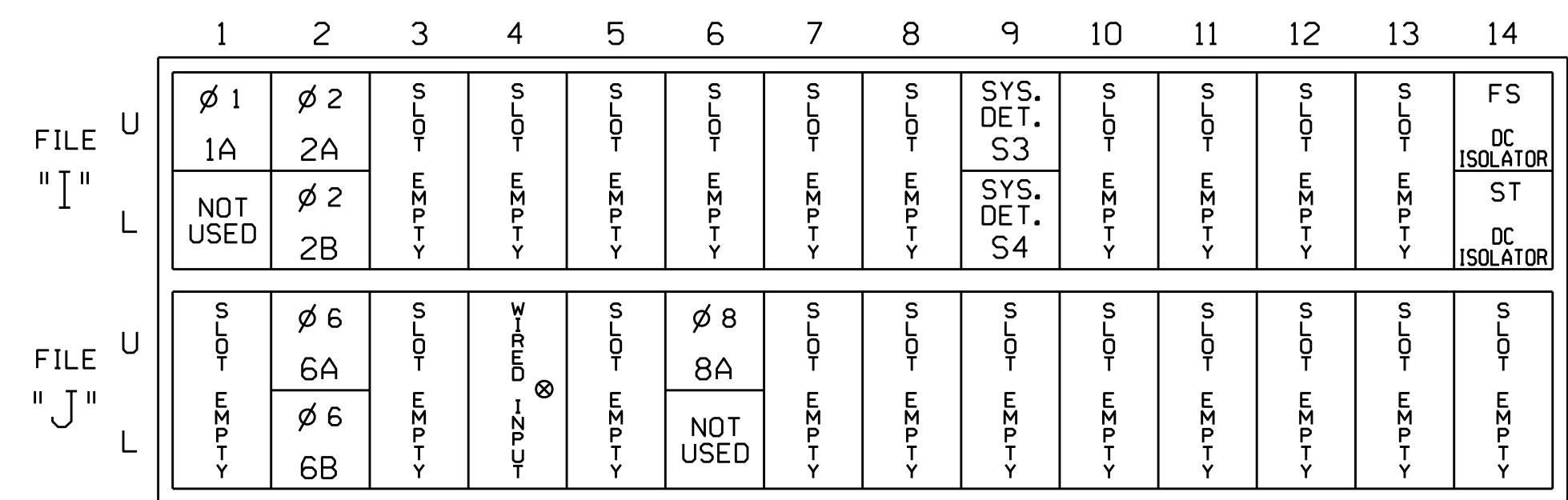


**NOTE**

- The sequence display for signal head 11 requires special logic programming. See sheet 2 of 4 for programming instructions.

### INPUT FILE POSITION LAYOUT

(front view)



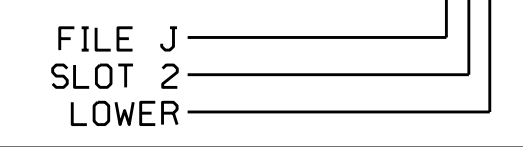
EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME  
 ⊗ Wired Input - Do not populate slot with detector card

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			20
	-	J4U	48	10	26*	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10
* S3	TB6-9,10	I9U	60	22	11	SYS					
* S4	TB6-11,12	I9L	62	24	13	SYS					

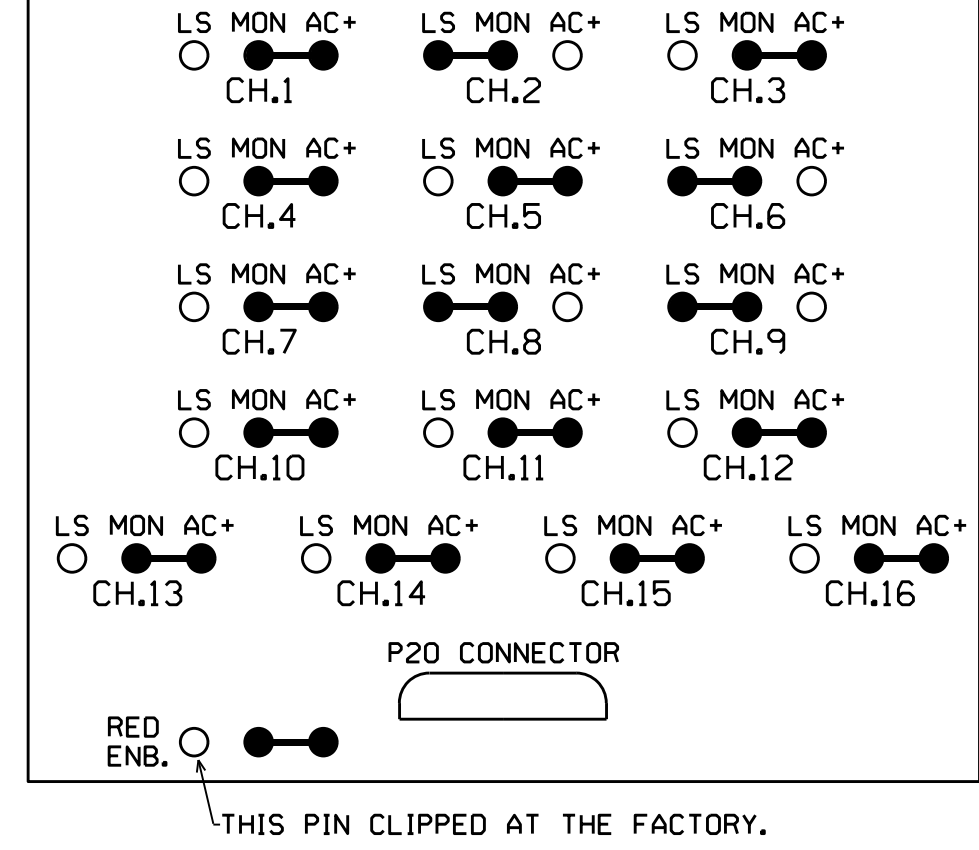
- Add jumper from I1-W to J4-W, on rear of input file.
- System detector only. Remove the vehicle phase assigned to this detector in the default programming.
- See input Page Assignment programming detail on sheet 3.

INPUT FILE POSITION LEGEND: J2L



### RED MONITOR BOARD PROGRAMMING

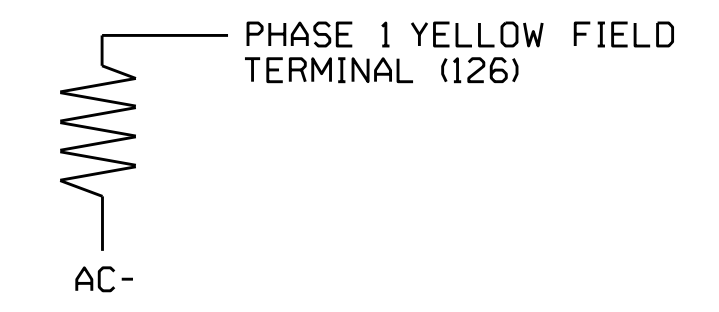
(position jumpers as shown below)



### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0929  
 DESIGNED: January 2016  
 SEALED: 2-16-16  
 REVISED: N/A

Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

REVISION SEAL  
 KEITH M. MIMS  
 ENGINEER  
 3-03-16  
 DATE

Prepared in the Offices of:  
 TRANSPORTATION MOBILITY AND SAFETY  
 750 N. Greenfield Pkwy, Garner, NC 27529

NC 133 (Castle Hayne Road) at I-140-US 17 EB Ramps  
 Division 03 New Hanover County Wilmington  
 PLAN DATE: September 2009 REVIEWED BY: T. Joyce  
 PREPARED BY: S. Armstrong REVIEWED BY:  
 REVISIONS  
 Added alternate phasing and updated overlap programming. I.P.T. KMM 3-03-16

Not a certified document as to the Original Document but only as to the Revisions - This document originally issued and sealed by George C. Brown, PE #022013, on 09/29/09. This document is only certified as to the revisions.  
 SIGNATURE DATE  
 SIG. INVENTORY NO. 03-0929

I:\MS-2016\_07-12\_S:\IT\ASIS\15\_S:\Signal\work\nc133\smc\_ele\_20160303.dgn T:paterson