

**SE-PAC2070 CONTROLLER  
OVERLAP PROGRAMMING**

*(program controller as shown below)*

FROM MAIN MENU PRESS 4 (UNIT DATA)

SE-PAC UNIT DATA	PRESS # DESIRED
1-STARTUP & MISC	6-ALT SEQUENCES
2-REMOTE FLASH	7-PORT 1 DATA
3-OVERLAP STANDARD	8-I/O MISC
4-OVERLAP SPECIAL	9-SIG DRV OUT
5-RING STRUCTURE	
	F-PRIOR MENU

PRESS 'B' FIVE TIMES

SE-PAC OVERLAP - F	(0-NO/1-YES)
OVL PHASES: 010111000 0000000	
PHS/CHN: 123456789 0123456789 01234	
OVL CHN(S): 000000100 0000000000 00000	
A-UP B-DN D-DspChn	E-EDIT F-PRIOR MENU

PRESS 'B' ONE TIME

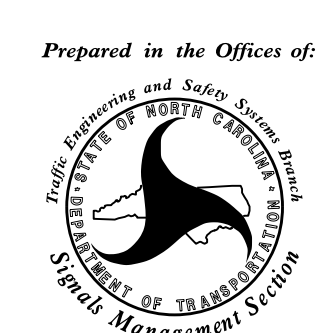
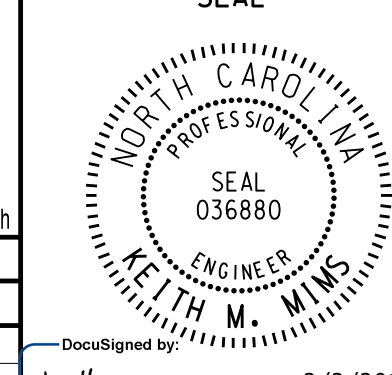
SE-PAC OVERLAP - G	(0-NO/1-YES)
OVL PHASES: 010000000 0000000	
PHS/CHN: 123456789 0123456789 01234	
OVL CHN(S): 000000000 0010000000 00000	
A-UP B-DN D-DspChn	E-EDIT F-PRIOR MENU

PRESS 'F' TO RETURN TO UNIT DATA

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 05-1642T4  
DESIGNED: December 2015  
SEALED: 2/1/2016  
REVISED: N/A

**DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED**

Electrical Detail - Temp Design 4 (TMP Area II Phase IV, Step 2) - Sheet 2 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:  Prepared in the Offices of:  750 Greenfield Parkway, Garner, NC 27529	<b>W. Peace Street at US 70 WB-401/NC 50 NB (Capital Blvd.) Ramps</b>		SEAL 
	Division 5 PLAN DATE: January 2016 PREPARED BY: S. Armstrong	Wake County REVIEWED BY: T. Joyce REVIEWED BY:	Raleigh DATE: 2/2/2016 DATE:

05-1642-2016\_10.dgn  
 C:\WITS\SIG\T\S\Sig\mol\work\hgr\051642\_sm.ele.xxx.dgn  
 sarmstrong